2024 MathWorks 中国汽车年会

基于模型的系统工程应用于需求开发和管理

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MathWorks

基于模型的系统工程 – MBSE

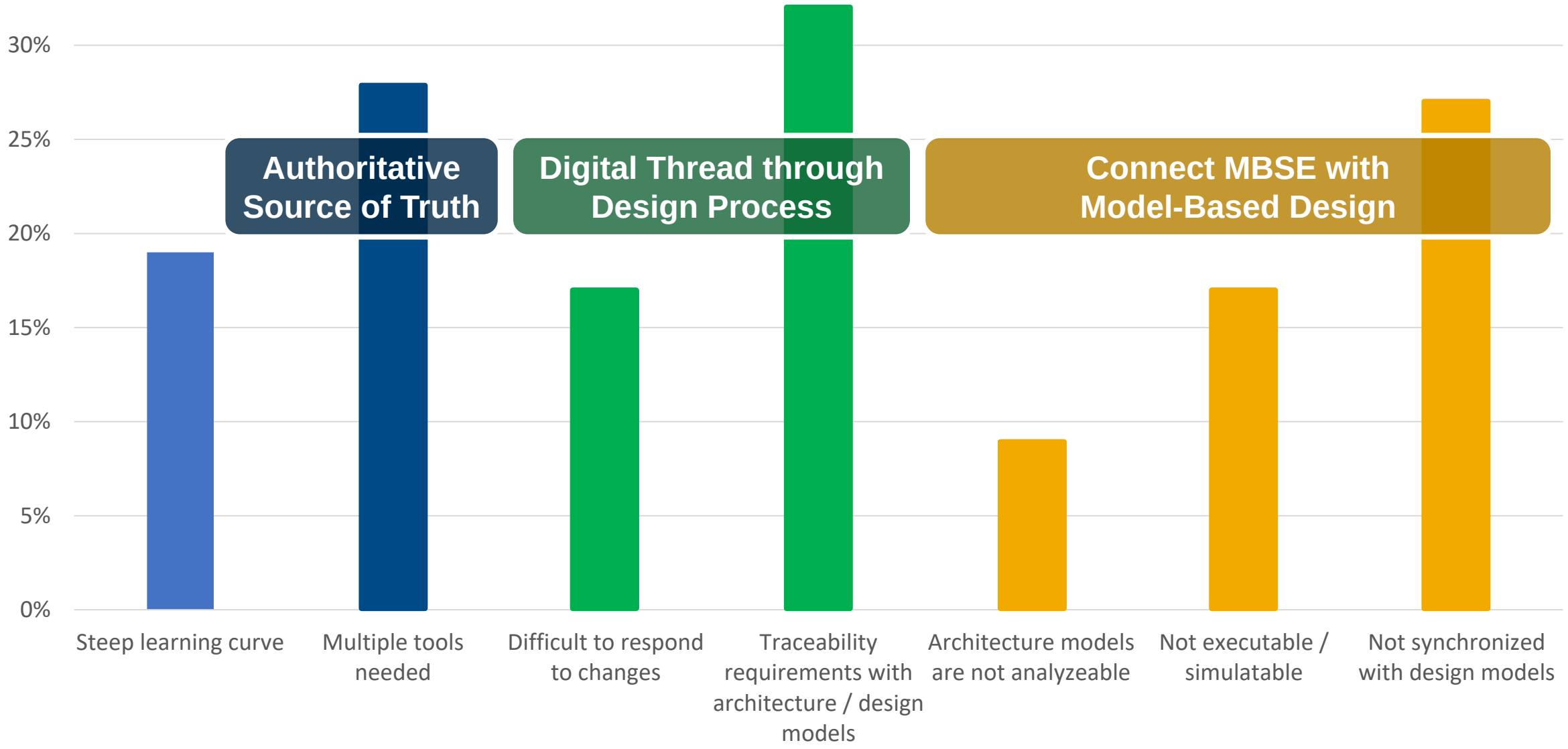
> “ Systems Engineering is a **transdisciplinary and integrative approach** to enable the successful realization, use, and retirement of engineered systems, using systems principles and concepts, and scientific, technological, and management methods.

> “ Model-based systems engineering (MBSE) is the **formalized application of modeling** to support system requirements, design, analysis, verification and validation activities beginning in the conceptual design phase and continuing throughout development and later life cycle phases.

INCOSE: What is Systems Engineering?
INCOSE: Systems Engineering Glossary

INCOSE Model Based Systems Engineering (MBSE) Initiative
Sanford Friedenthal, Regina Griego, Mark Sampson

MBSE的应用挑战



Source: MathWorks webinar, Model-Based Systems Engineering - Practical Use and Applications, December 9th, 2021 (173 responses)

MBSE开发和管理需求

Maintain **requirements as an authoritative source of truth** throughout the product development process, by using (simulation) models to:

Manage Requirements

- Transform stakeholder requirements/needs into design requirements using models, simulation and code generation
- Establish traceability between requirements, models and testcases

Manage Complexity

- Explore the design space through (reusable) trade-off studies
- Through views and traceable architecture models

Manage Interfaces

- Connect system architecture with software architecture, component implementation, FMEA (fault injection models)

Authoritative Source of Truth

Digital Thread through Design Process

Connect MBSE with Model-Based Design

Digital Thread through Design Process

Connect MBSE with Model-Based Design

Digital Thread through Design Process

Connect MBSE with Model-Based Design

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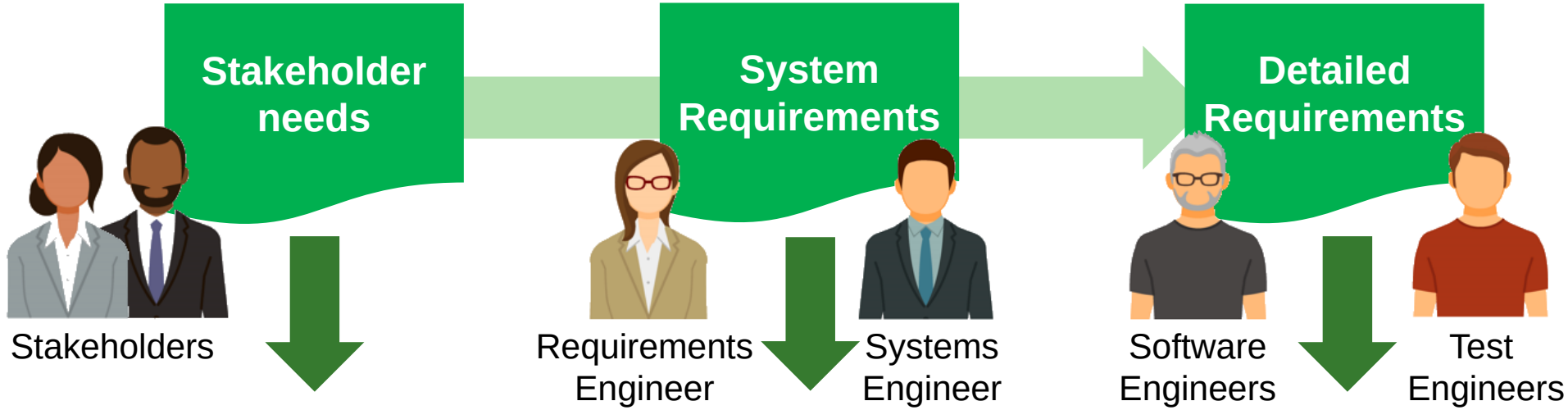
Digital Thread through Design Process
Connect MBSE with Model-Based Design

需求开发流程 – 传统方式

Requirements flow



Produced artifacts

Requirements set (textual)

Architecture models (descriptive)
Requirements set (textual)
Traceability

Requirements set (textual)
Design models
Code (C/C++/HDL)
Test harnesses
Traceability

Tools used

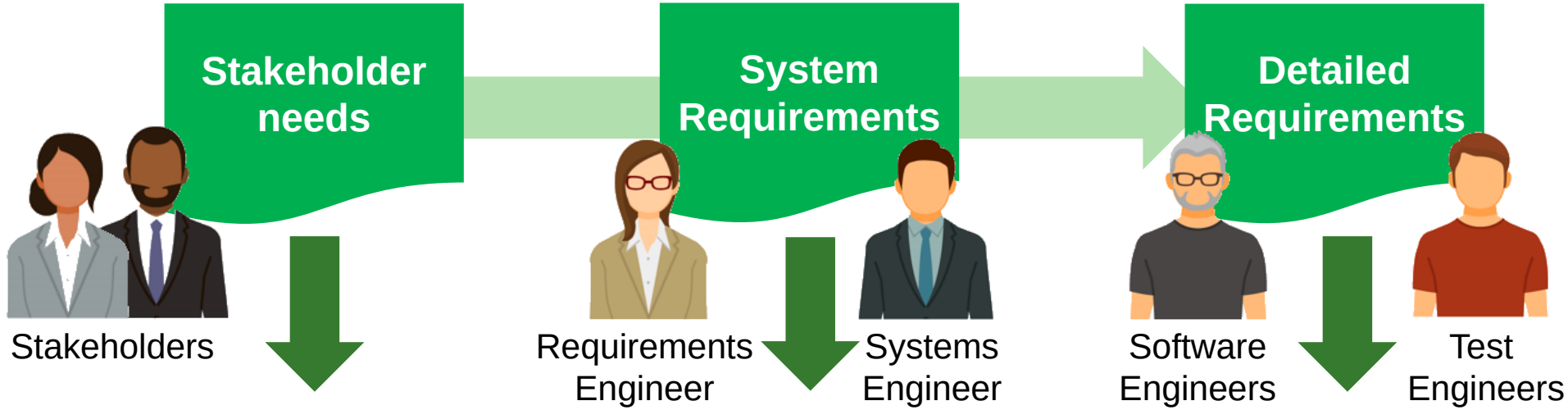
Design – MBD (Simulink)

Architecture – MBSE

Requirements tools (Requirements Toolbox, ….)

需求开发流程 – 同平台方式

Requirements flow



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Requirements models
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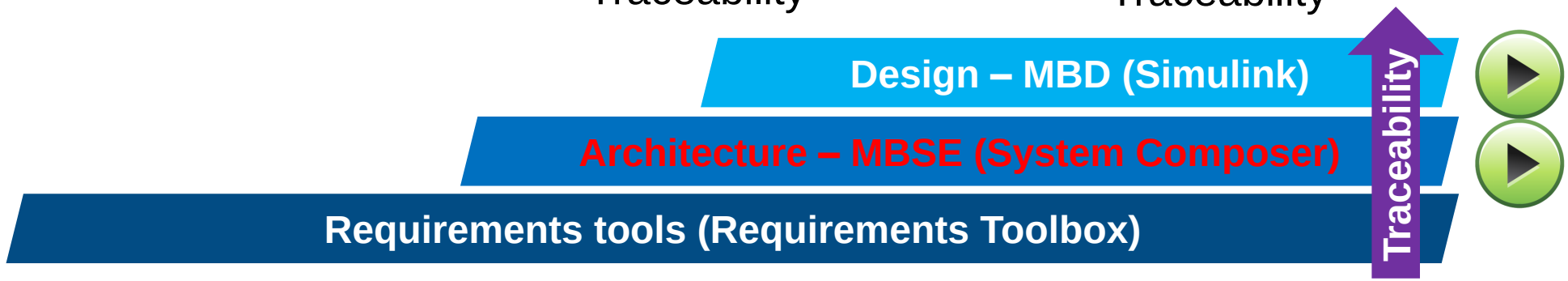
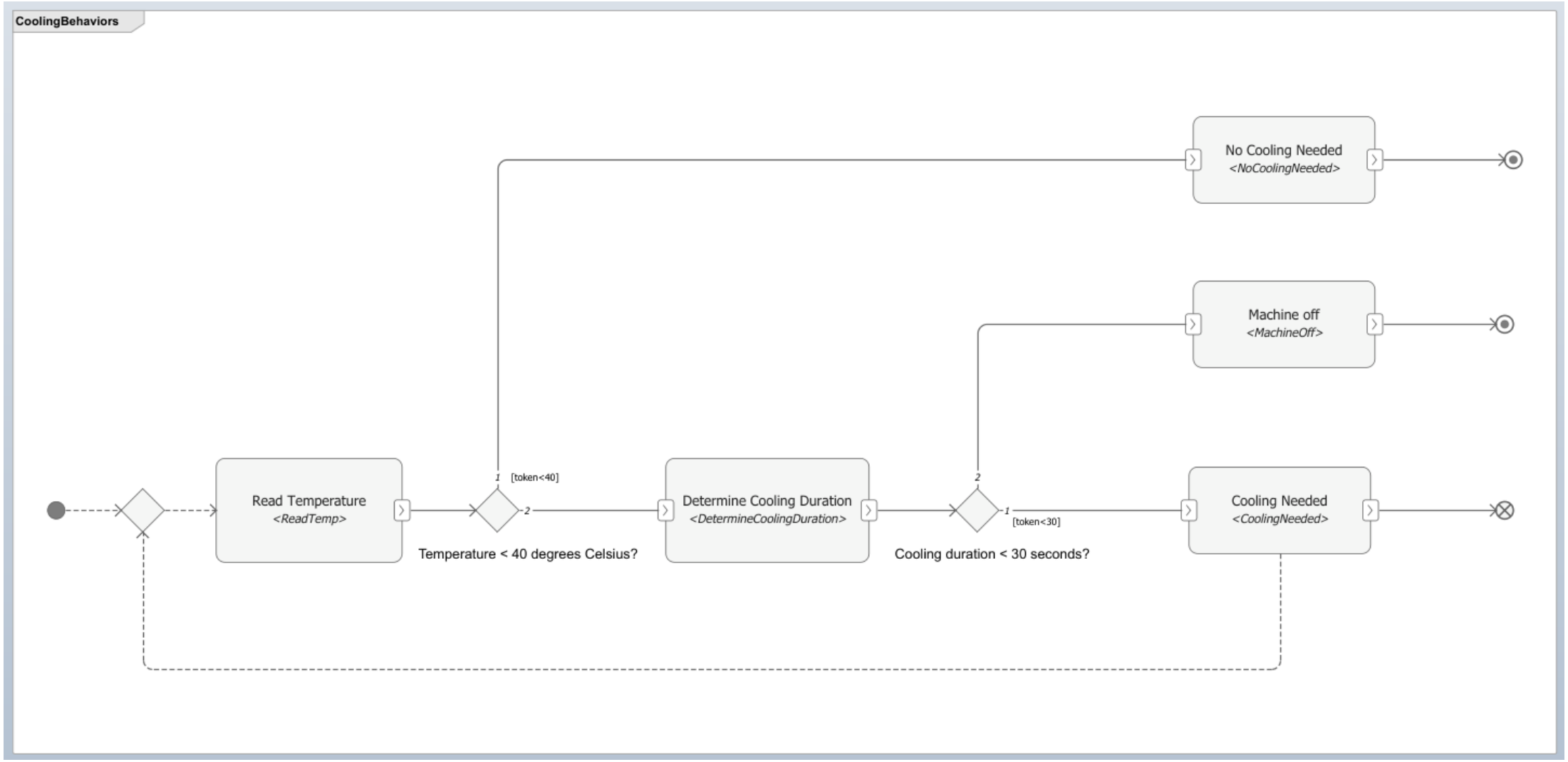


案例 – 冷却系统需求开发

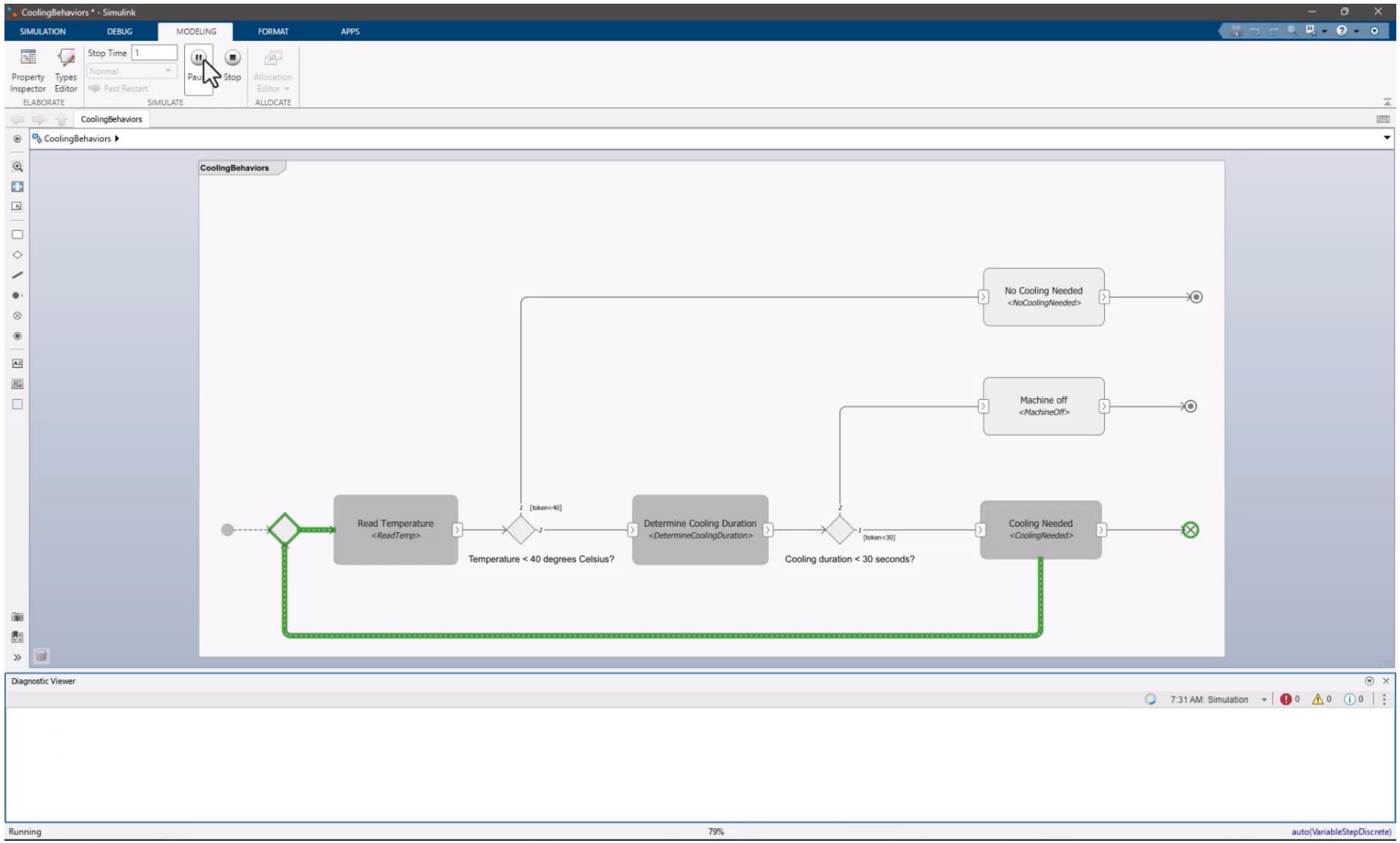
Provide a system which maintains the operating temperature of a machine, avoiding damage to the machine because of overheating.

- **[constraint]** Cooling system needs to maintain operating temperature.
- **[constraint]** Cooling needs to be effective within a predetermined time.
- **[assumption]** Environmental temperature greater than -10 degrees and smaller than 80 degrees.

理解和确认用例场景 – 活动图

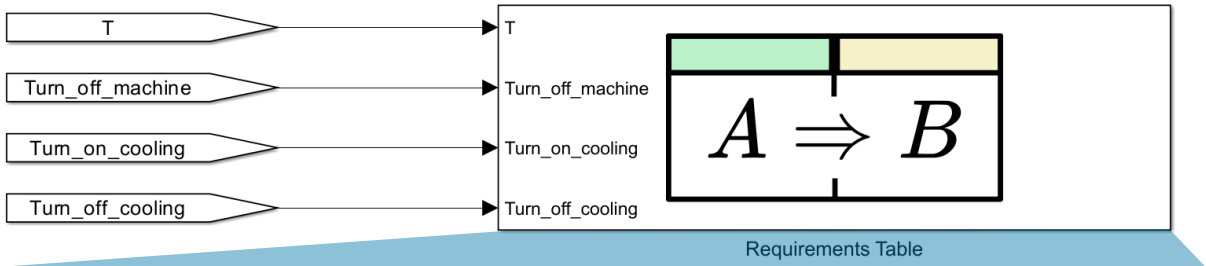


理解和确认用例场景 – 活动图仿真



理解和确认用例场景 – 需求表

Formal description of requirements



Index	Summary	Precondition		Duration	Postcondition		
		T	prev(Turn_off_machine)		Turn_off_cooling	Turn_on_cooling	Turn_off_machine
1	Cooling off when T<40	<40	false		true	false	false
2	Temperature is T >= 40	>40	false				
2.1	Machine off cooling duration >=30 sec	>40		30	false	false	true
2.2	Cooling on cooling done within 30 sec	Else			false	true	false
3	When machine is off, it should stay off		true		false	false	true

Input condition to activate a requirement

Expected outcome of a requirement

理解和确认用例场景 – 需求表分析

Index	Summary	Precondition		Duration	Postcondition		
		T	prev(Turn_off_machine)		Turn_off_cooling	Turn_on_cooling	Turn_off_machine
1	Cooling off when T<40	<40	false		true	false	false
2	Temperature is T >= 40	>40	false				
2.1	Machine off cooling duration >=30 sec	>40		30	false		
2.2 D	Cooling on cooling done within 30 sec	Else			false		
3	When machine is off, it should stay off		true		false		

Formal description

Incompleteness Issues

Incompleteness 1: 'Turn_off_machine' is not specified at time 0 for the following inputs:

Time	0
Step	1
T	40

Incompleteness 2: 'Turn_on_cooling' is not specified at time 0 for the following inputs:

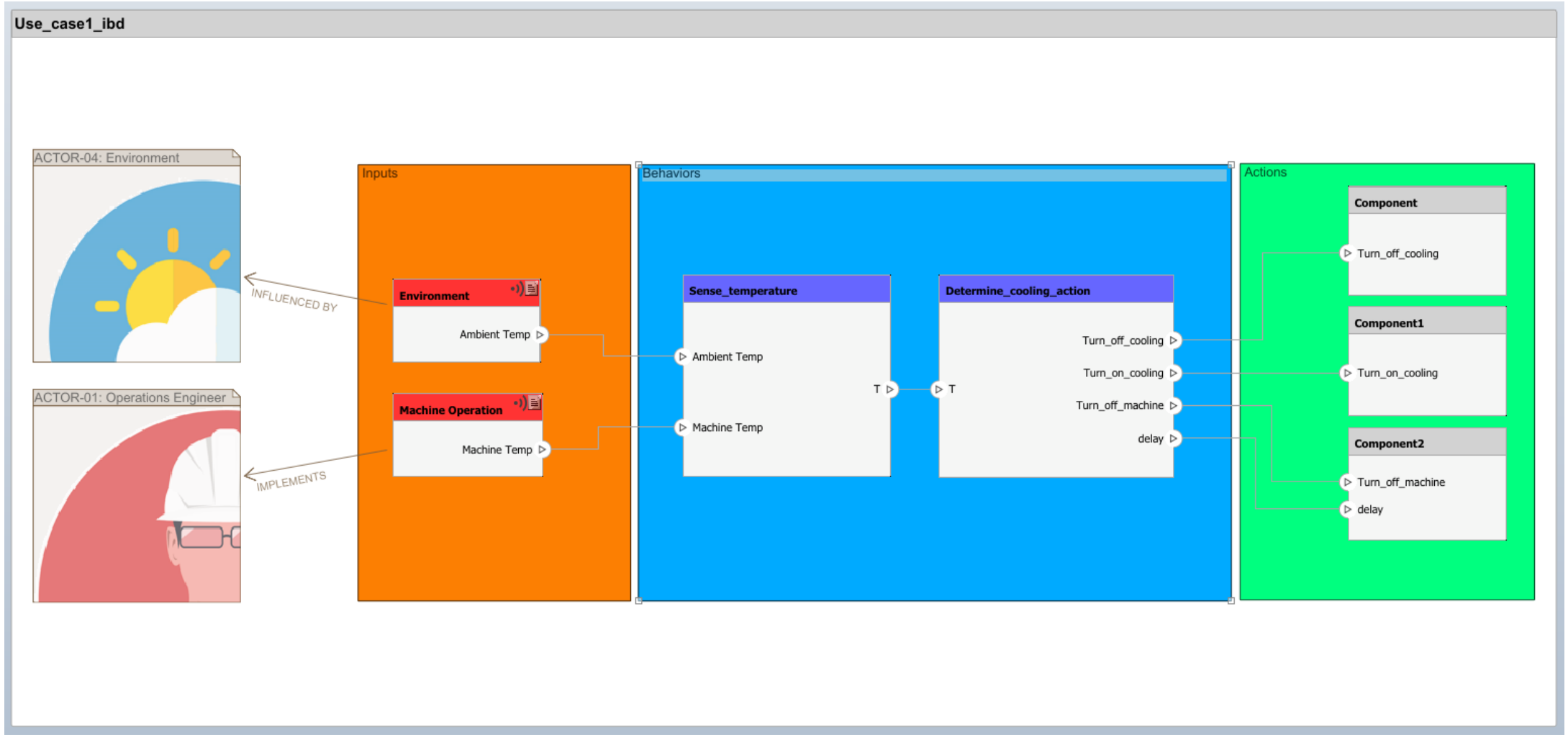
Time	0
Step	1
T	40

Incompleteness 3: 'Turn_off_cooling' is not specified at time 0 for the following inputs:

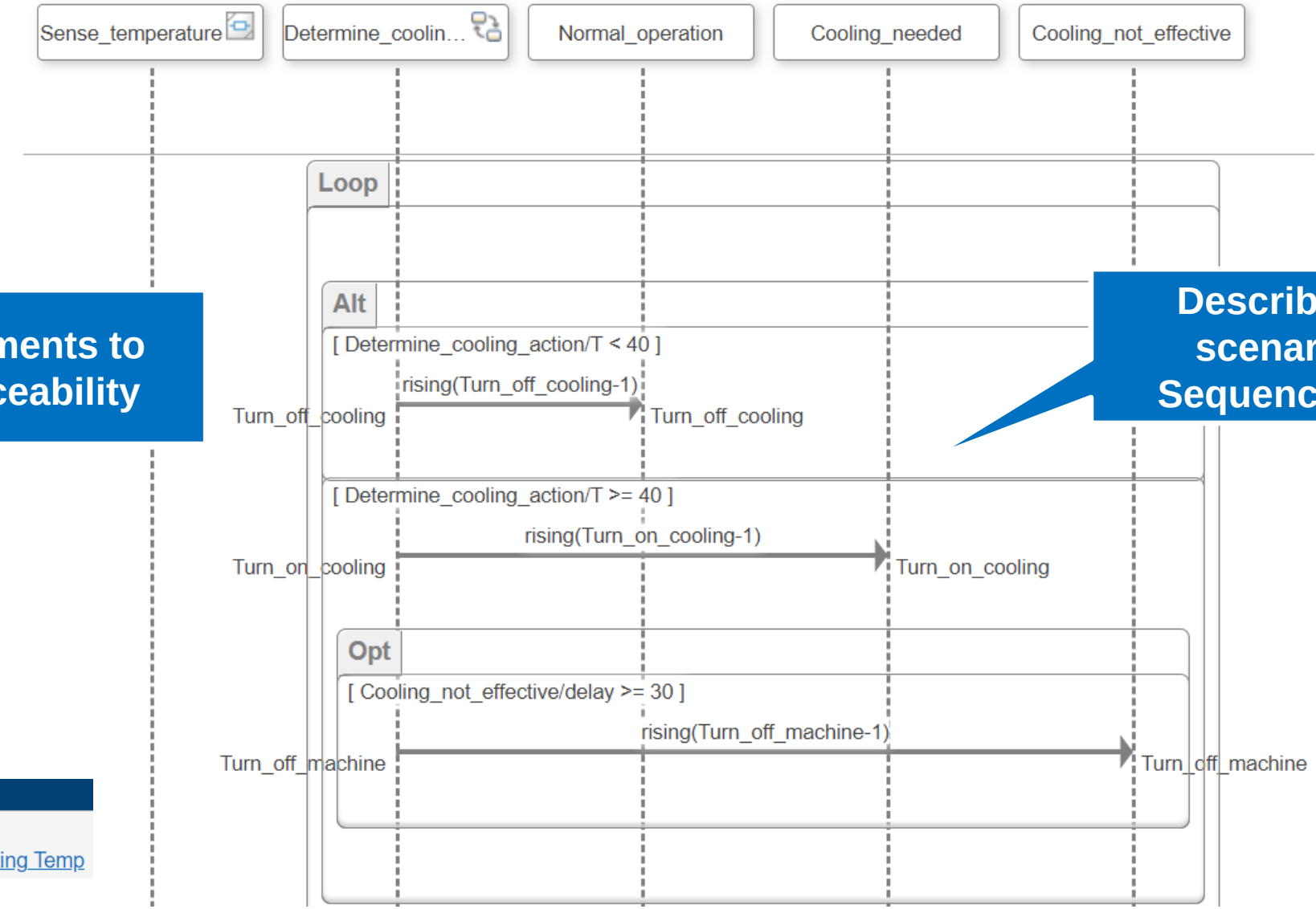
Time	0
Step	1
T	40

Formal analysis of requirements: completeness and consistency ➔ T = 40 is not specified!

描述系统结构和接口 – 结构图



描述系统组件交互 – 顺序图



Link requirements to facilitate traceability

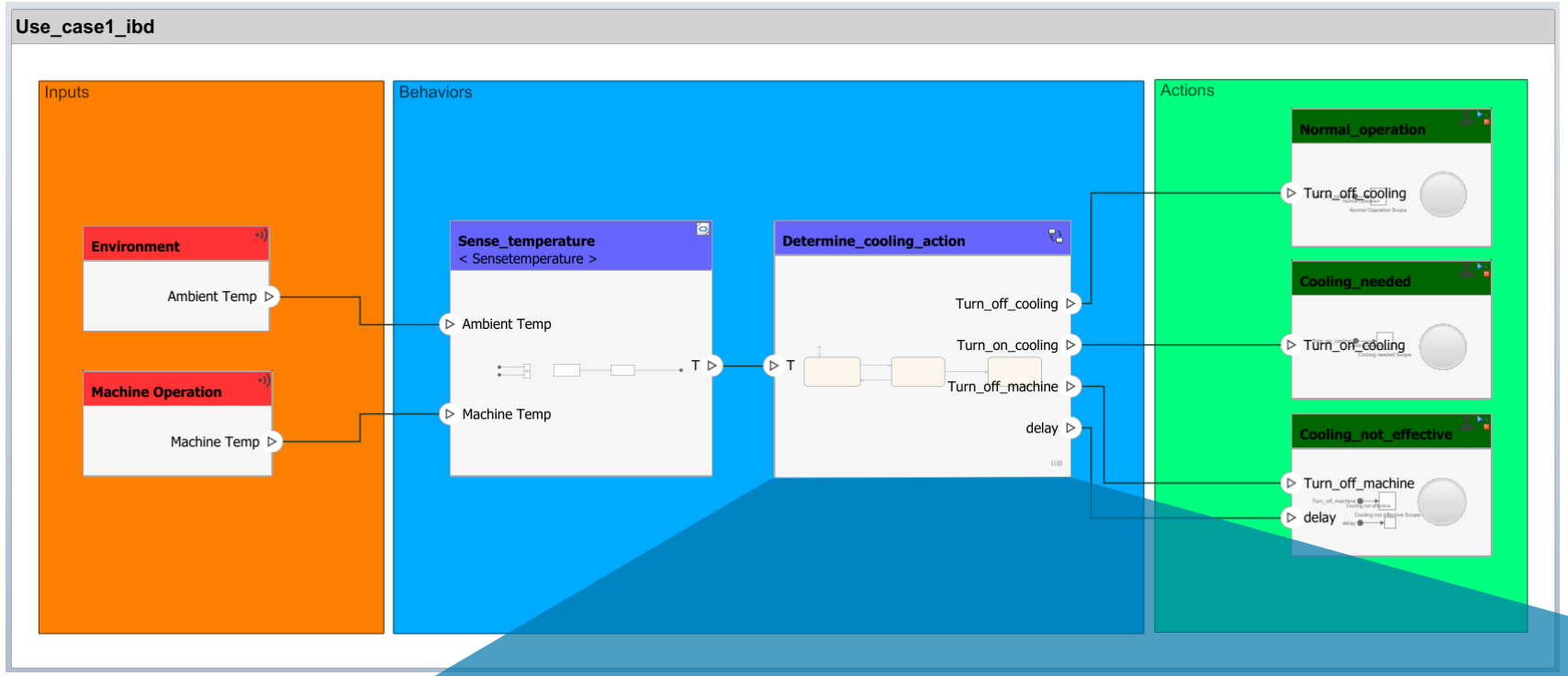
Describe complex scenarios using Sequence Diagrams

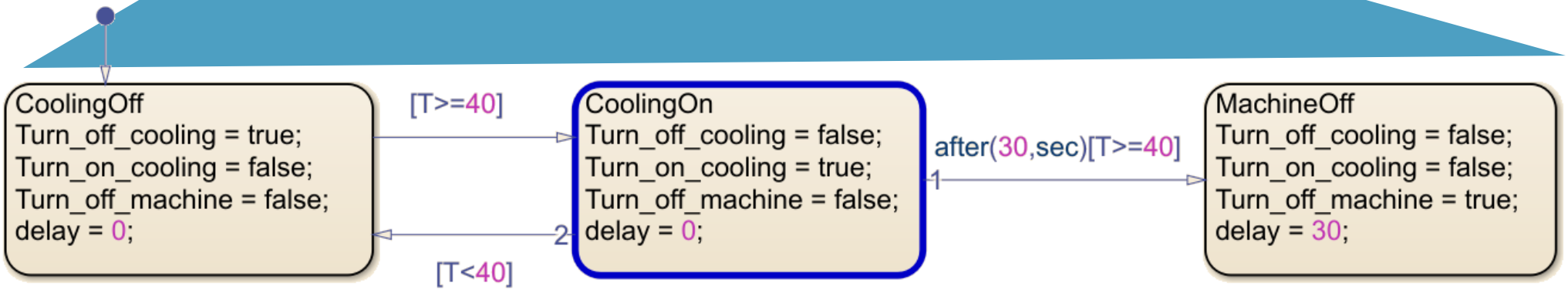
Requirement Links

⇒ **Describes:**

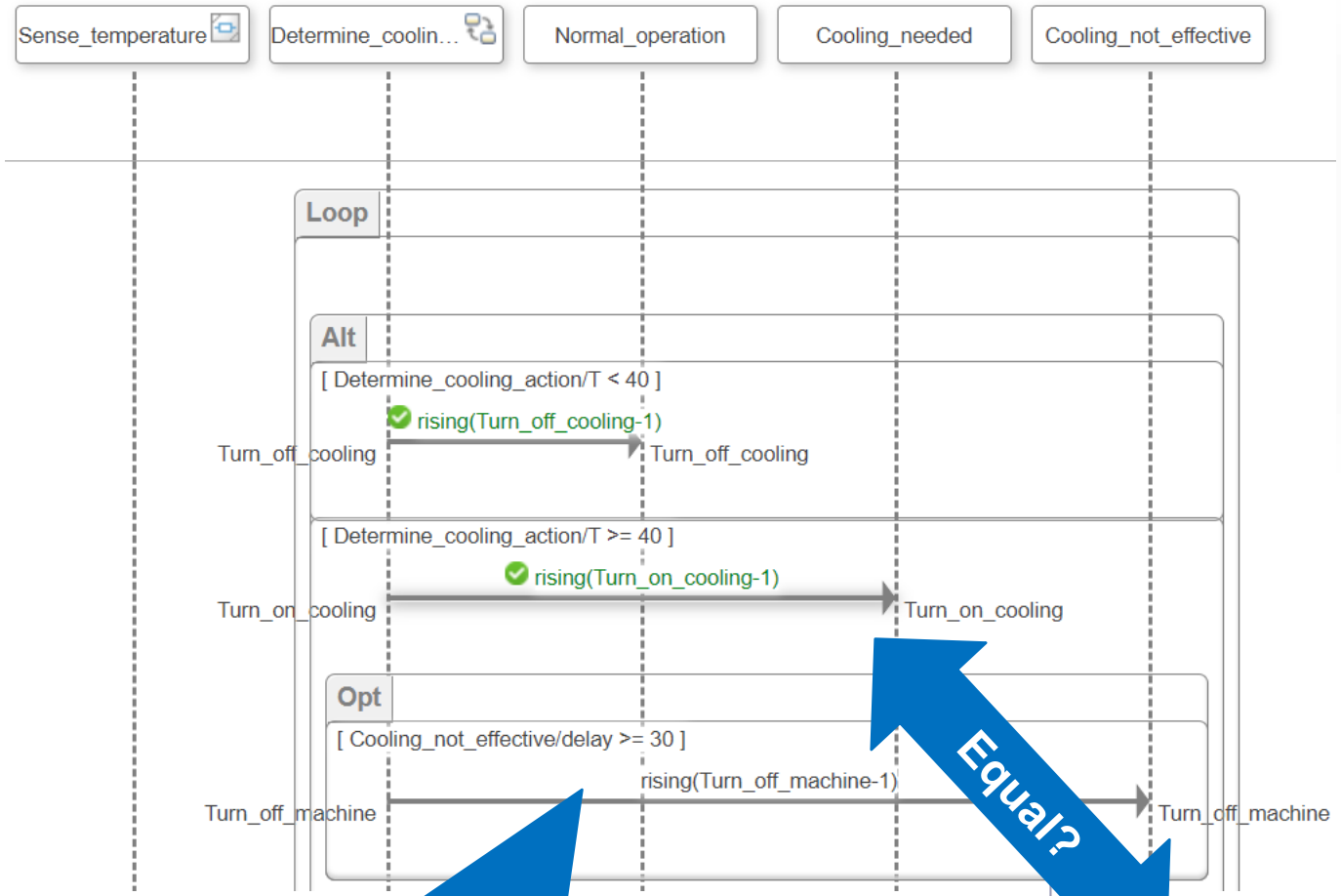
STAKEHOLDER-03 Operating Temp

开发系统详细设计模型 – 状态图

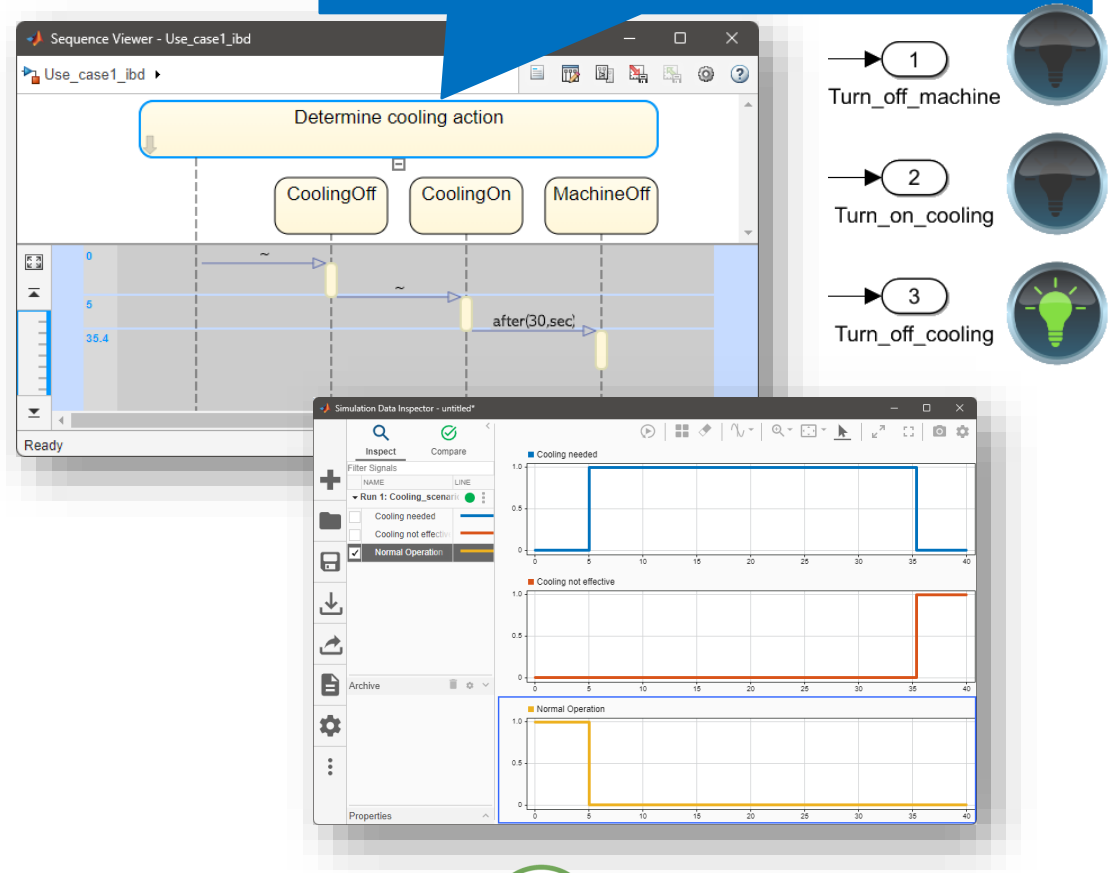




验证详细设计模型行为 – 需求仿真

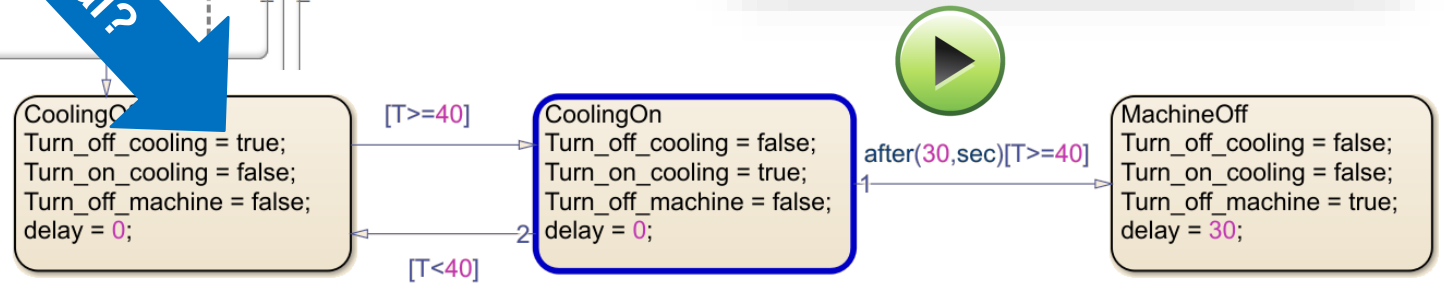


Visualize simulation results

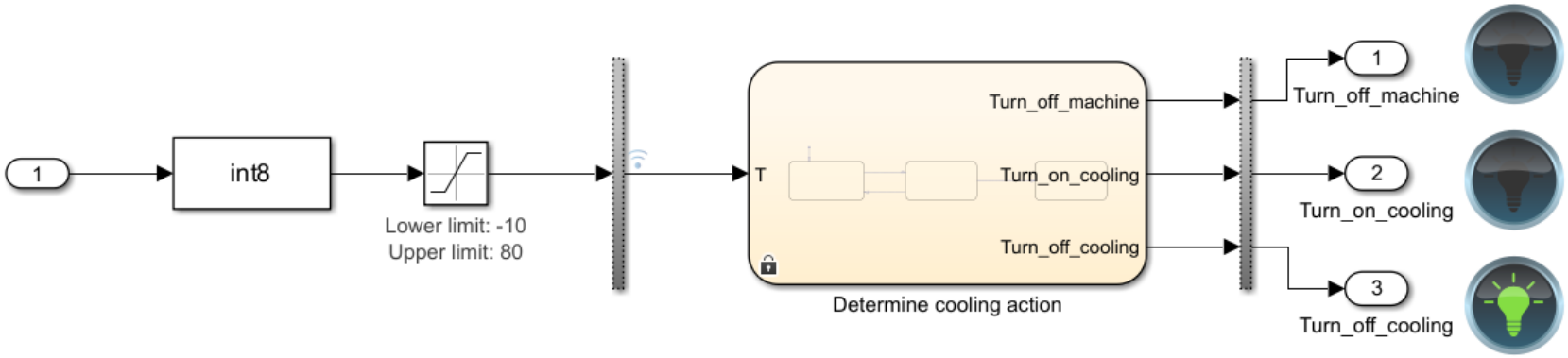


Equal?

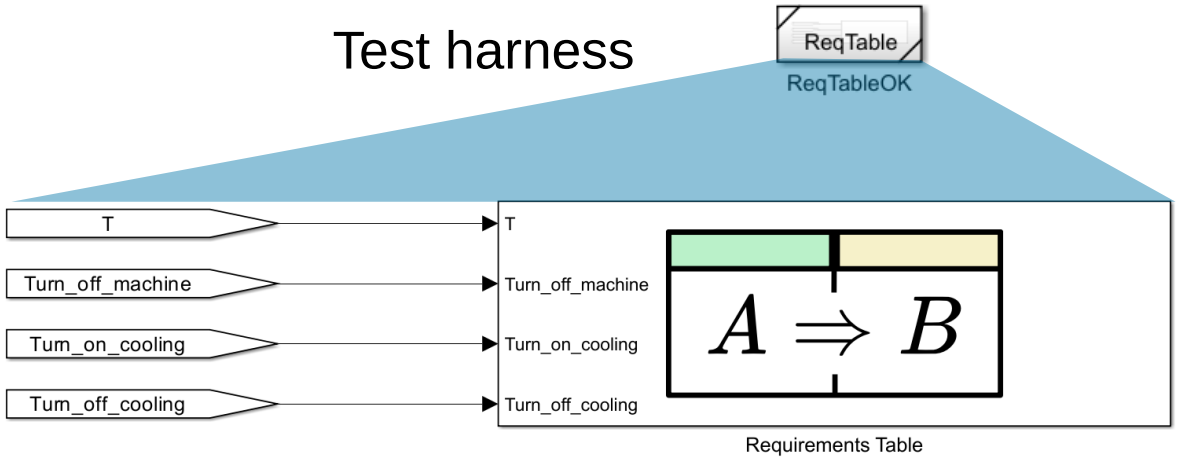
Verify expected behavior through simulation



验证详细设计模型行为 – 需求确认



Test harness

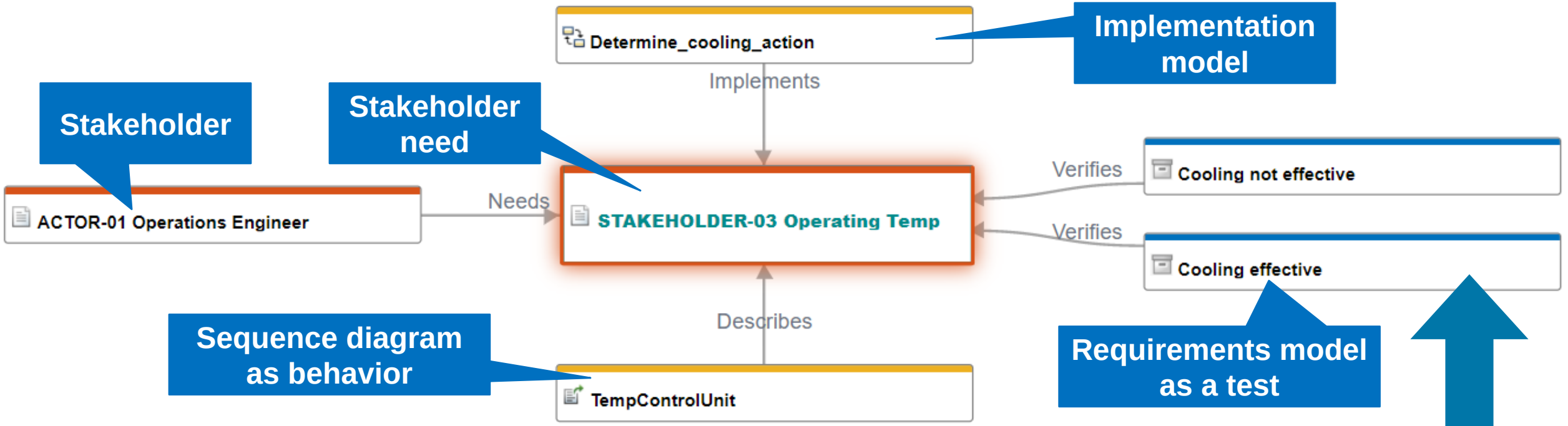


Test definition

NAME	STATUS
Results: 2023-Apr-04 10:49:58	2
validateCooling	2
Test cooling behavior	2
Cooling not effective	✓
Cooling effective	✓
Verify Statements	✓
R:1 (Requirements Table)	✓
R:2.1 (Requirements Table)	⊘
R:2.2 (Requirements Table)	✓
R:3 (Requirements Table)	⊘
Sim Output (Simulati : normal)	

Validate compliance to requirements through simulation

构建数字化线索 – 需求/设计/测试



Index	ID	Summary	Implemented	Verified
StakeholderRequirements				
1	-	Stakeholders		
2	-	Operating Environment		
2.1	STAKEHOLDER-03	Operating Temp		
2.2	STAKEHOLDER-05	Noise Pollution		
3	-	Performance		
4	STAKEHOLDER-15	Mean Repair Cost		
5	-	Safety		

- **Described by:**
  - TempControlUnit
- **Implemented by:**
  - Determine_cooling_action
- **Needed by:**
  - ACTOR-01 Operations Engineer
- **Verified by:**
  - Cooling not effective
  - Cooling effective

Status

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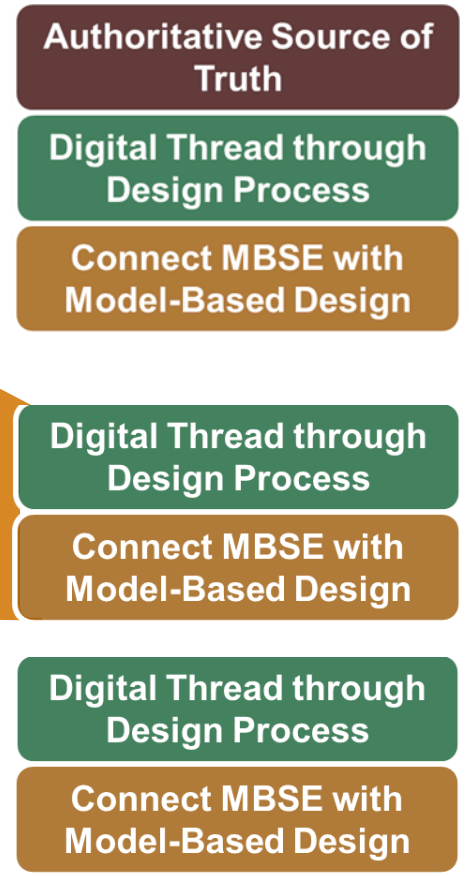
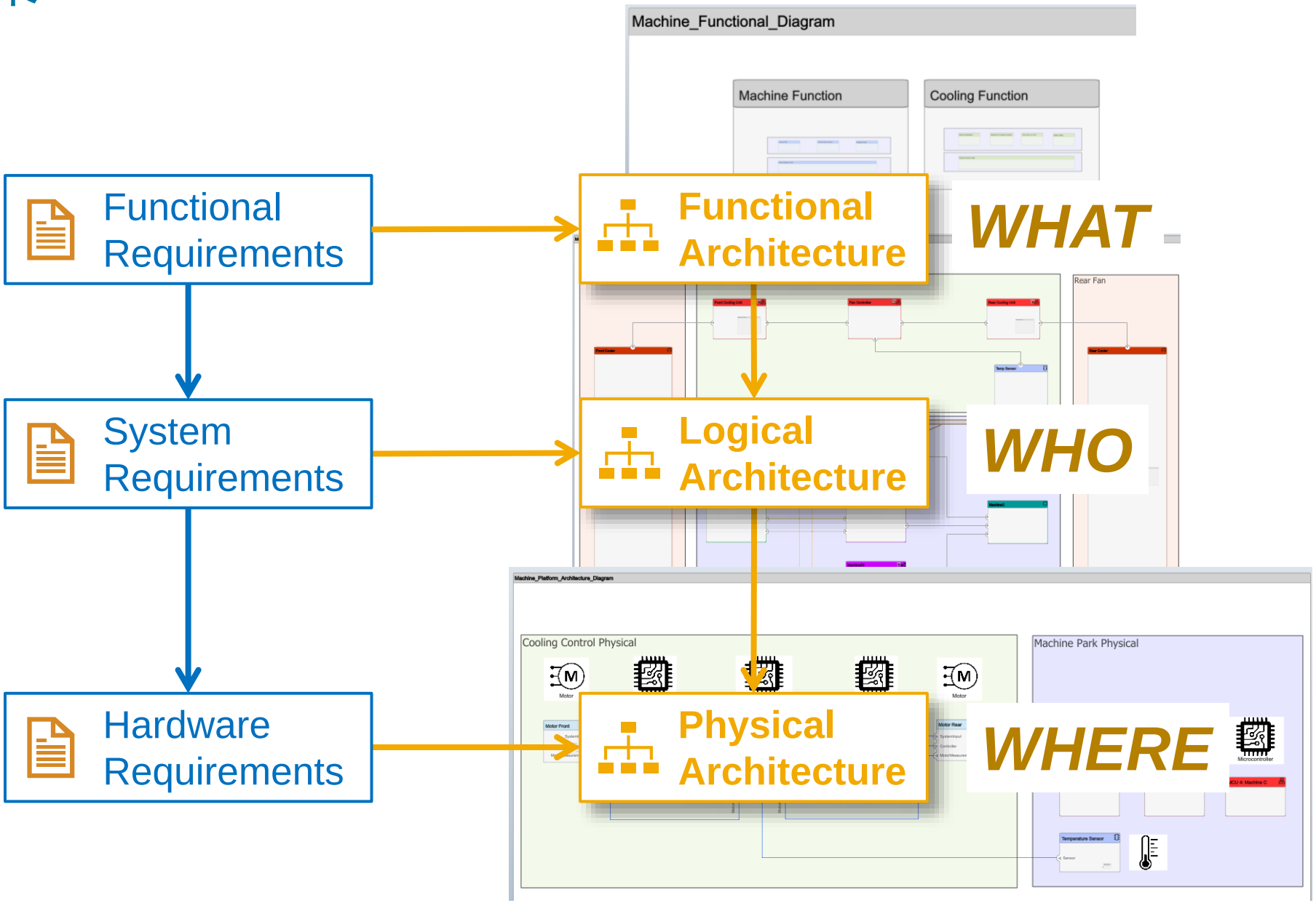
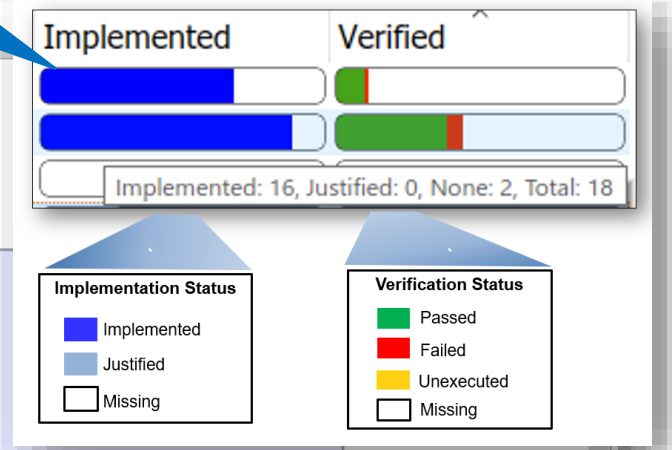


架构框架


Functional Requirements
System Requirements
Hardware Requirements
Functional Architecture
Logical Architecture
Physical Architecture
WHAT
WHO
WHERE
Machine_Functional_Diagram
Machine Function
Cooling Function
Rear Fan
Machine_Platform_Architecture_Diagram
Cooling Control Physical
Machine Park Physical

功能分解和需求追溯 – 功能架构

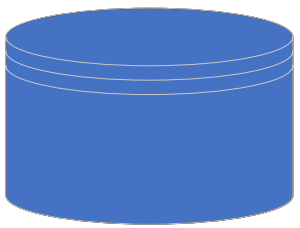
Implementation and verification status



Import requirements using ReqIF

System Requirements

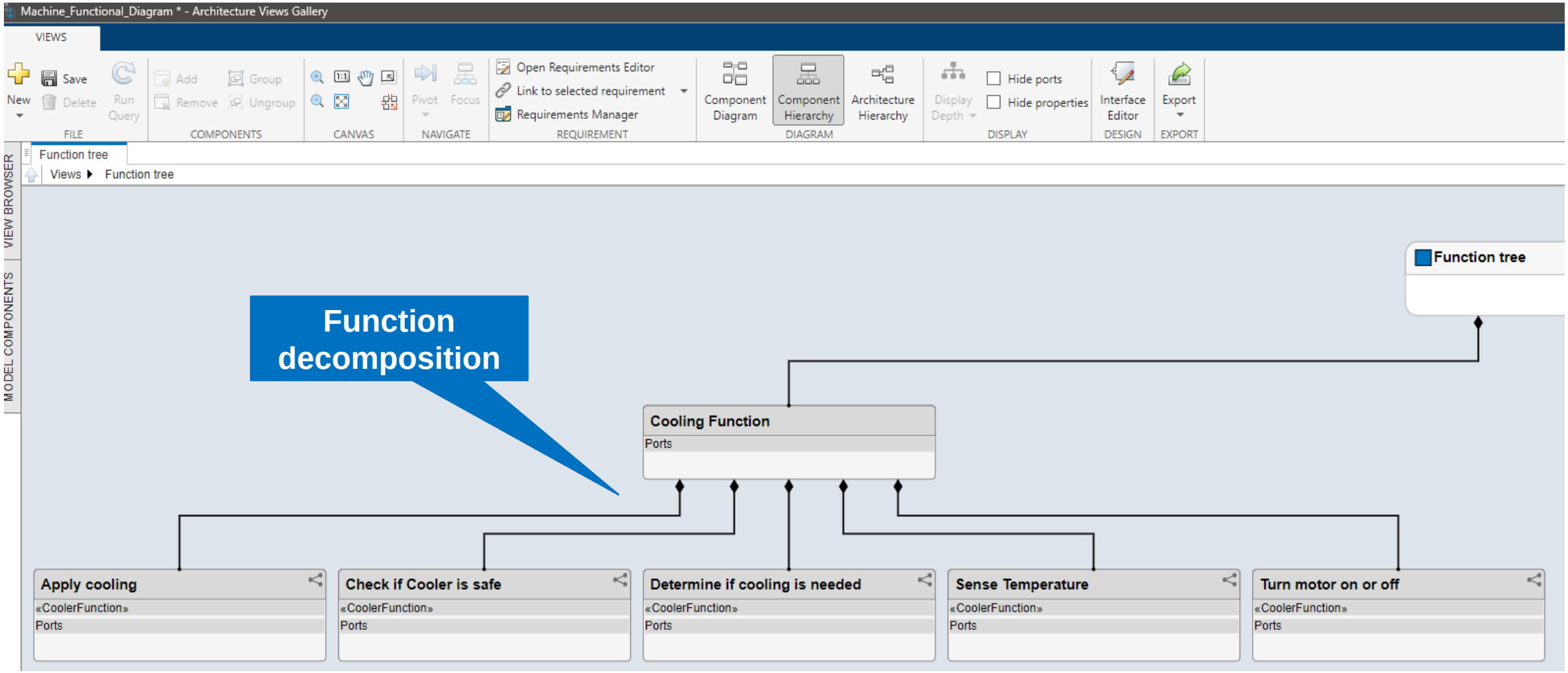
High-Level Requirements



ReqIF

Bi-directional traceability

理解功能架构 – 层级视图


Machine_Functional_Diagram * - Architecture Views Gallery
VIEWS
New
Save
Delete
Run Query
FILE
Add
Remove
Group
Ungroup
COMPONENTS
CANVAS
Pivot
Focus
NAVIGATE
Open Requirements Editor
Link to selected requirement
Requirements Manager
REQUIREMENT
Component Diagram
Component Hierarchy
Architecture Hierarchy
DIAGRAM
Display Depth
Hide ports
Hide properties
DISPLAY
Interface Editor
DESIGN
Export
EXPORT
Function tree
Views
Function tree
VIEW BROWSER
MODEL COMPONENTS
Function decomposition
Function tree
Cooling Function
Ports
Apply cooling
«CoolerFunction»
Ports
Check if Cooler is safe
«CoolerFunction»
Ports
Determine if cooling is needed
«CoolerFunction»
Ports
Sense Temperature
«CoolerFunction»
Ports
Turn motor on or off
«CoolerFunction»
Ports

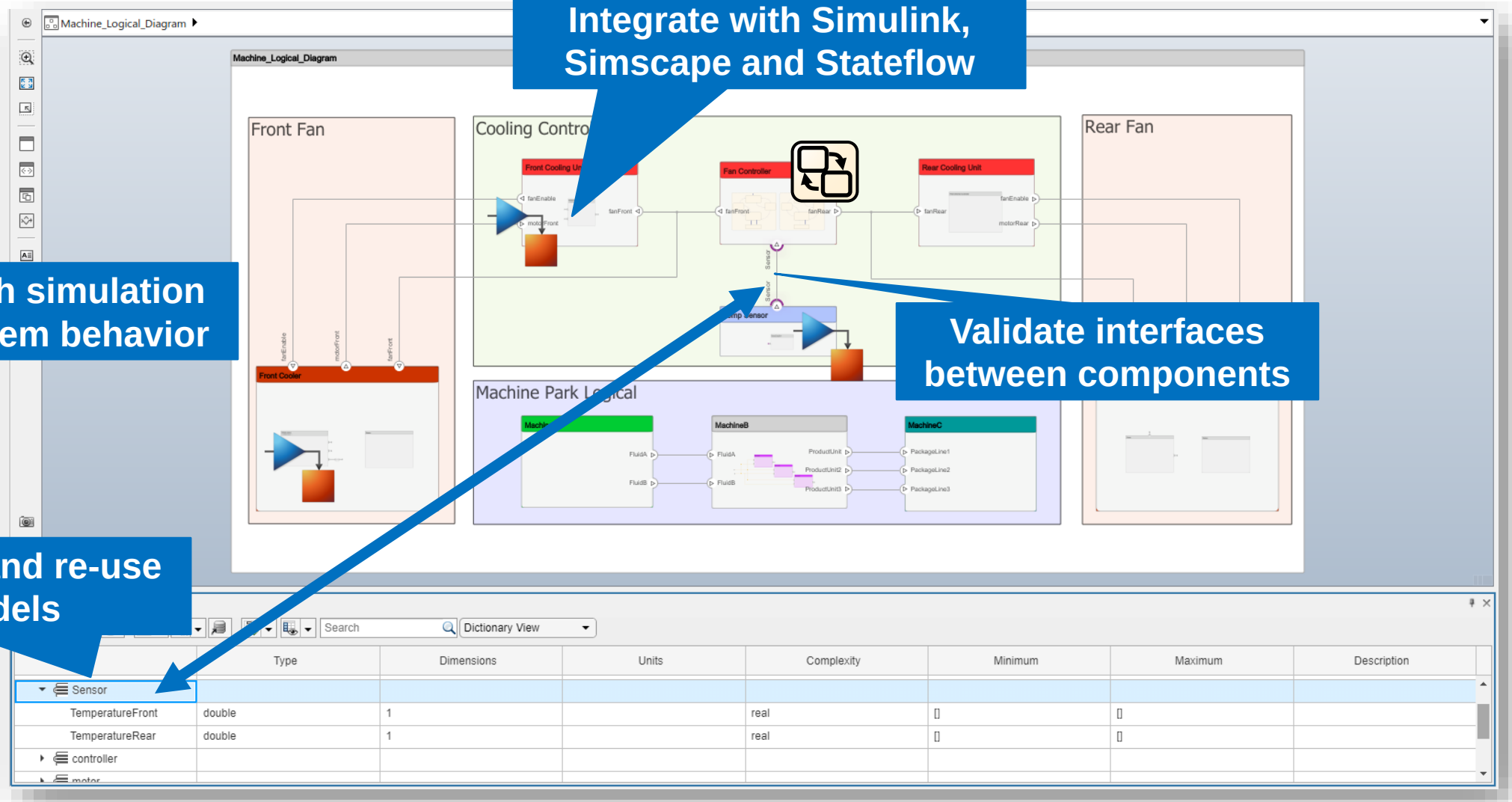
确认组件接口及系统行为 – 逻辑架构

Enable models with simulation to understand system behavior

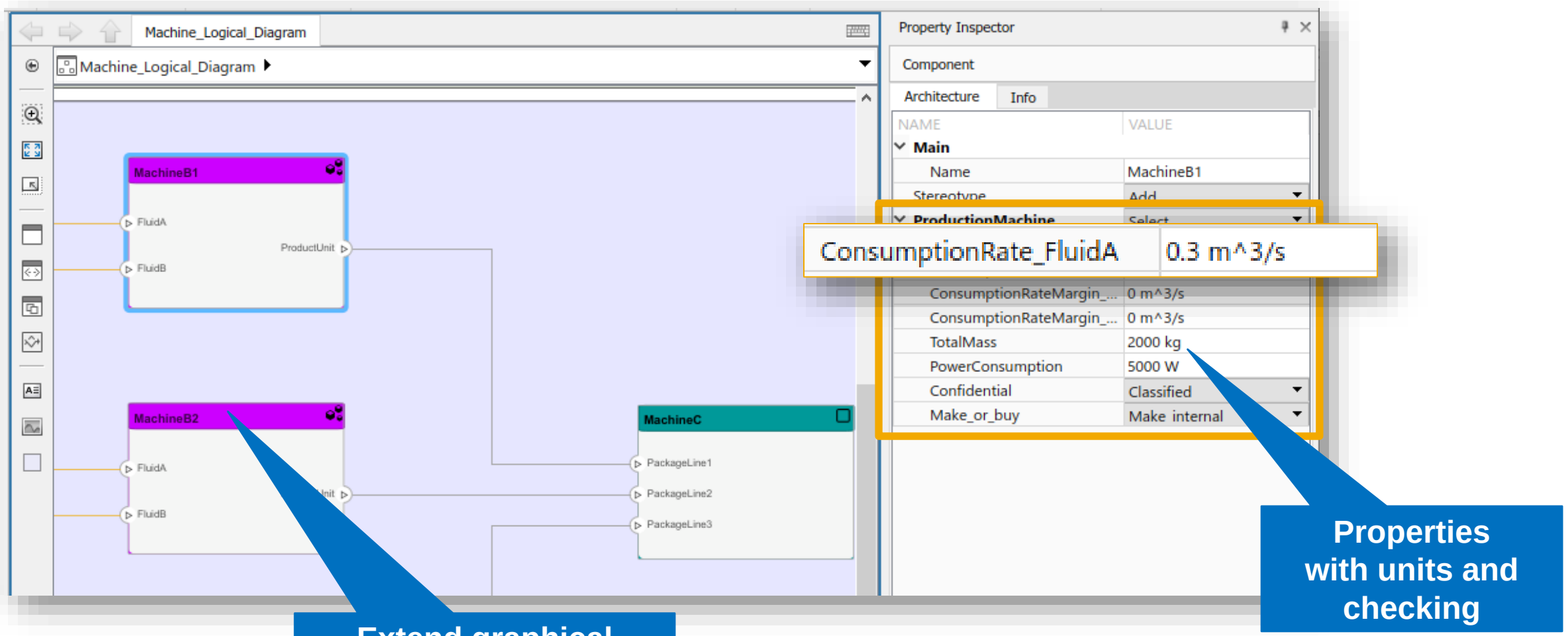
Integrate with Simulink, Simscape and Stateflow

Validate interfaces between components

Define interfaces and re-use between models



扩展架构属性 – 构型模板


Machine_Logical_Diagram
Machine_Logical_Diagram
MachineB1
FluidA
FluidB
ProductUnit
MachineB2
FluidA
FluidB
MachineC
PackageLine1
PackageLine2
PackageLine3
Property Inspector
Component
Architecture
Info
NAME
VALUE
Main
Name
MachineB1
Stereotype
Add
ProductionMachine
Select
ConsumptionRate_FluidA
0.3 m^3/s
ConsumptionRateMargin_...
0 m^3/s
ConsumptionRateMargin_...
0 m^3/s
TotalMass
2000 kg
PowerConsumption
5000 W
Confidential
Classified
Make_or_buy
Make internal
Extend graphical language with domain specific elements
Properties with units and checking

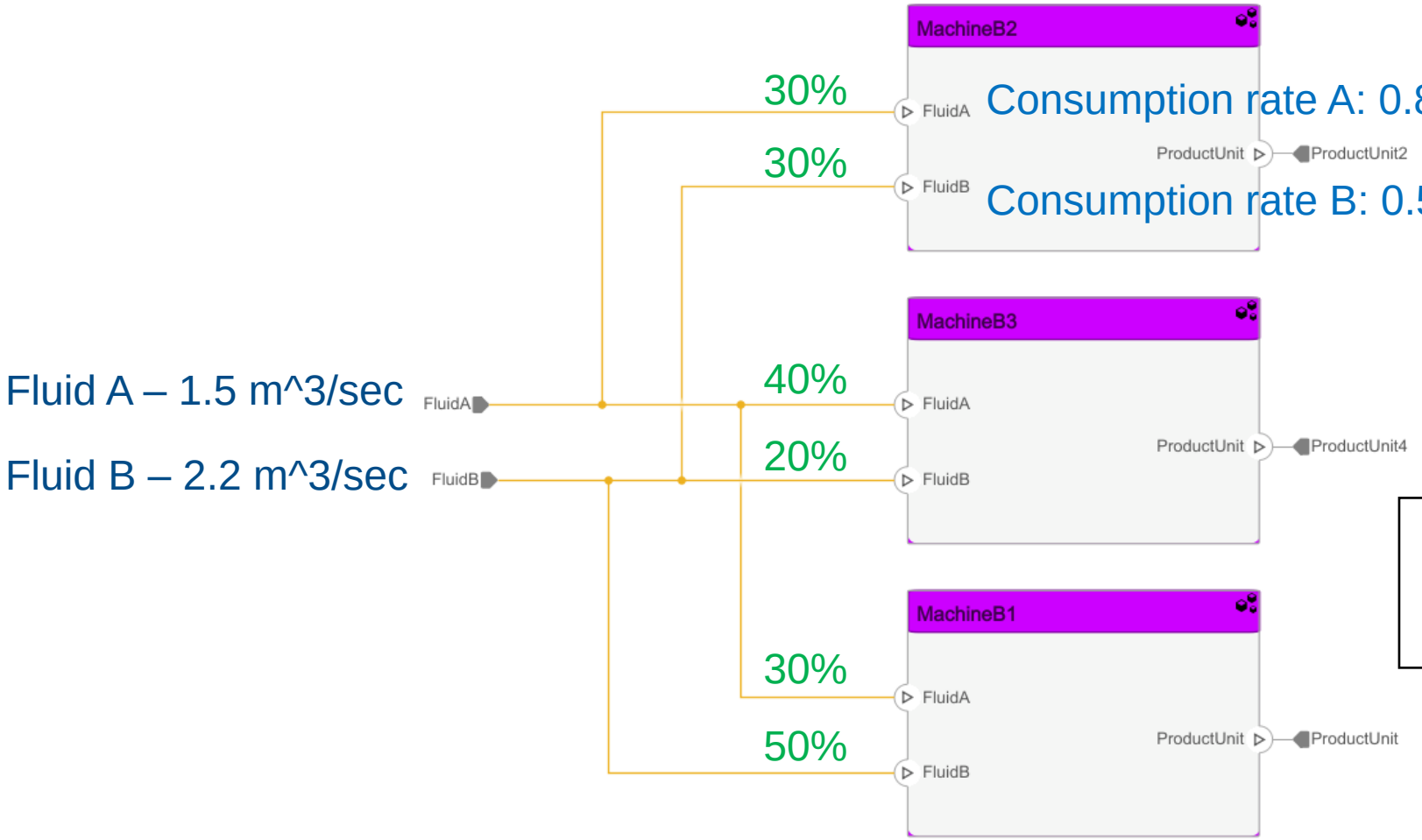
基于属性的需求验证

2.1	ReqSys2.1	Cooling Unit		
2.2	ReqSys2.2	Machine park		
2.2.1	ReqSys2.2.1	Machine topology should have a fluid throughput at specified levels	0	m^3/sec
2.3	ReqSys2.3	Constraints		

Arial 10 **B** *I* U

Fluid A: 1.5 m^3/sec
Fluid B: 2.2 m^3/sec

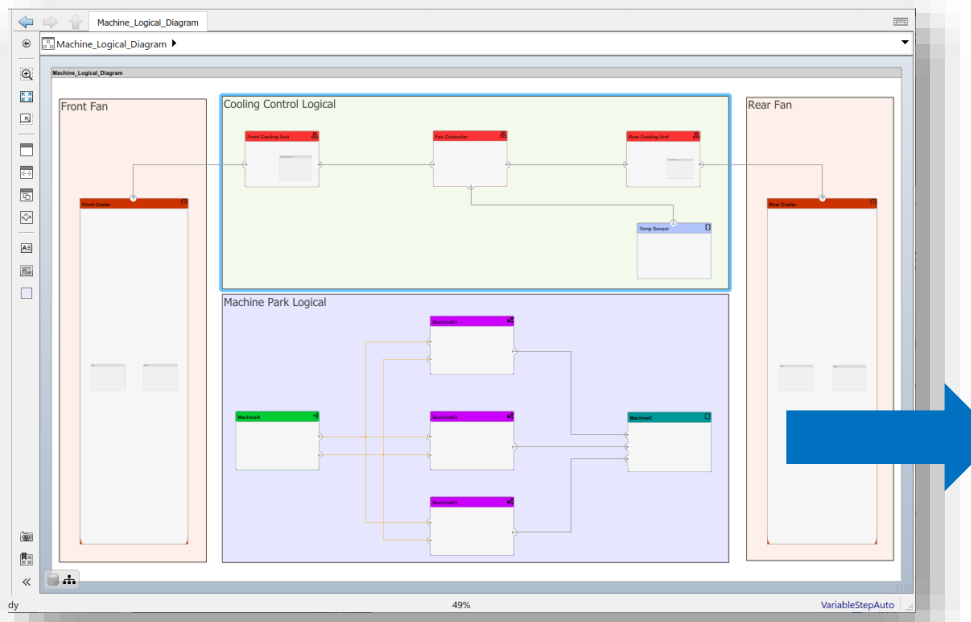
Consumtion rate margin => 0 m^3/sec for all individual machines as well as total of all machines.



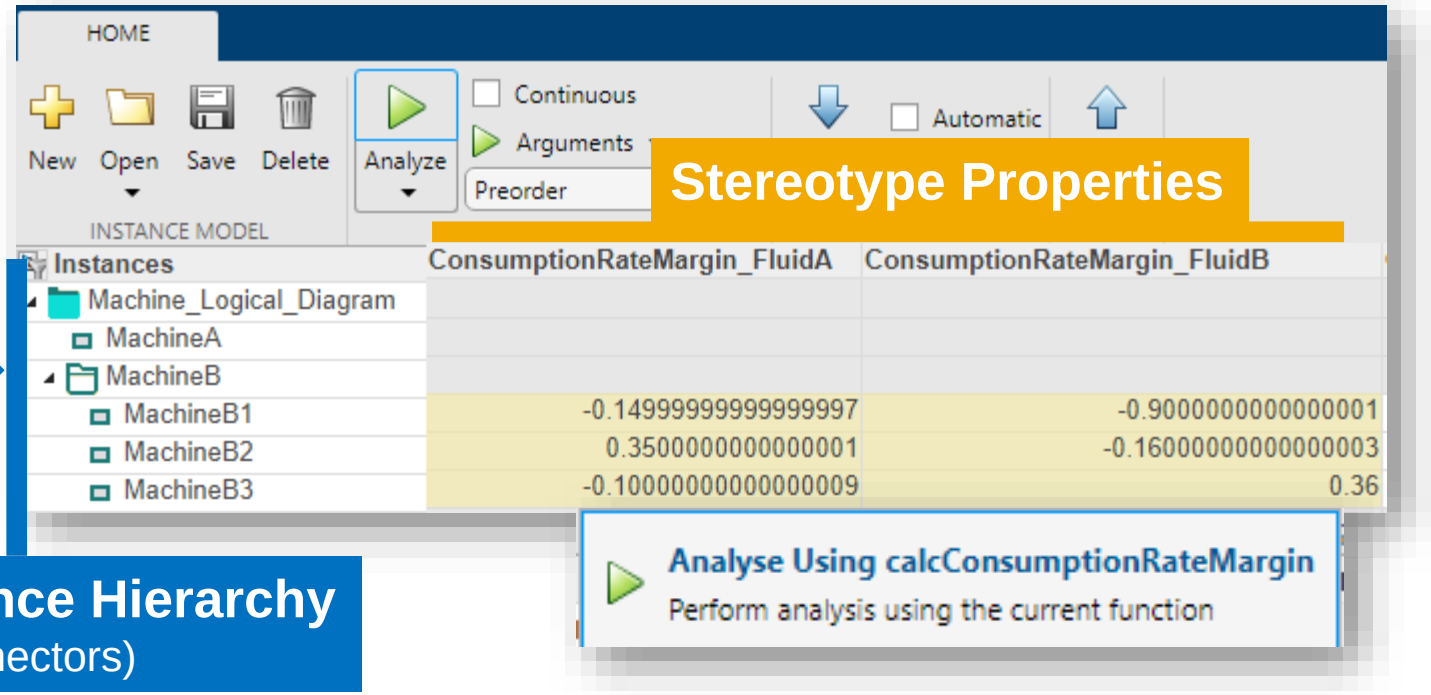
Validate whether requirements are met through static analysis

Consumption rate margin = Consumption rate - (Throughput x Fluid rate)

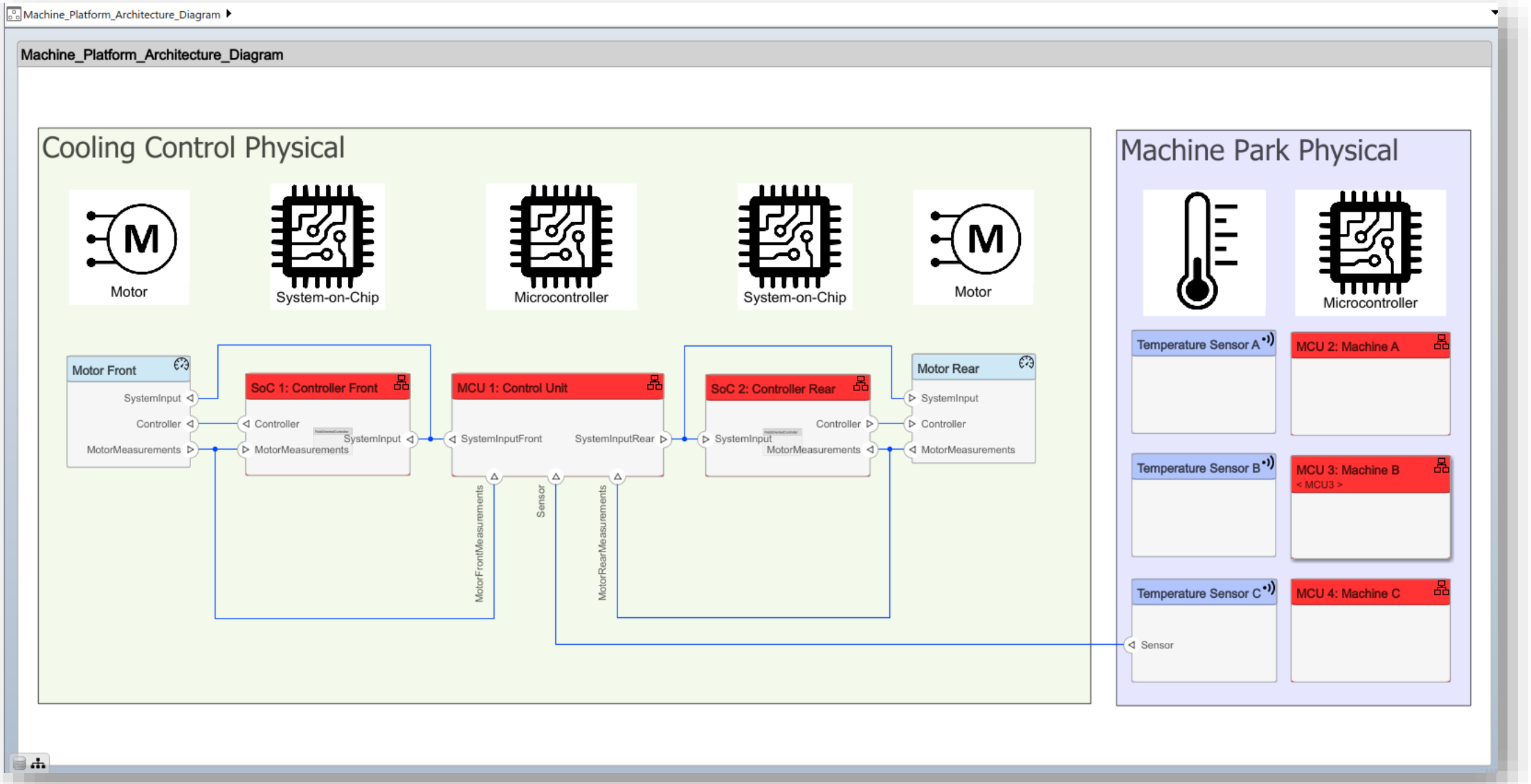
基于属性的设计权衡



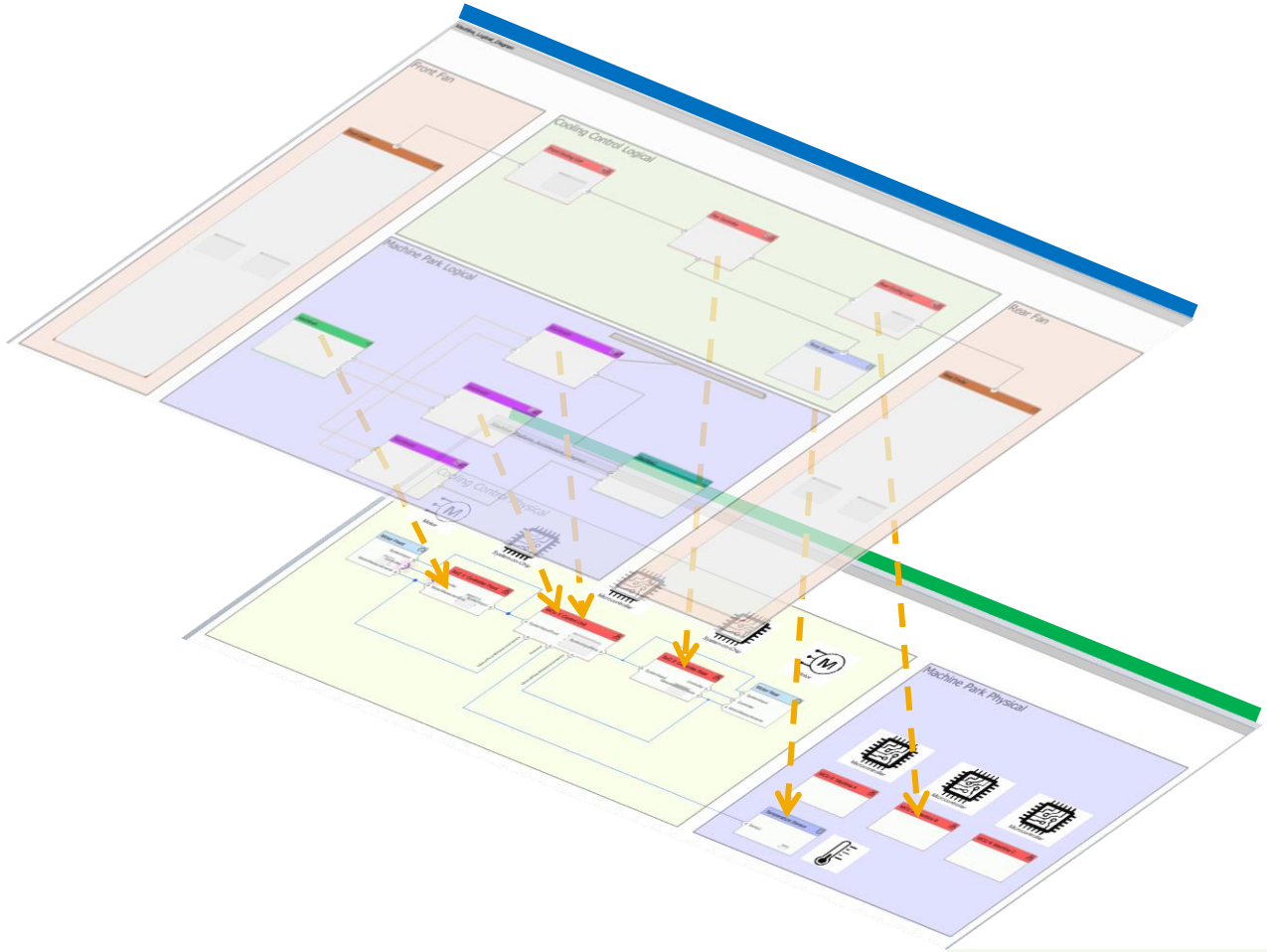
Model Instance Hierarchy
(incl Ports, Connectors)



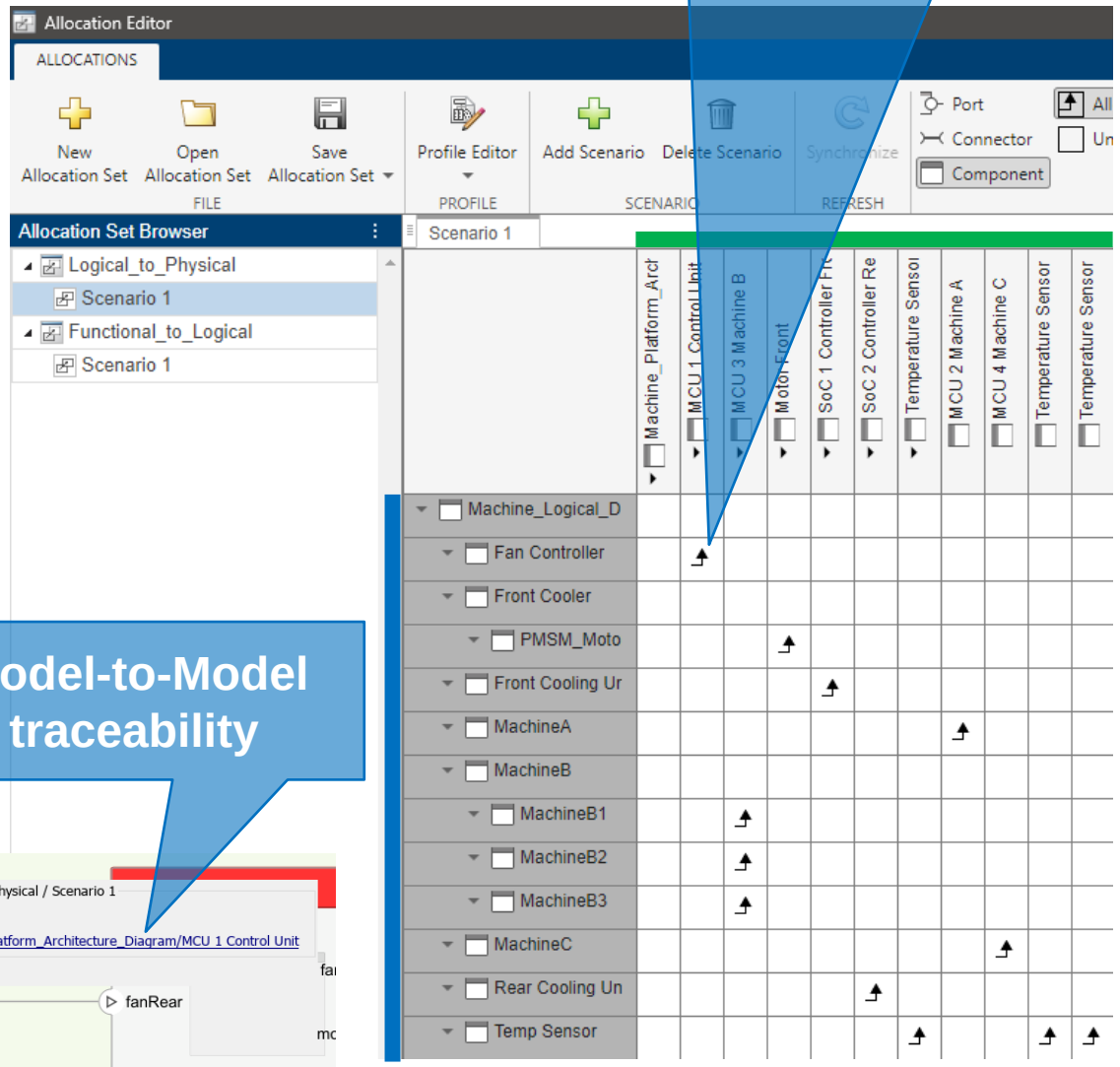
描述部署平台 – 物理架构


Machine_Platform_Architecture_Diagram
Machine_Platform_Architecture_Diagram
Cooling Control Physical
Motor
System-on-Chip
Microcontroller
System-on-Chip
Motor
Motor Front
SystemInput
Controller
MotorMeasurements
SoC 1: Controller Front
Controller
SystemInput
MotorMeasurements
MCU 1: Control Unit
SystemInputFront
SystemInputRear
SoC 2: Controller Rear
SystemInput
Controller
MotorMeasurements
Motor Rear
SystemInput
Controller
MotorMeasurements
MotorFrontMeasurements
Sensor
MotorRearMeasurements
Machine Park Physical
Microcontroller
Temperature Sensor A
MCU 2: Machine A
Temperature Sensor B
MCU 3: Machine B
< MCU3 >
Temperature Sensor C
MCU 4: Machine C
Sensor

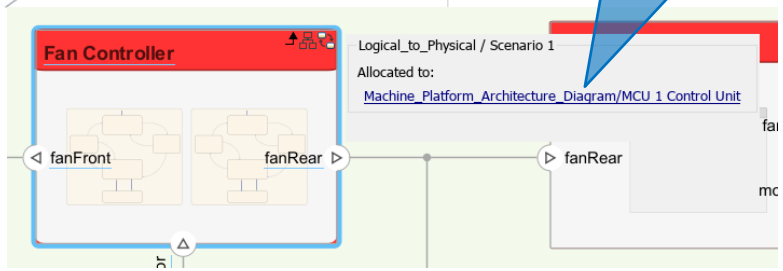
架构模型分配和追溯



Allocation between functional, logical and physical architecture models

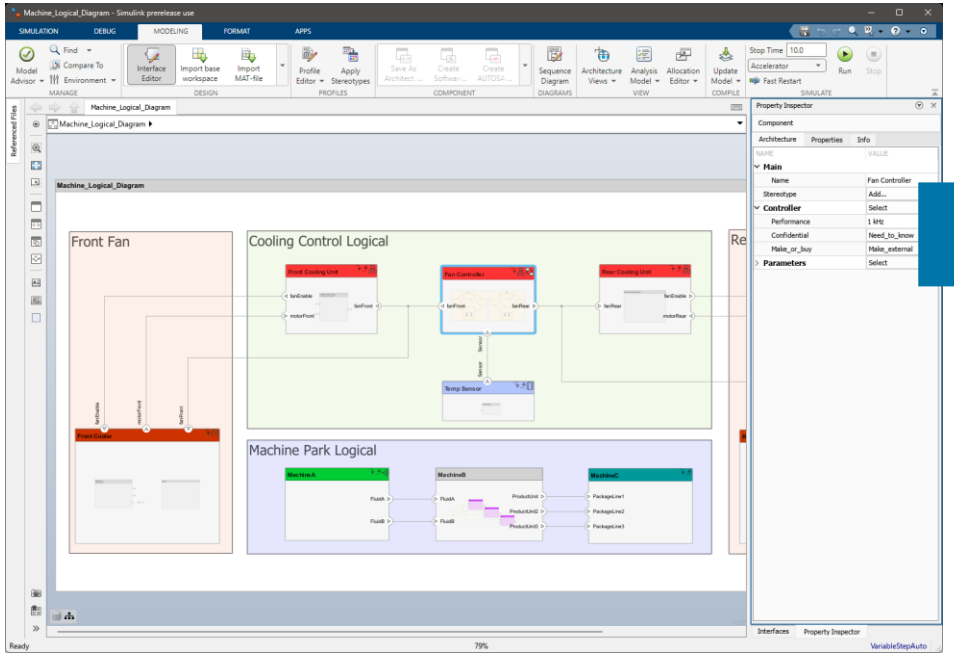


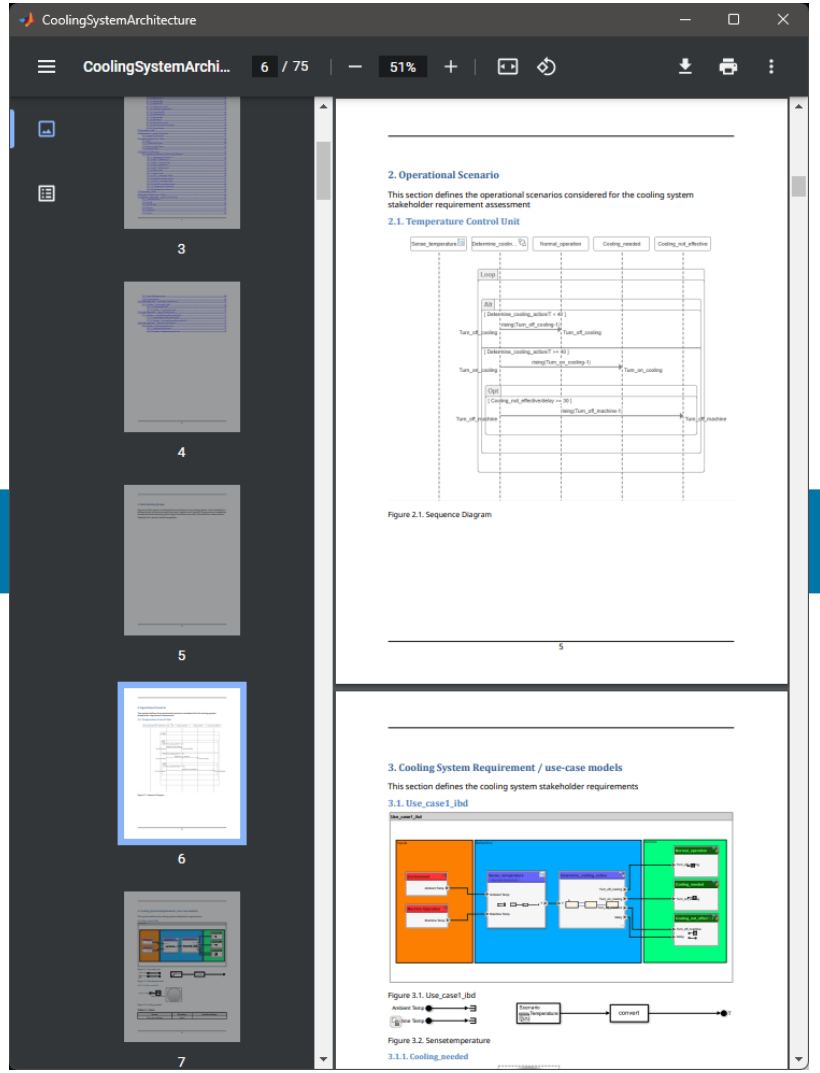
Model-to-Model traceability



协同团队开发 – 架构报告

Systems Engineer





Software Engineers

Stakeholders

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Authoritative Source of Truth

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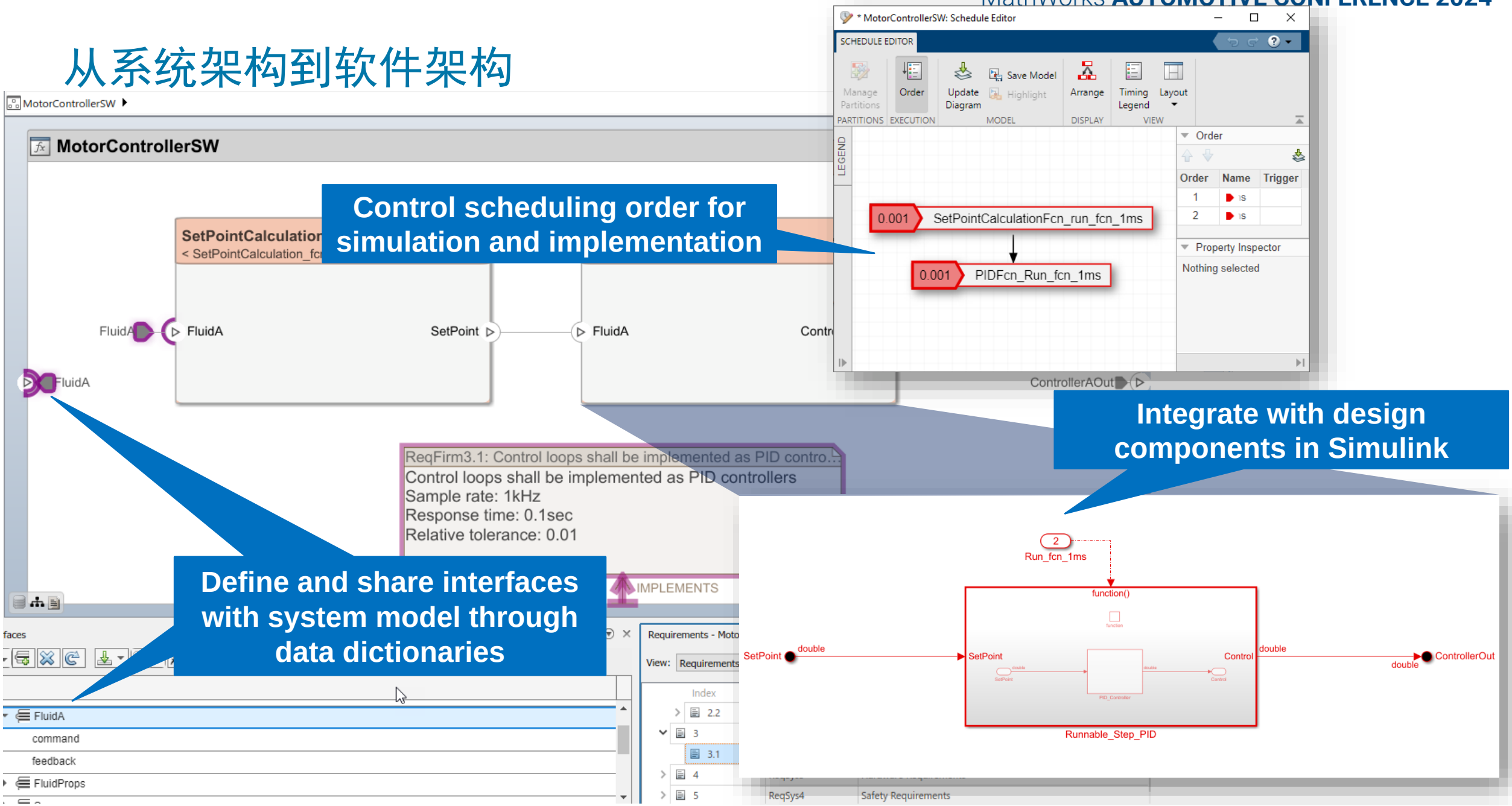
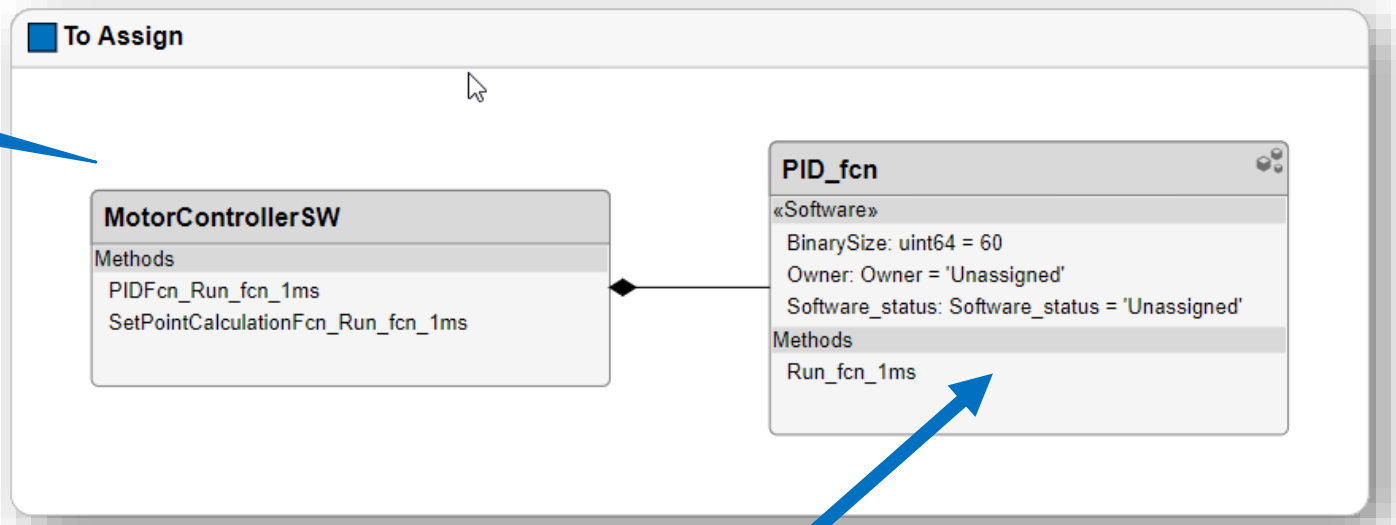
从系统架构到软件架构

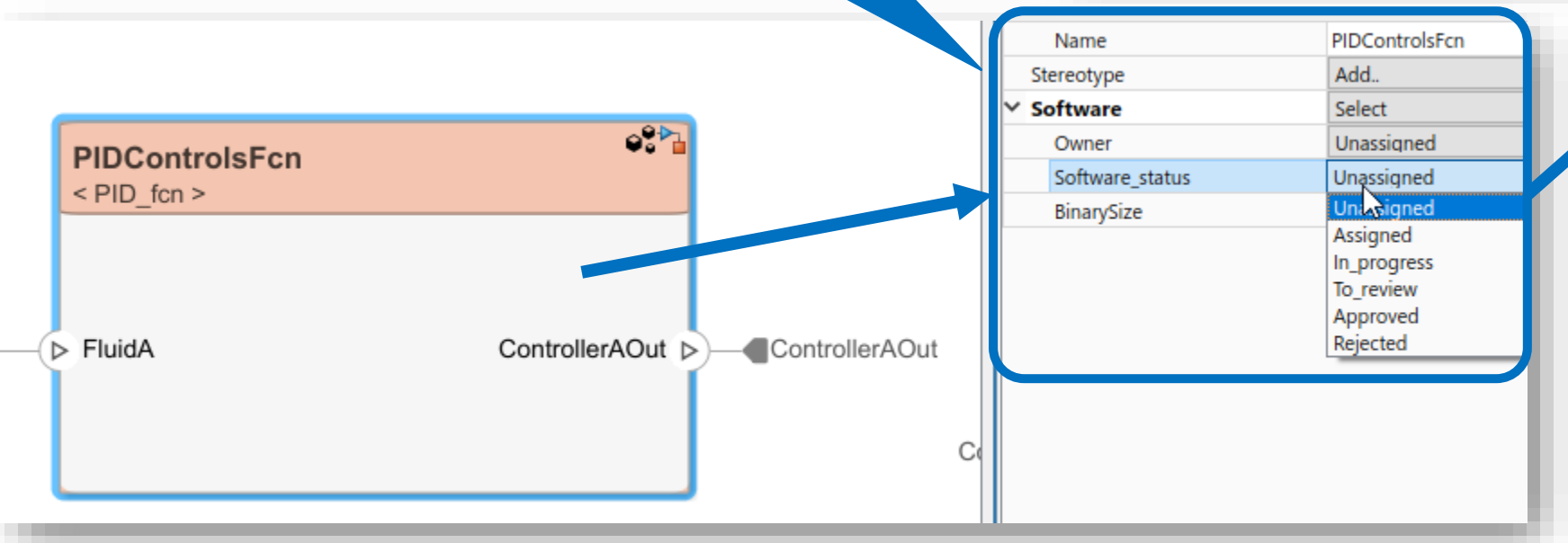

MotorControllerSW
MotorControllerSW
SetPointCalculation
< SetPointCalculation_fc
FluidA
FluidA
SetPoint
FluidA
FluidA
Control scheduling order for simulation and implementation
* MotorControllerSW: Schedule Editor
SCHEDULE EDITOR
Manage Partitions
Order
Update Diagram
Save Model
Highlight
Arrange
Timing Legend
Layout
PARTITIONS
EXECUTION
MODEL
DISPLAY
VIEW
LEGEND
0.001
SetPointCalculationFcn_run_fcn_1ms
0.001
PIDFcn_Run_fcn_1ms
Order
Order
Name
Trigger
1
2
Property Inspector
Nothing selected
ControllerAOut
Integrate with design components in Simulink
ReqFirm3.1: Control loops shall be implemented as PID contro...
Control loops shall be implemented as PID controllers
Sample rate: 1kHz
Response time: 0.1sec
Relative tolerance: 0.01
IMPLEMENTS
Define and share interfaces with system model through data dictionaries
faces
FluidA
command
feedback
FluidProps
Requirements - Moto
View: Requirements
Index
2.2
3
3.1
4
5
2
Run_fcn_1ms
function()
SetPoint
double
SetPoint
Control
double
double
ControllerOut
Runnable_Step_PID
ReqSys4
Safety Requirements

管理复杂度 – 软件视图

Views filtered by software status as a class diagram

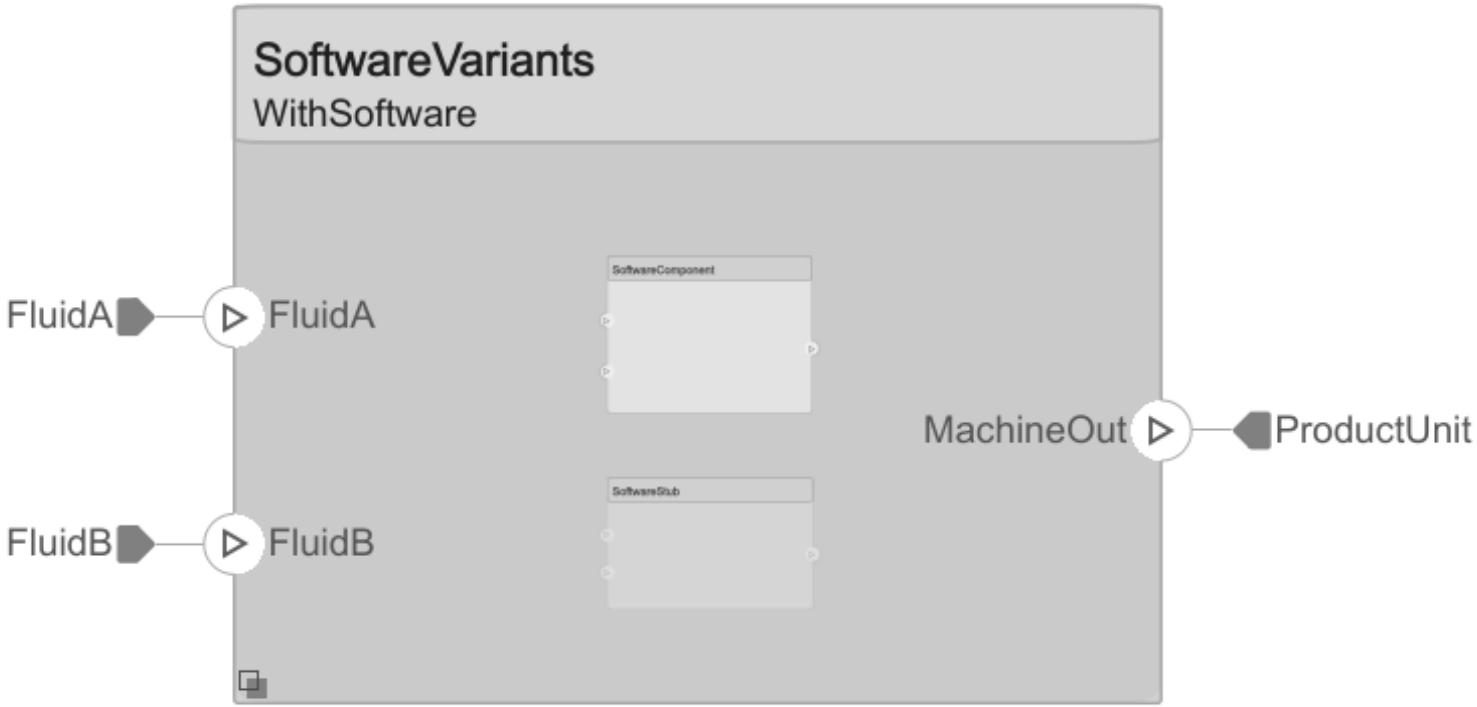
Stereotypes to specify custom properties

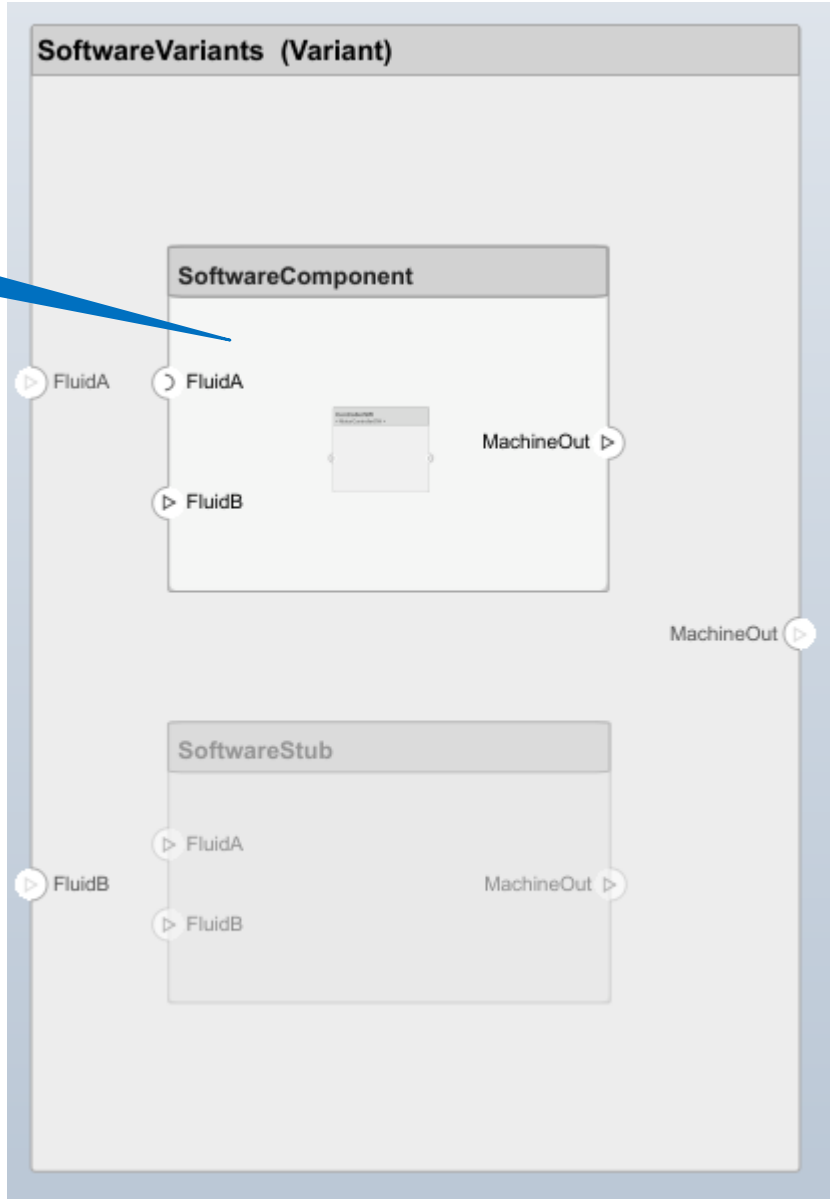




管理架构配置 – 变型设计

Active software variant



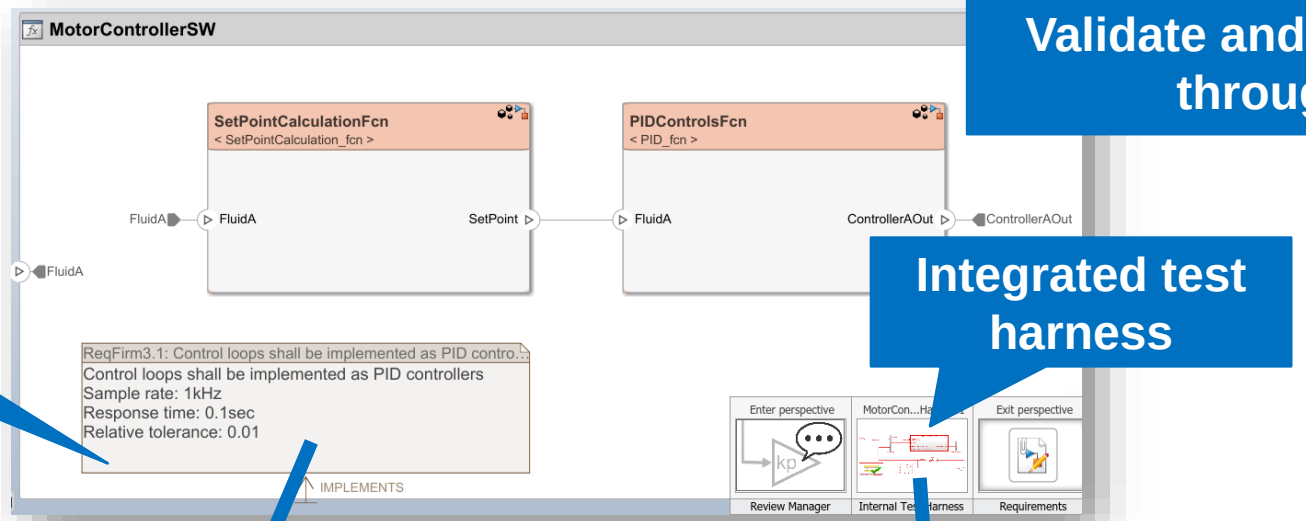


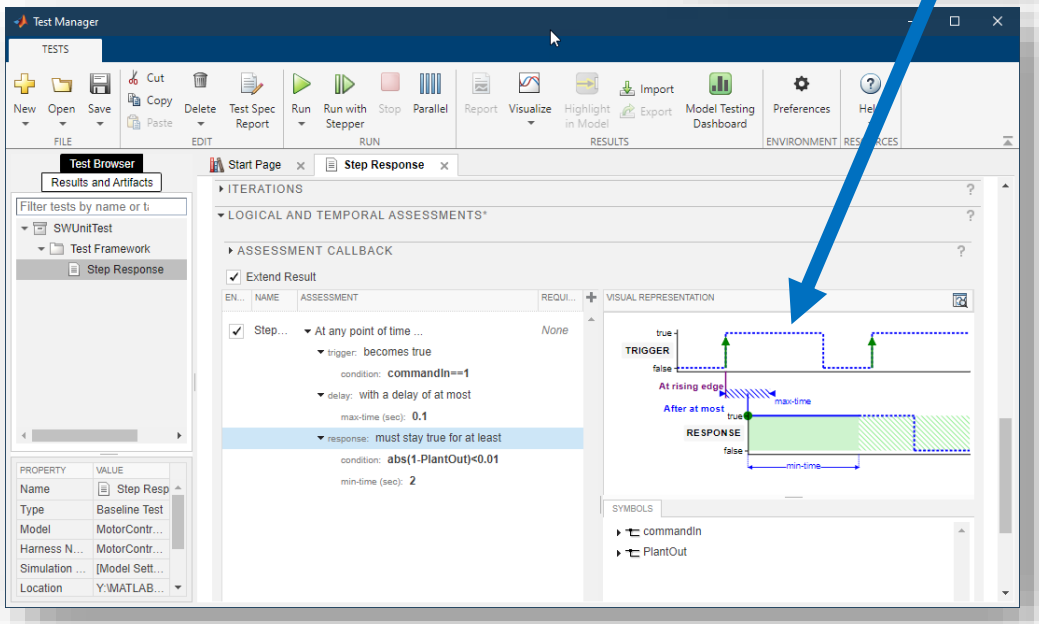
测试和验证软件组件

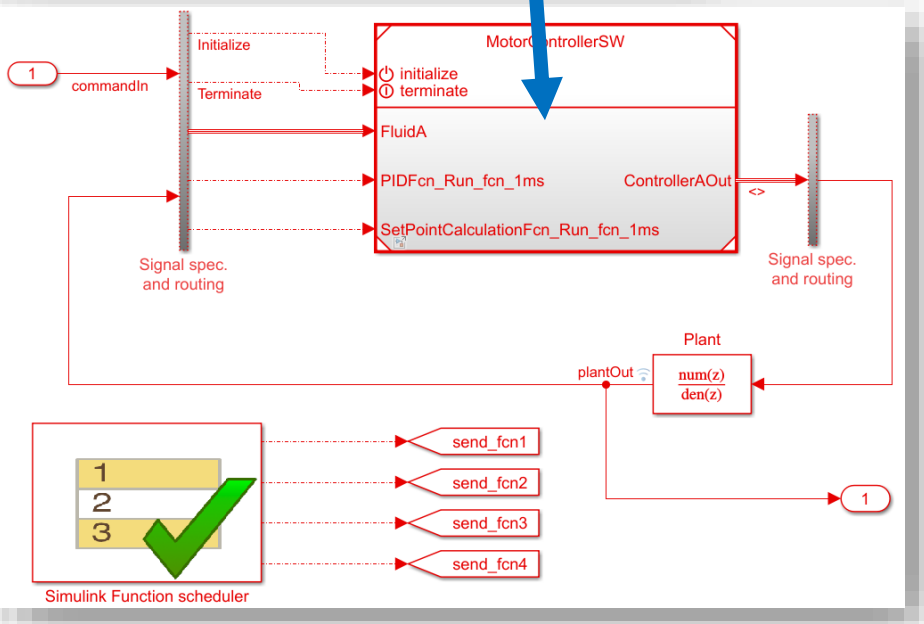
Capture design requirements through unit tests

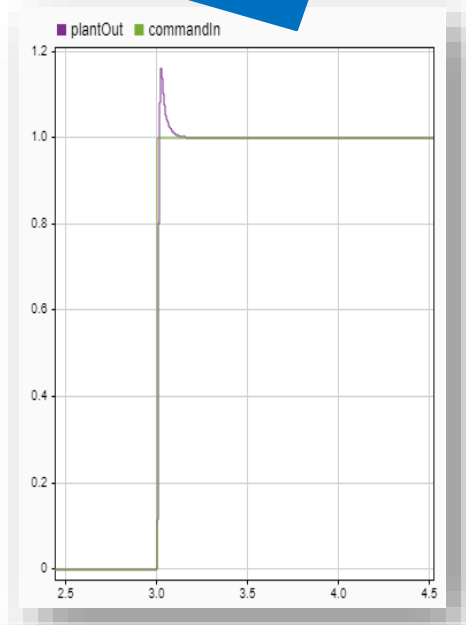
Validate and analyze test results through simulation

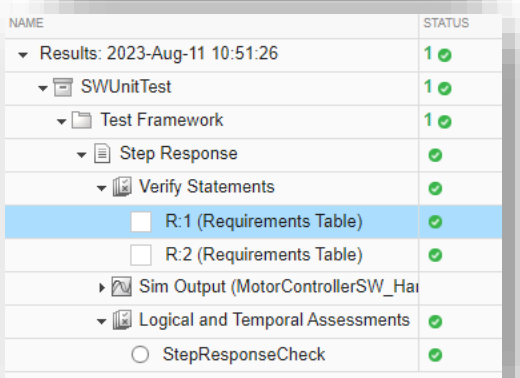
Integrated test harness



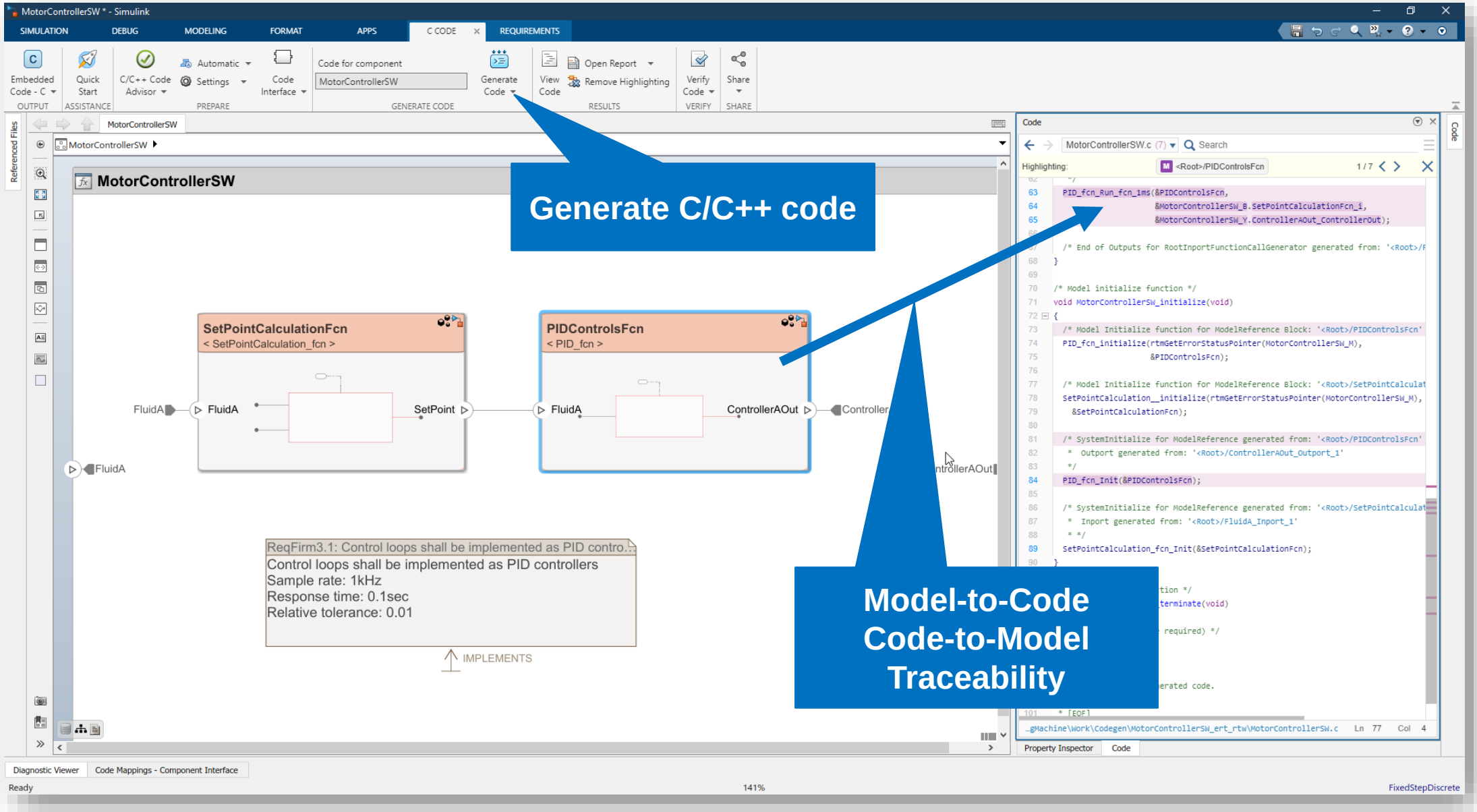






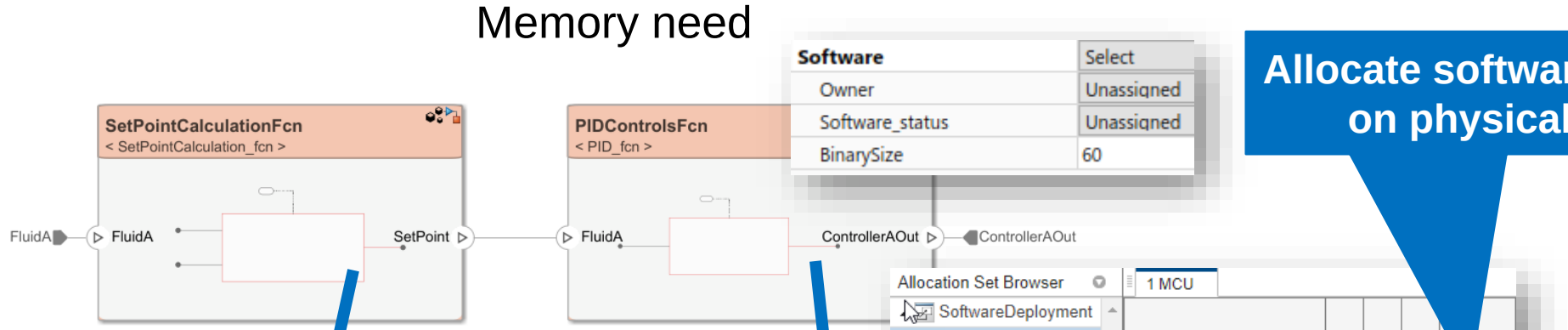


生成可追溯的代码

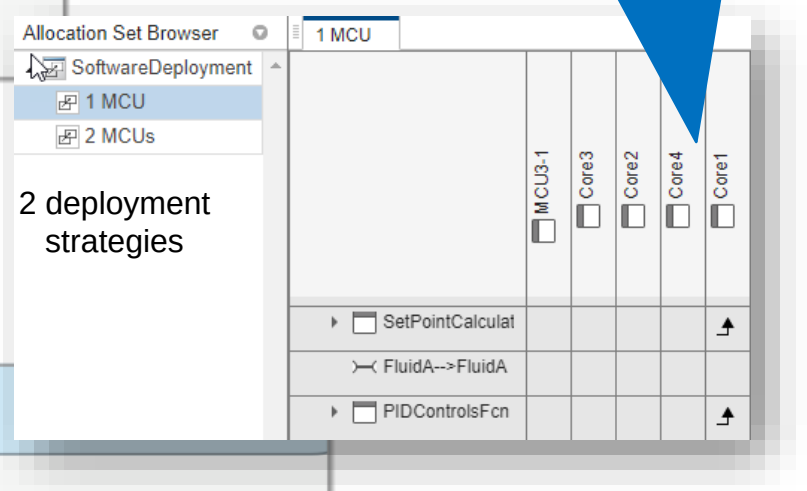

Generate C/C++ code
Model-to-Code
Code-to-Model
Traceability
MotorControllerSW * - Simulink
SIMULATION
DEBUG
MODELING
FORMAT
APPS
C CODE
REQUIREMENTS
Embedded Code - C
Quick Start
C/C++ Code Advisor
Automatic
Settings
Code Interface
Code for component
MotorControllerSW
Generate Code
View Code
Open Report
Remove Highlighting
Verify Code
Share
OUTPUT
ASSISTANCE
PREPARE
GENERATE CODE
RESULTS
VERIFY
SHARE
MotorControllerSW
SetPointCalculationFcn
< SetPointCalculation_fcn >
PIDControlsFcn
< PID_fcn >
FluidA
SetPoint
ControllerAOut
ReqFirm3.1: Control loops shall be implemented as PID contro...
Control loops shall be implemented as PID controllers
Sample rate: 1kHz
Response time: 0.1sec
Relative tolerance: 0.01
IMPLEMENTS
Code
MotorControllerSW.c
Search
Highlighting:
<Root>/PIDControlsFcn
PID_fcn_Run_fcn_1ms(&PIDControlsFcn,
&MotorControllerSW_B.SetPointCalculationFcn_i,
&MotorControllerSW_Y.ControllerAOut_ControllerOut);
/* Model initialize function */
void MotorControllerSW_initialize(void)
PID_fcn_initialize(rtmGetErrorStatusPointer(MotorControllerSW_M),
&PIDControlsFcn);
PID_fcn_Init(&PIDControlsFcn);
SetPointCalculation_fcn_Init(&SetPointCalculationFcn);
Property Inspector
Code
Diagnostic Viewer
Code Mappings - Component Interface
Ready
141%
FixedStepDiscrete

分配软件组件到硬件平台

Memory need

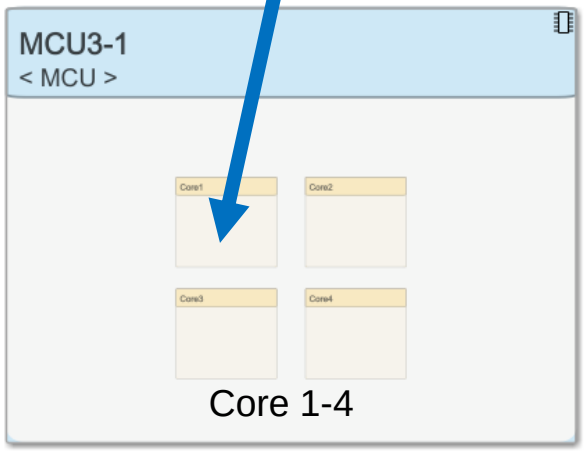


Allocate software components on physical platforms

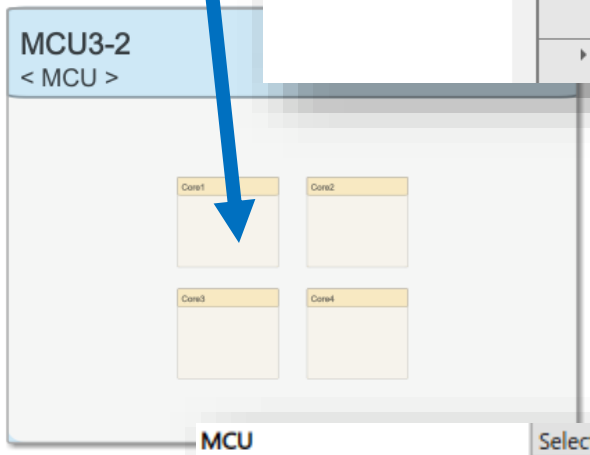


2 deployment strategies

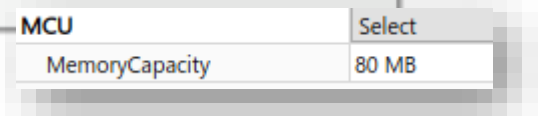
Analyze deployment impact on memory



Core 1-4



Memory availability

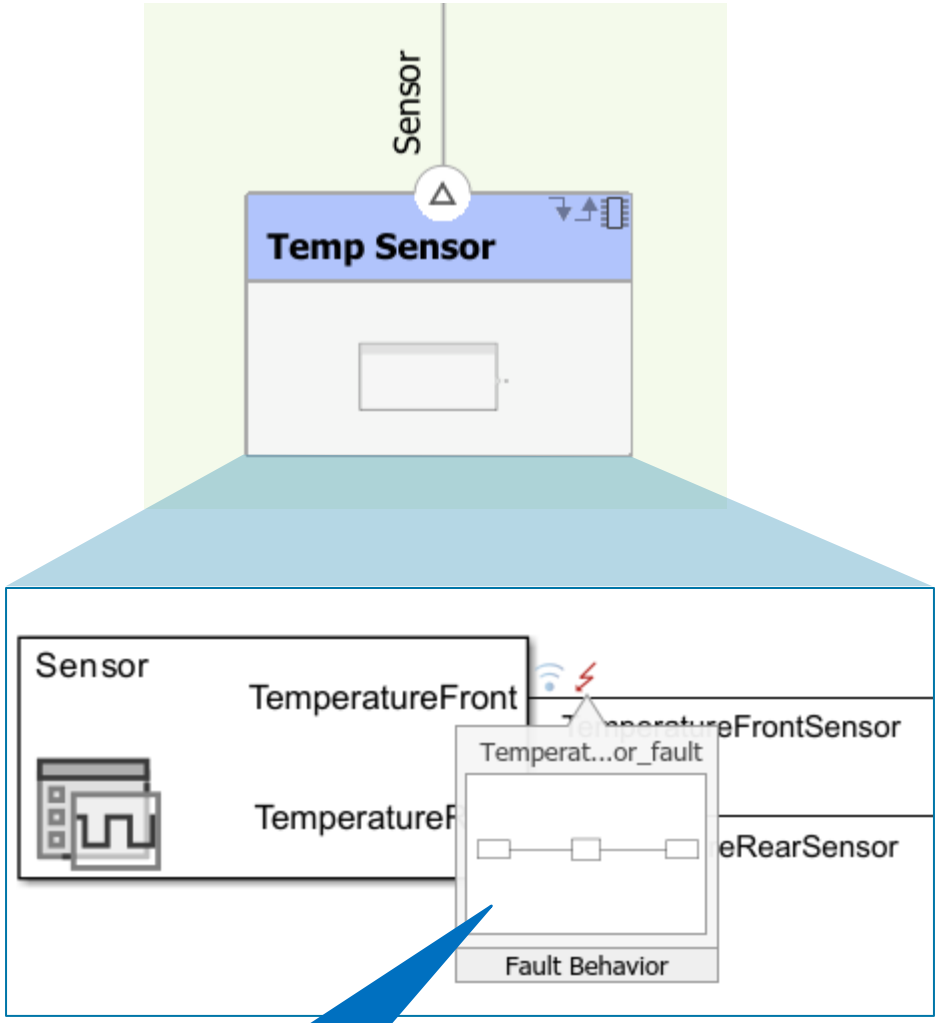


	1 MCU	2 MCUs
MCU1 Memory Used (MB)	100	60
MCU1 Memory (MB)	80	80
MCU1 Overloaded	1	0
MCU2 Memory Used (MB)	0	40
MCU2 Memory (MB)	80	80
MCU2 Overloaded	0	0

Memory overloaded

Memory not overloaded

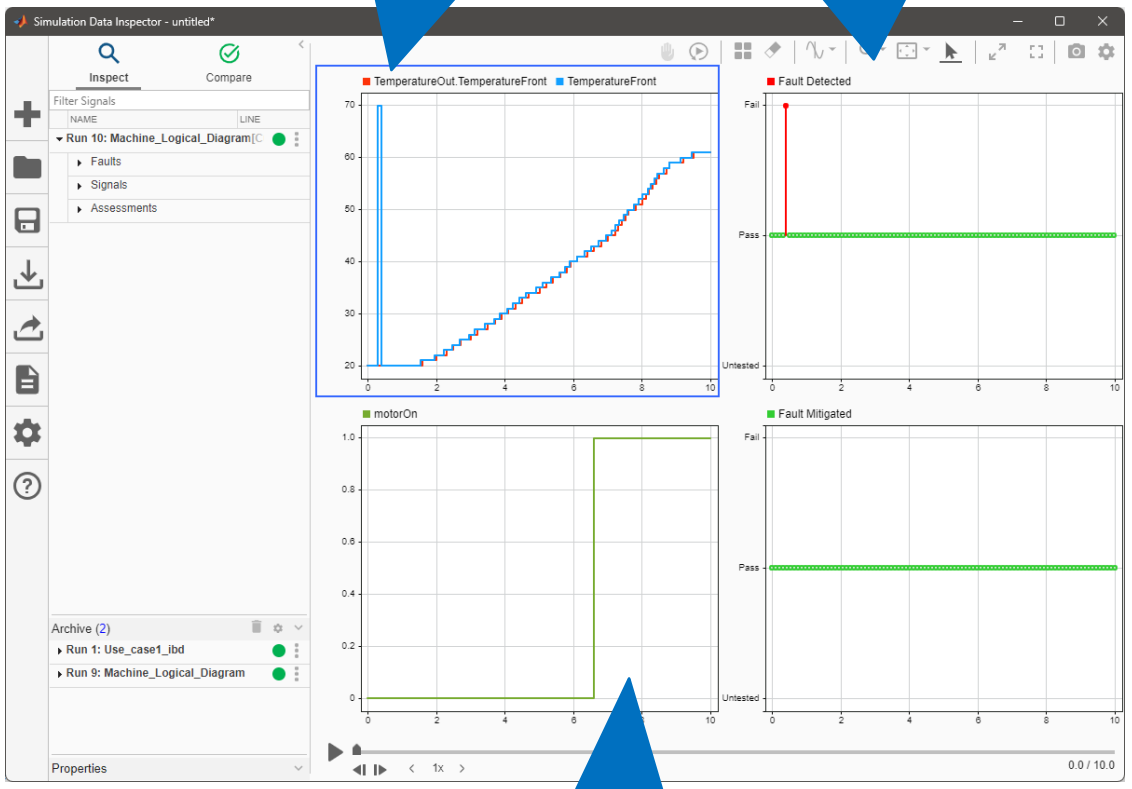
故障注入和仿真



Sensor fault model

Sensor fault injected

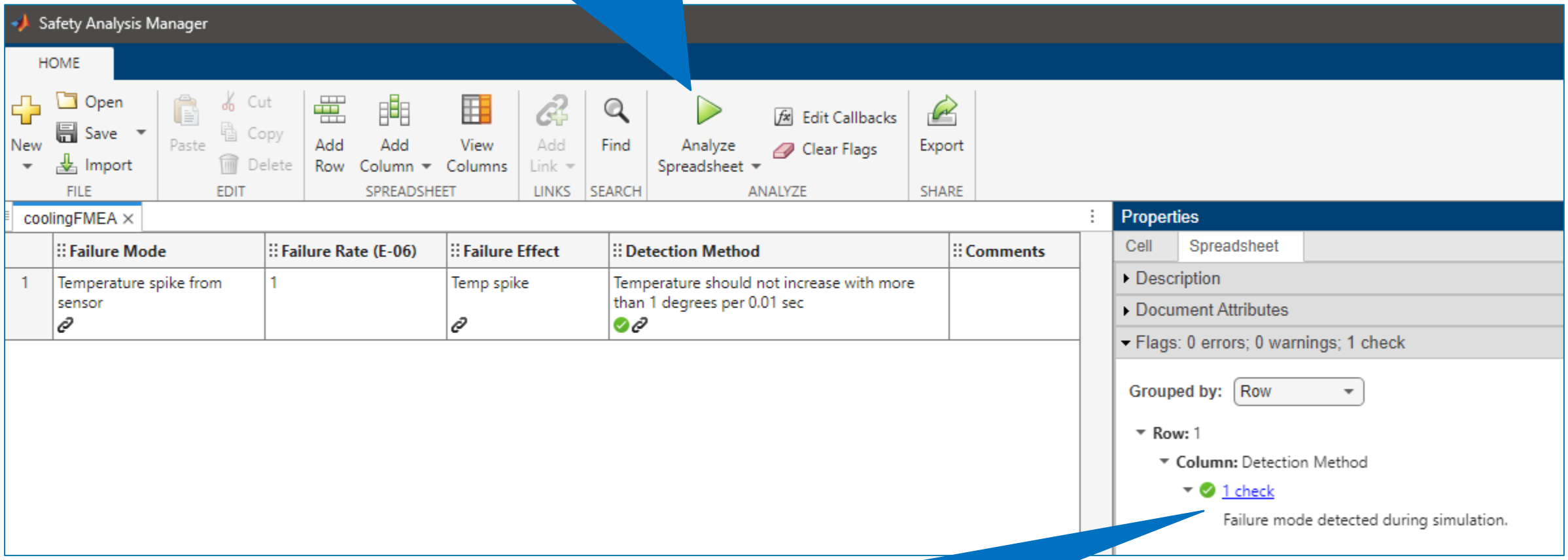
Sensor fault detected & mitigated



Cooling enabled

基于模型的安全分析

Analyze, simulate and report FMEA analyses

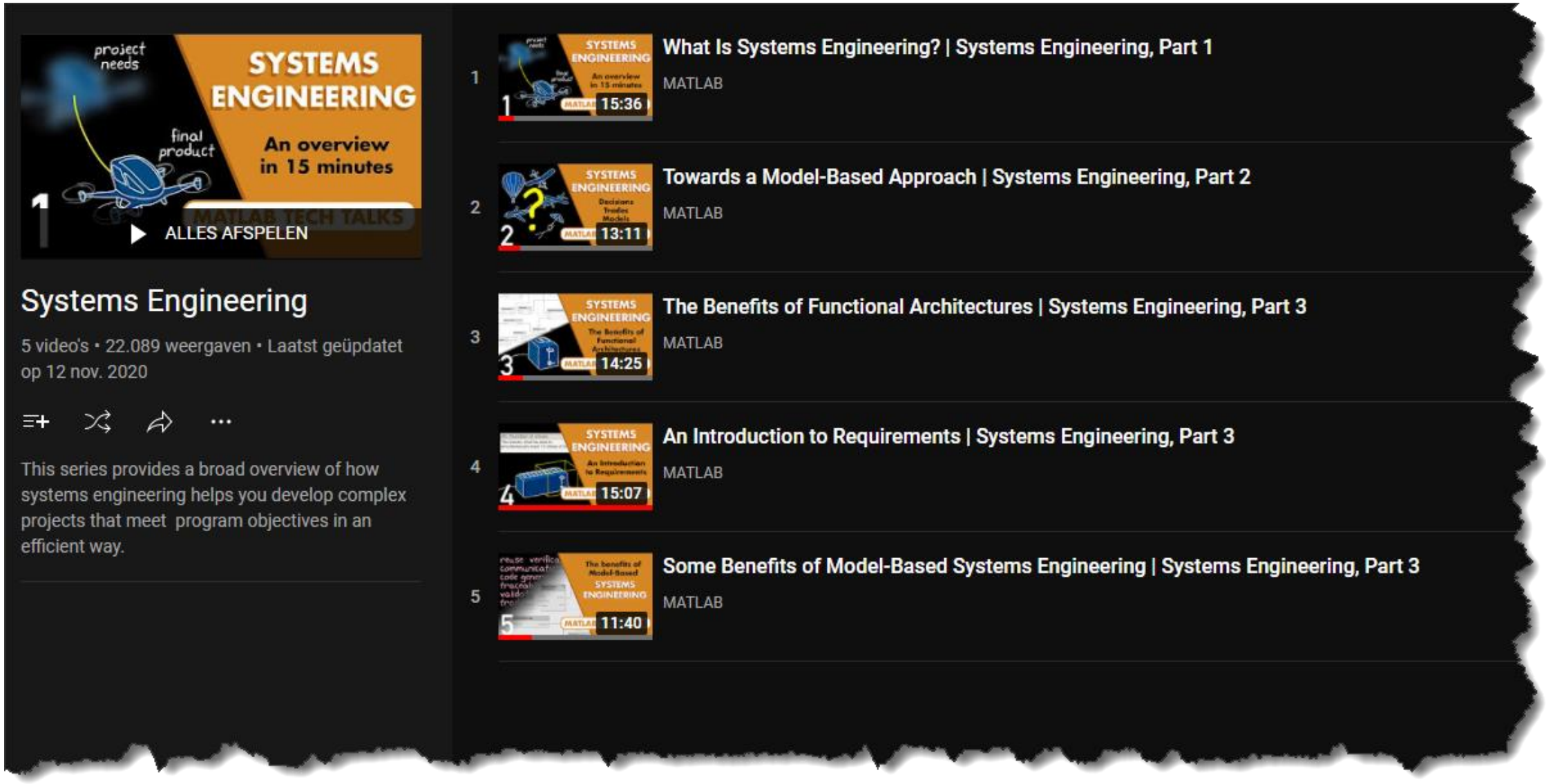


Automated verification if known failure modes are detected and mitigated

总结

- 通过建立追溯性来维护需求作为权威的设计来源是MBSE的关键之一。
- RFLP的架构框架和视图、模板功能有助于MBSE对复杂度进行管理。
- 基于同平台的架构模型与设计模型无缝对接最大程度上保证了接口的一致性。
- 充分利用分析和仿真为MBSE过程中的创建的模型提供了更高的附加值。

MathWorks的MBSE解决方案



[Systems Engineering: Managing System Complexity - MATLAB & Simulink (mathworks.cn)]

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Thank you

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