



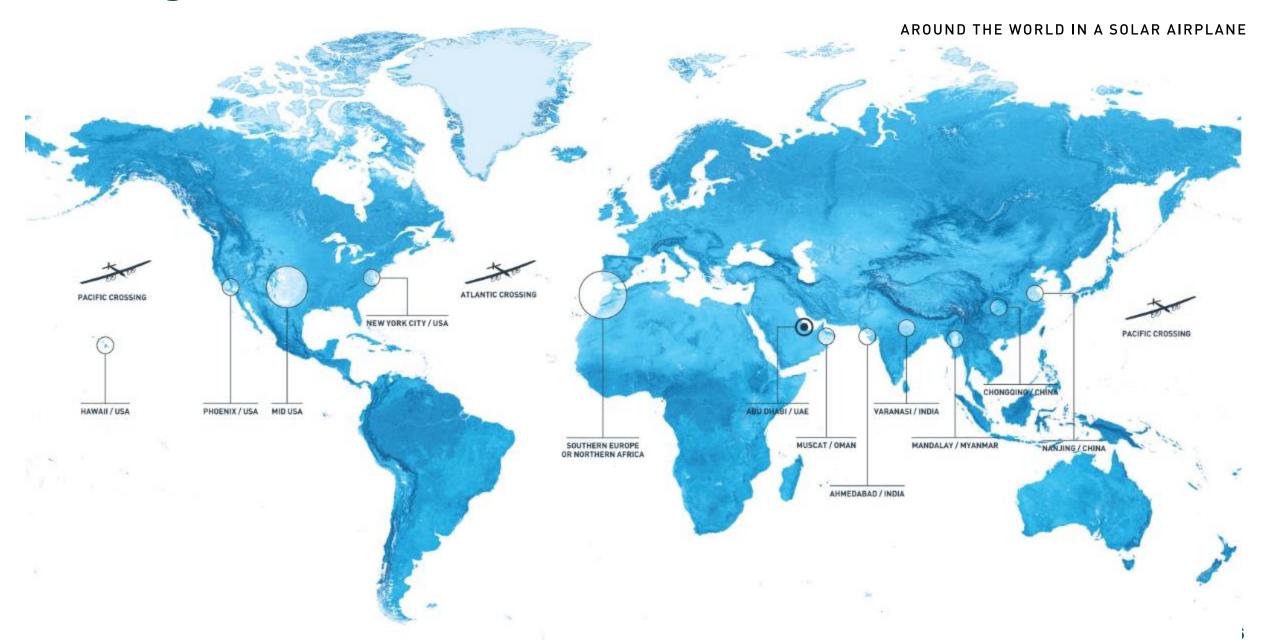


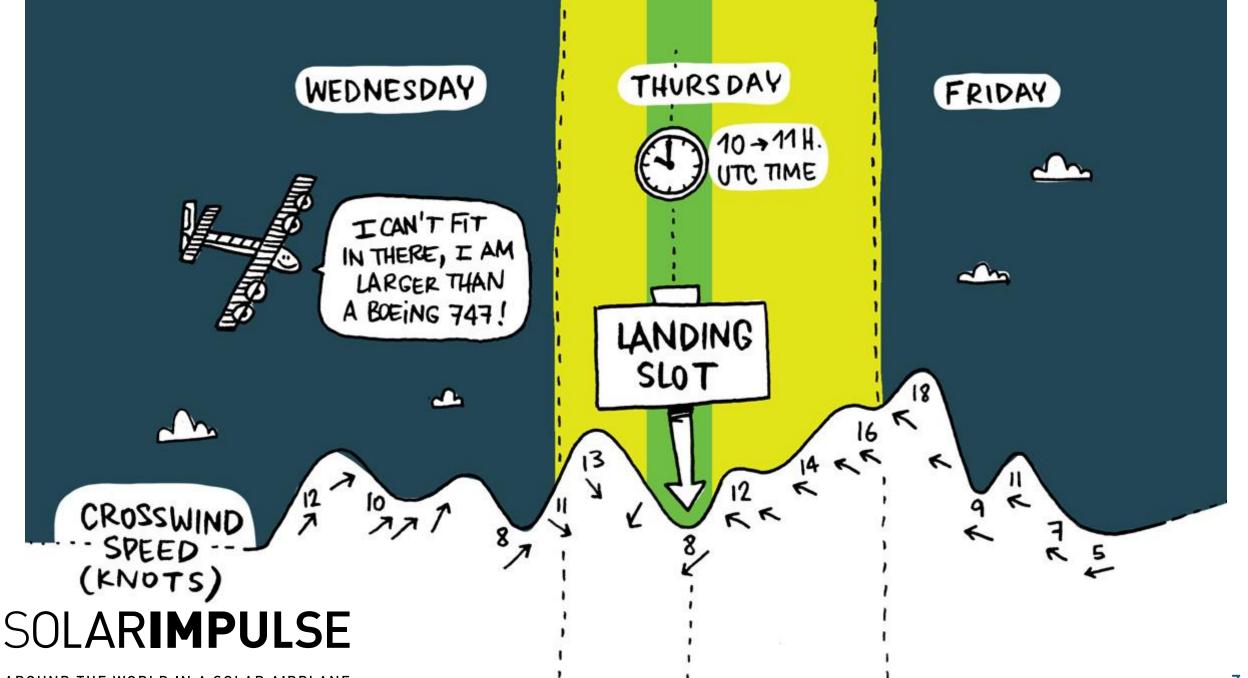




Challenges and Achievements

SOLARIMPULSE





P 1075 KM,

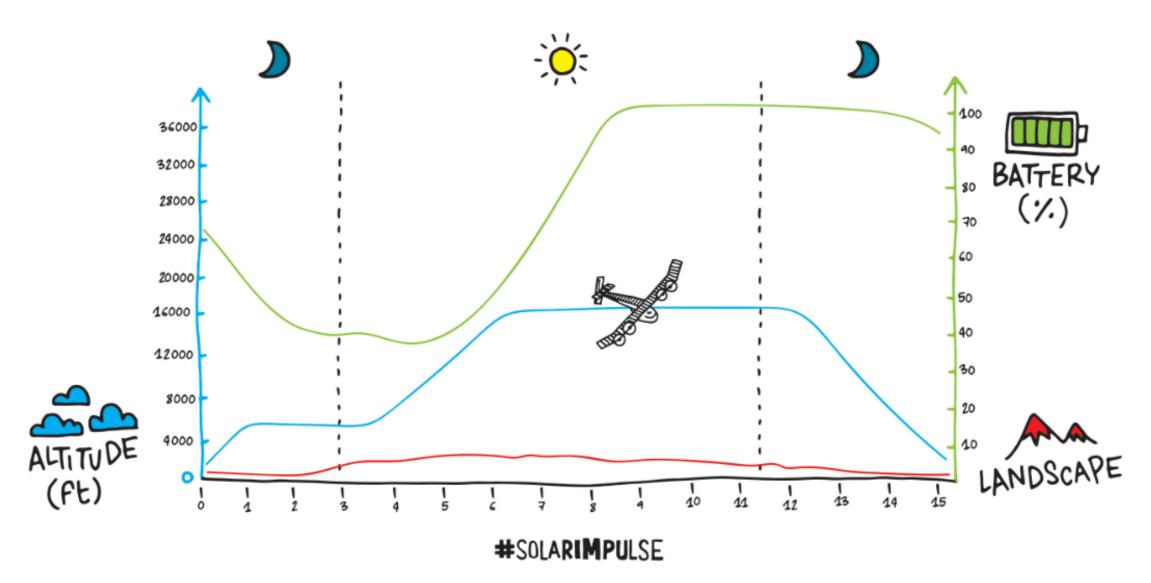


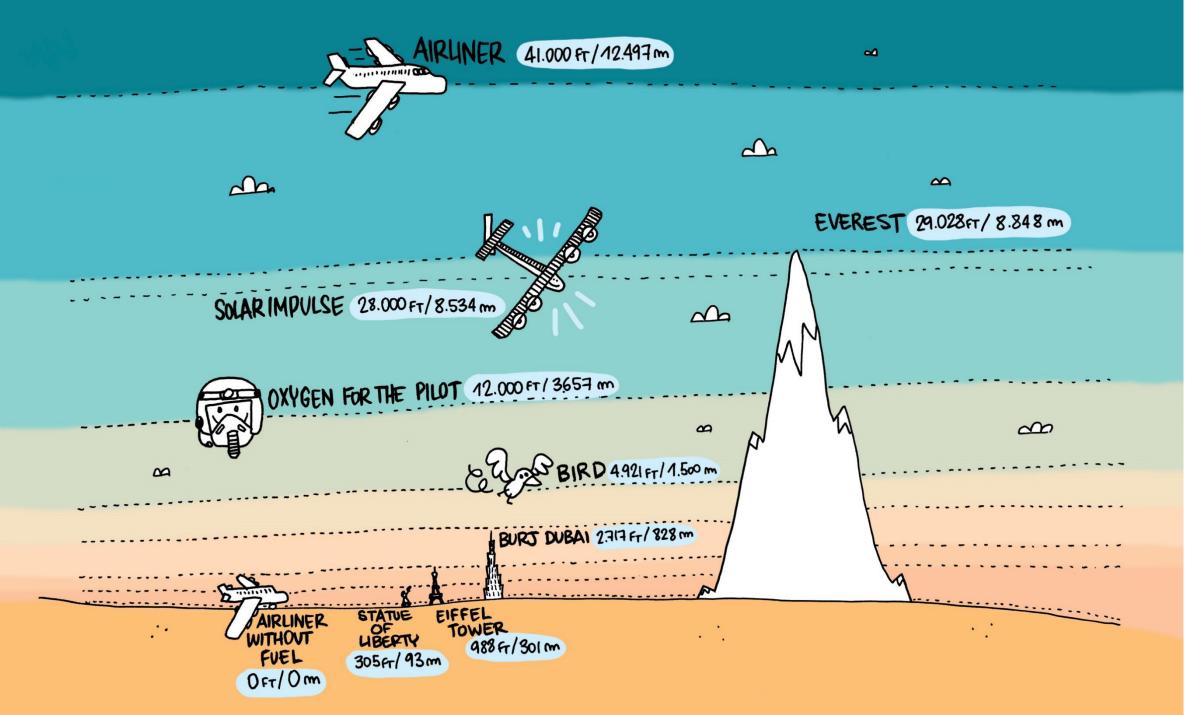
SOLARIMPULSE

AROUND THE WORLD IN A SOLAR AIRPLANE

AHMEDABAD

VARANASI





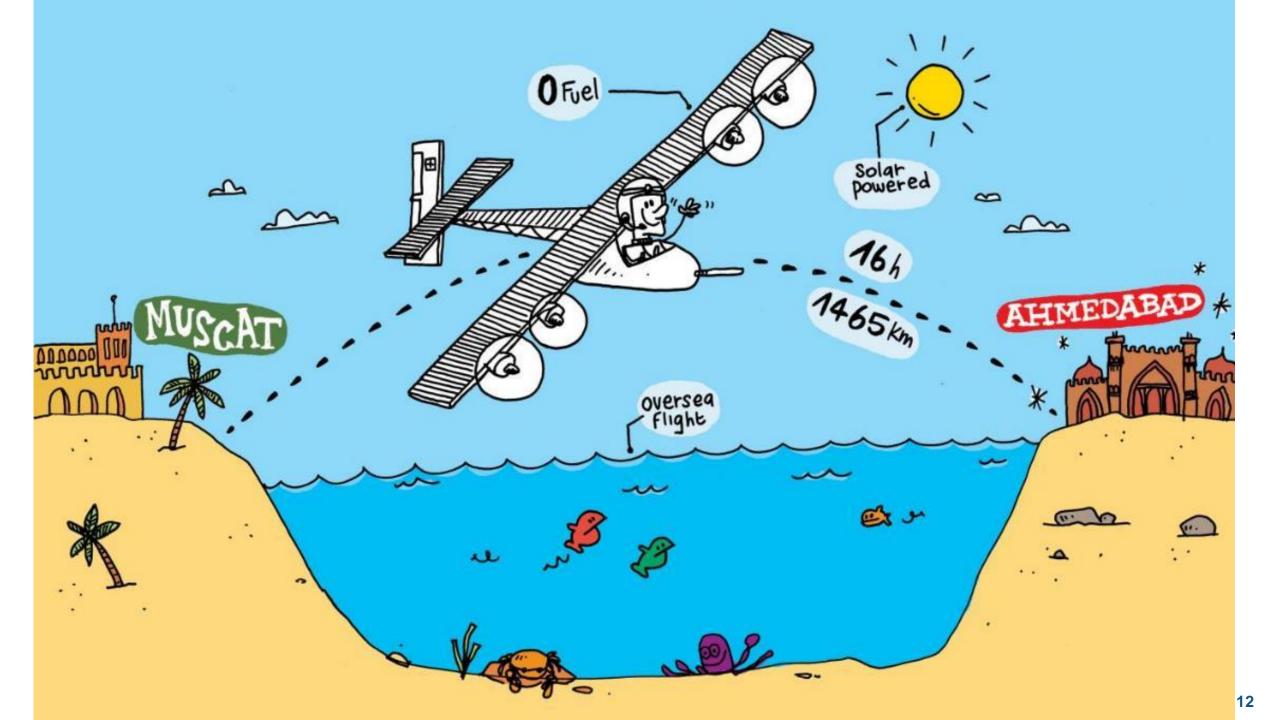


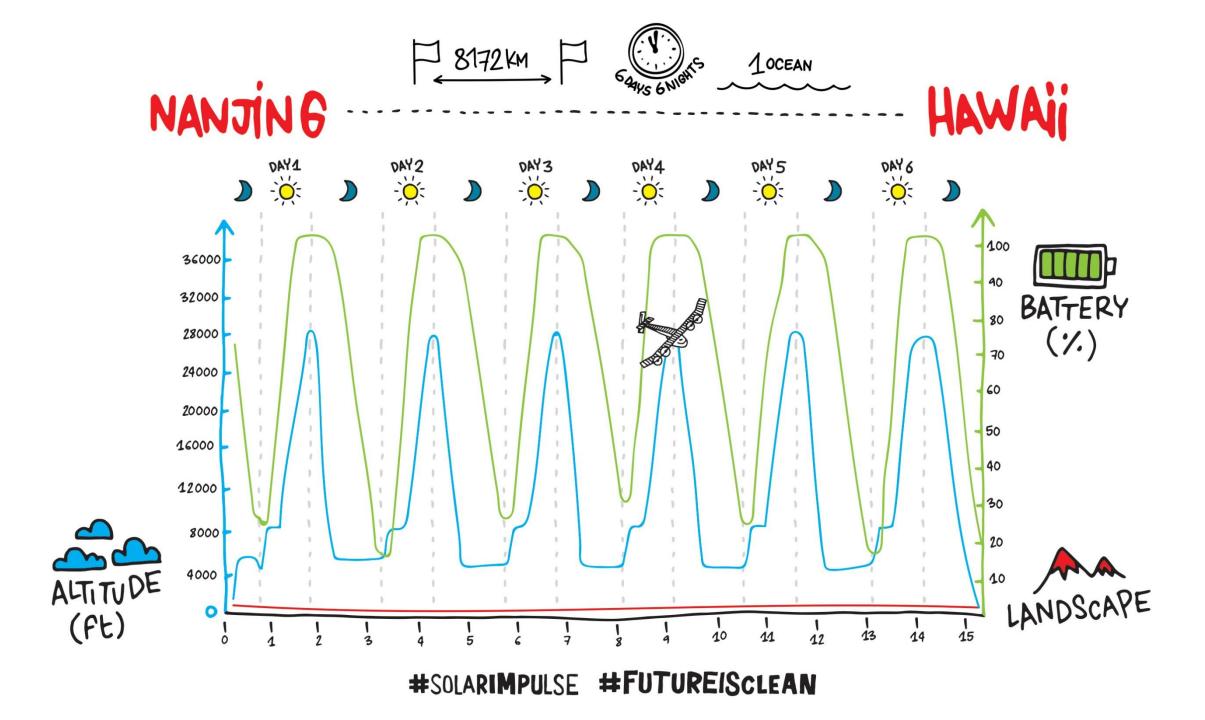












AROUND THE WORLD IN A SOLAR AIRPLANE

TIMELINE

December 2009 - The flea hop 2011 European Solar Flights

2013 – Across America Summer 2014 –Test Flights

















July 2010 Solar Impulse Night Flight

2012 – Crossing Frontiers

April 2014 – Unveiling Solar Impulse 2

2015 The Round-The-World Solar Flight



Model-Based Design of the Aircraft

SOLAR**IMPULSE**

AROUND THE WORLD IN A SOLAR AIRPLANE Tail Sizing, Fuselage Shape Wing Dihedral, **Ailerons** Autopilot, Avionics, Inertial Platform Engine (Automatically Generated Code) **Position**









Combined 72h Mission and Flight Simulation 2012 and 2013

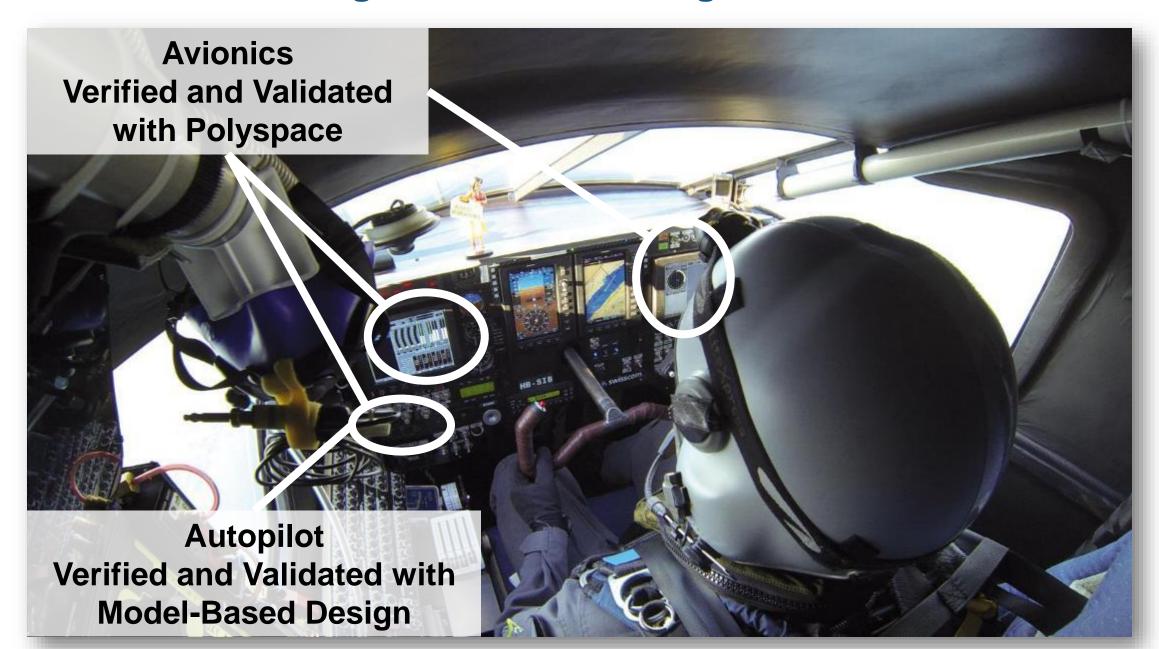








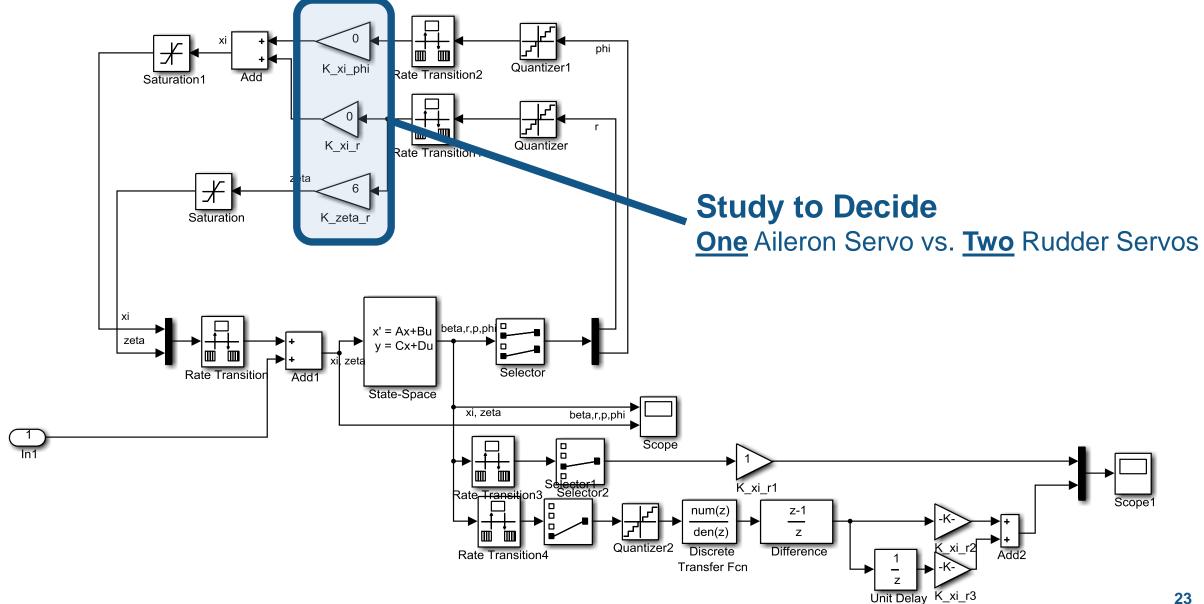
How did we Leverage MathWorks Design Flows



Autopilot (Basic Loop) in Simulink

SOLAR**IMPULSE**

AROUND THE WORLD IN A SOLAR AIRPLANE



Formal Analysis of Avionic Software to DO-178B

AROUND THE WORLD IN A SOLAR AIRPLANE

applying Polyspace Bug Finder and Code Prover

- > 290k Lines of Code
- Power Management / Mission Information Computer
 - → QNX on COTS Board (x86, 32 Bit, 500 MHz, UNIX RTOS)
- Throttle Box, Air Data Computer, Independent Display
 - → ATMEL on SI Boards (ATCAN90, 8 Bit, 8 MHz, No OS)
- Monitoring and Alert System
 - → ARM on ALTRAN Board (Cortex-M4F, 32 Bit, 168 MHz, No OS)



Formal Analysis of Avionic Software to DO-178B

AROUND THE WORLD IN A SOLAR AIRPLANE

applying Polyspace Bug Finder and Code Prover

- Latent bug or defect hunting, e.g. incorrect temperature in throttle box
- No test cases or compilation needed

```
101
               // Enabled ADC
                                                                                       // Clear Status Trig.
              ADCSRA |= (1<<ADEN);
 102 M
                                                                                       // Start ADC
          // --- wait stabilizes Aref rising level after Enable
 103
                                                                                       ADCSRA |= (1<<ADSC);
 104 M
          for (i=0; i<(1<<(ADC WAIT))>>2; i++) asm("nop");
                                                                                       while(((*(volatile uint8 t *)(0x7A)) & (1<<6)) == 1);
 105
              // Clear Status Trig.
                                                                                      // Clear Status Trig.
              // Start ADC
                                                                                      // Start ADC
 108 M
              ADCSRA \mid = (1 << ADSC);
                                                                                      ADCSRA |= (1<<ADSC);
 109 M
              while((ADCSRA & (1<<ADSC)) == 1);
                                                                                      while(((*(volatile uint8 t *)(0x7A)) & (1<<6)) == 1);
                                                                                            Probable cause for 'Dead code':
                                                                                     single c
                                                                                                        while((ADCSRA & (1<<ADSC)) == 1);
                                                                                      5RA &= ^
                                                                                                                               Press 'F2' for focus
                                                                                      ADCSRA
                                                                                                  (ICCHDUC),
While ((ADCSRA & (1<<ADSC)
```

Formal Analysis of Avionic Software to DO-178B

AROUND THE WORLD IN A SOLAR AIRPLANE

applying Polyspace Bug Finder and Code Prover

- Independent, systematic code reviews, compliance to MISRA-C
- Complexity results to support DO-178B "simple system" argument for case where we had to "re-engineer" design assurance level equivalence
- Bug Finder and Code Prover provided 1-2 Man-Year savings and automated capability in parallel to development which were not available otherwise

Concluding Remarks

AROUND THE WORLD IN A SOLAR AIRPLANE

Model-Based Design with MATLAB and Simulink helps us

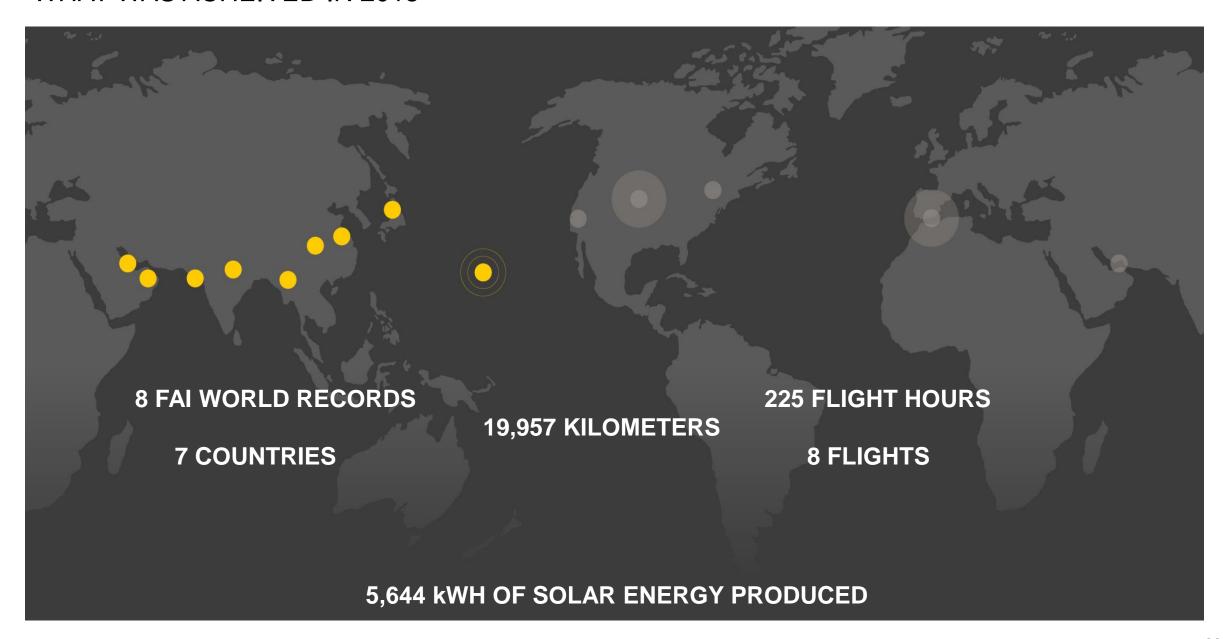
- Reuse, build, test and fly whilst exploring new ideas and concepts
- Make key design decisions early, saving time and avoiding manually coded errors
- Focus on design and development instead of low-level coding
- Understand the system and its interdependencies
- Validate and verify the final performance including pilot training
- Adapt to new situations in pre- and during- flight

Using Polyspace code verifiers

- Identified and fixed potential run-time errors and unsafe code
- Reliably analyzed C codebase early, without test cases and compilation!



WHAT WAS ACHEIVED IN 2015



WHAT IS PLANNED FOR 2016





