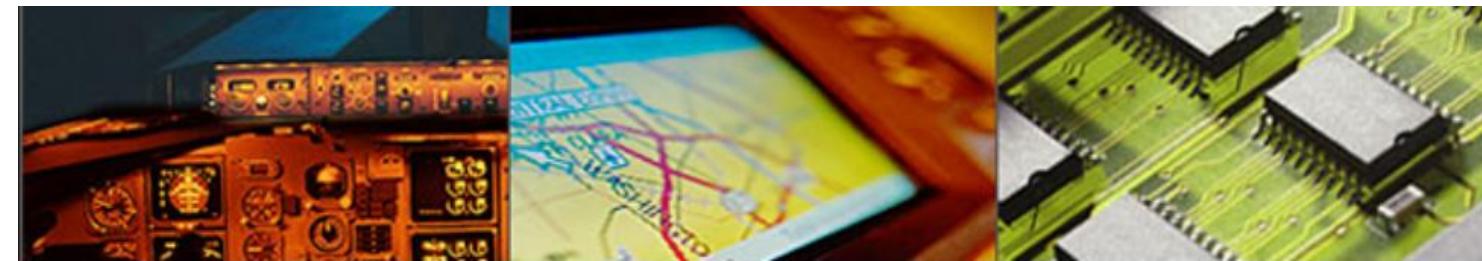


Increasing Design Confidence

Model and Code Verification



The Cost of Failure...

Ariane 5



\$7,500,000,000

Rocket & payload lost

The Cost of Failure...

USS Yorktown



0 Knots

Top speed

The Cost of Failure...

Therac-25



6 Casualties

due to radiation overdose

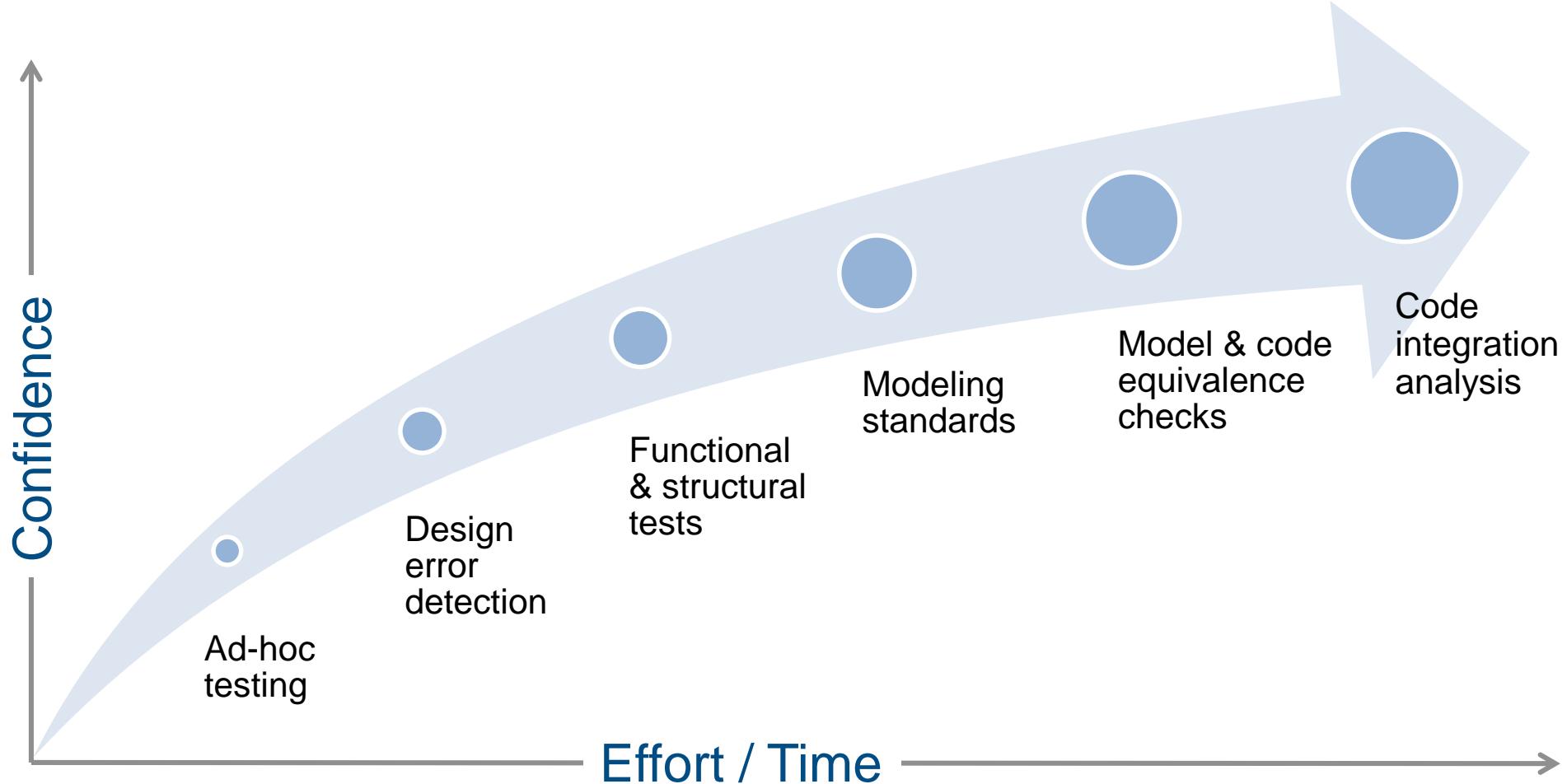
Motivation

It is easier and less expensive to fix design errors early in the process when they happen.

Model-Based Design enables:

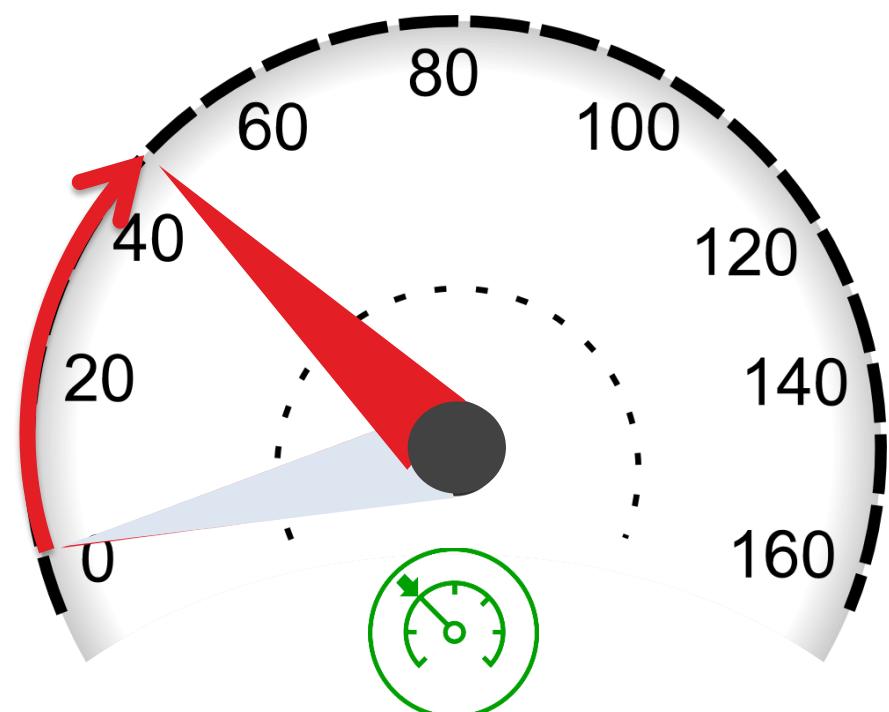
1. *Early testing to increase confidence in your design*
2. *Delivery of higher quality software throughout the workflow*

Gaining Confidence in our Design



Application: Cruise Control

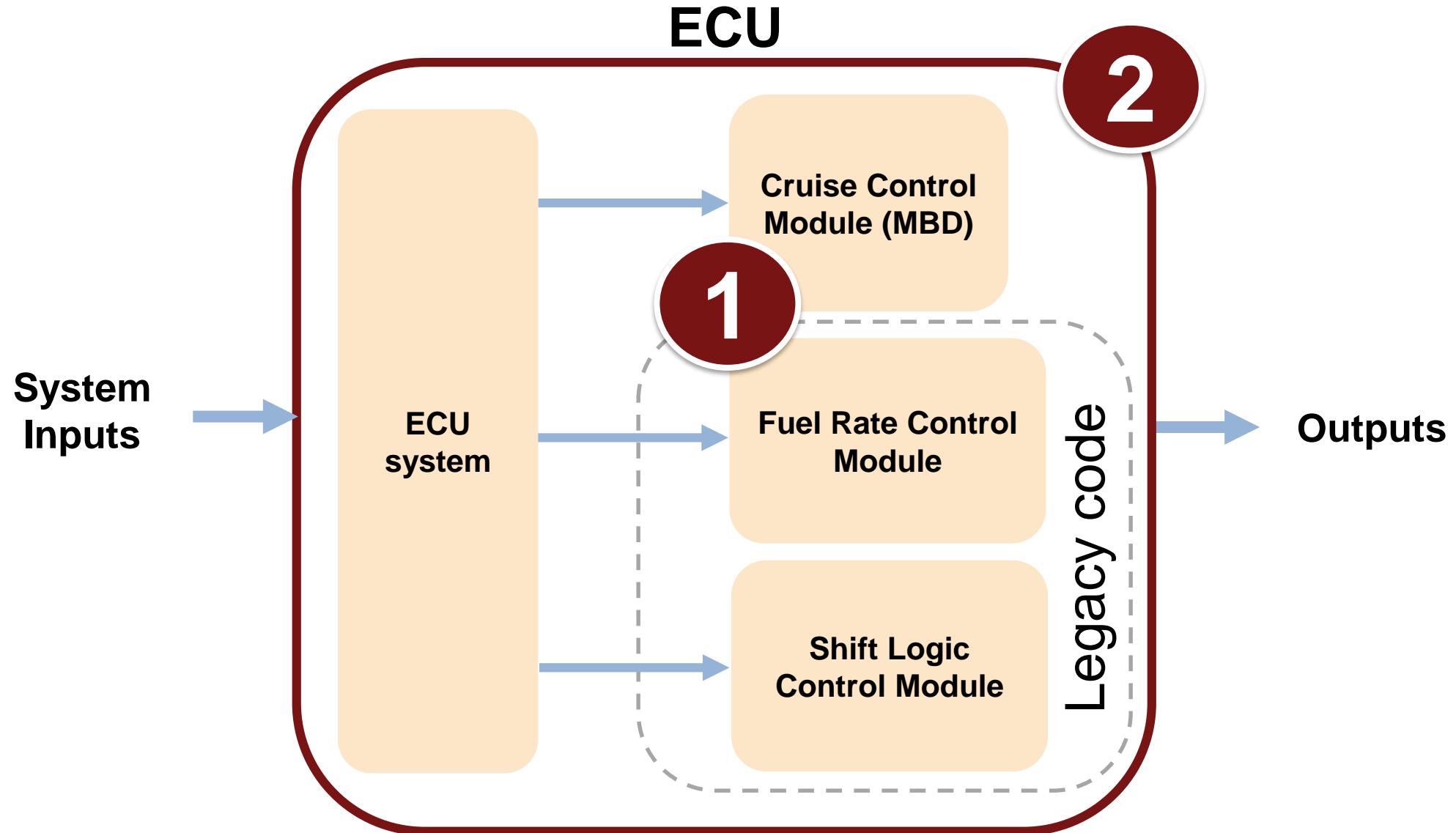
Control speed according to setpoint



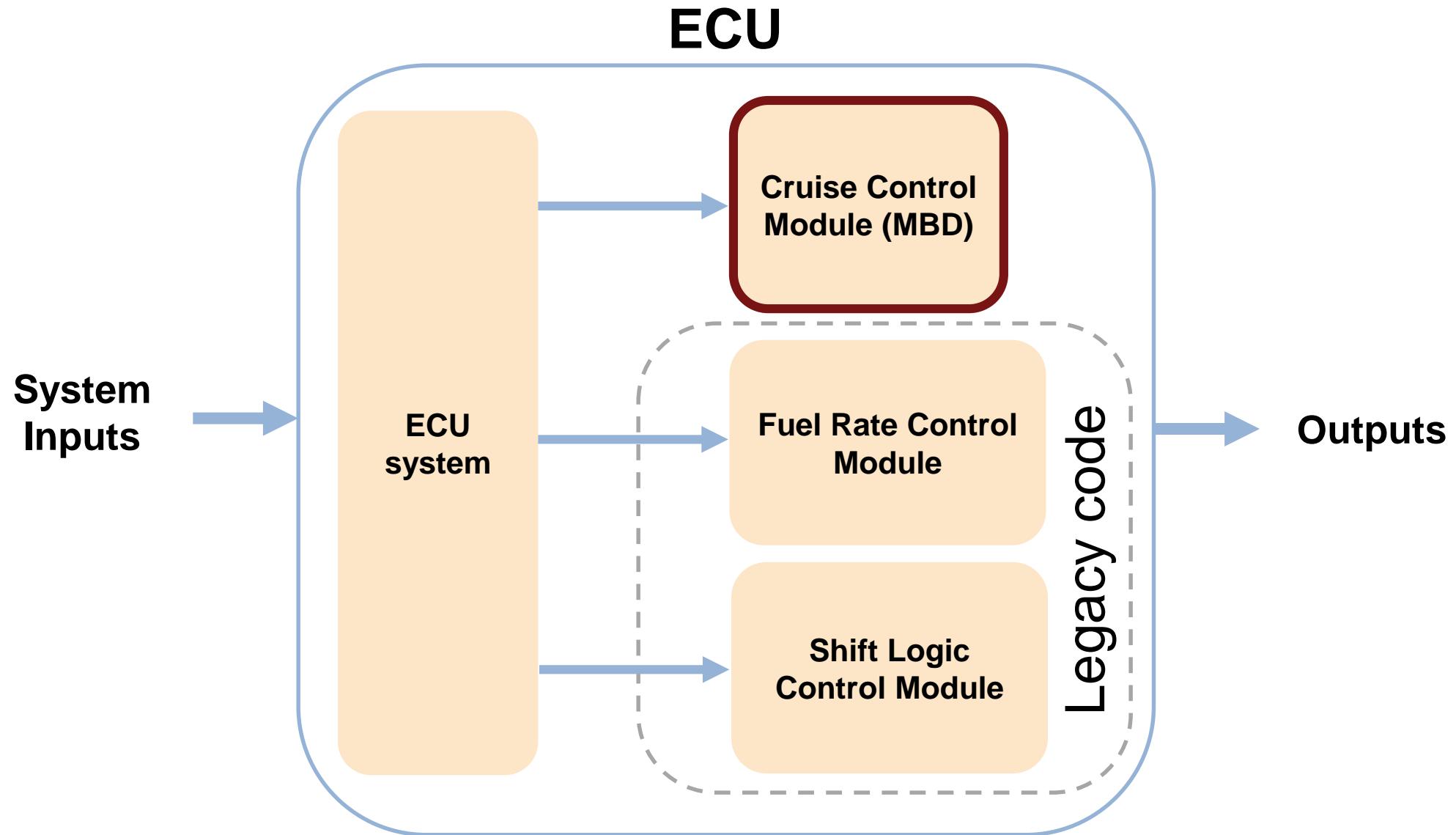
50 km/h



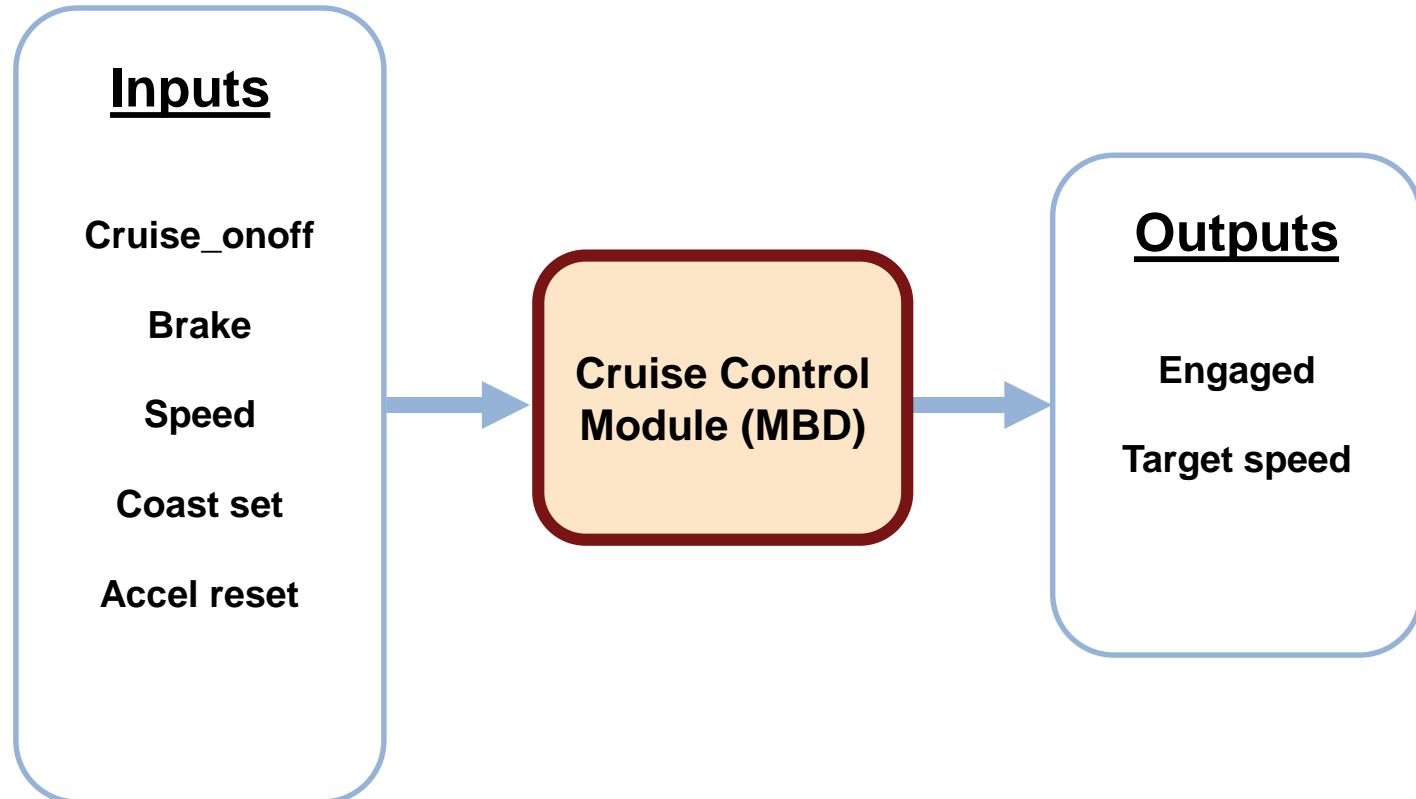
Application: Cruise Control



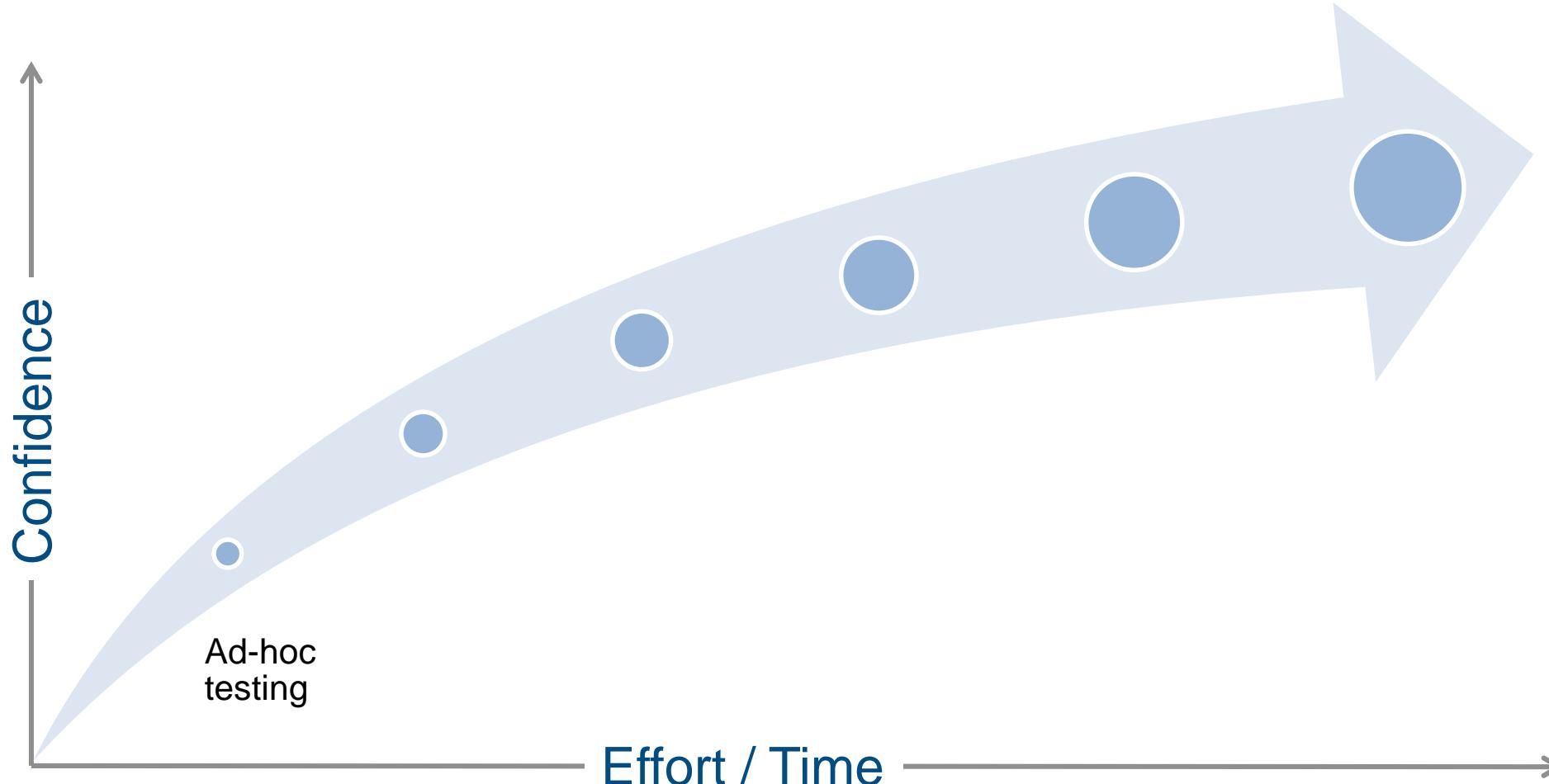
Application: Cruise Control



Application: Cruise Control

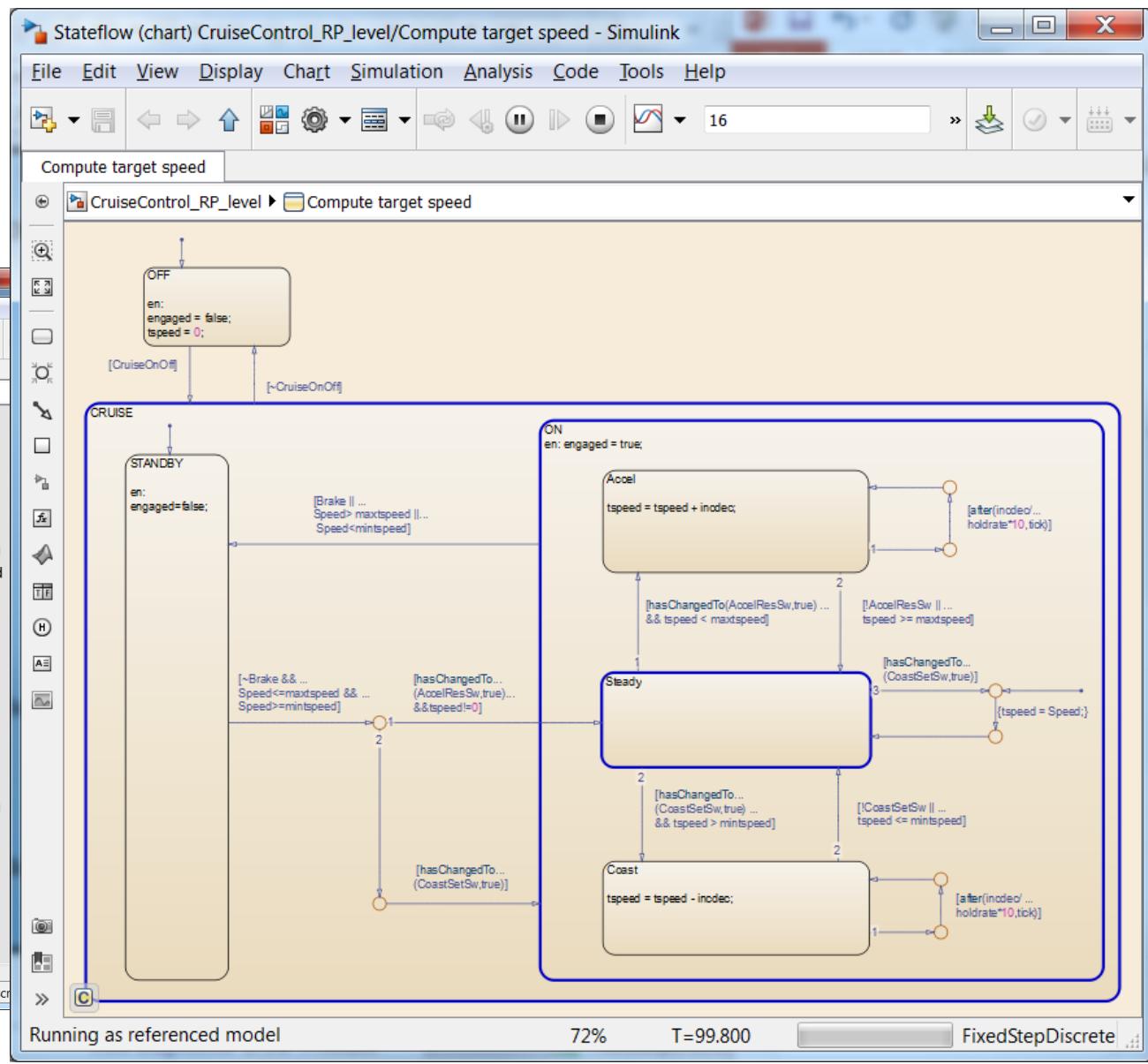
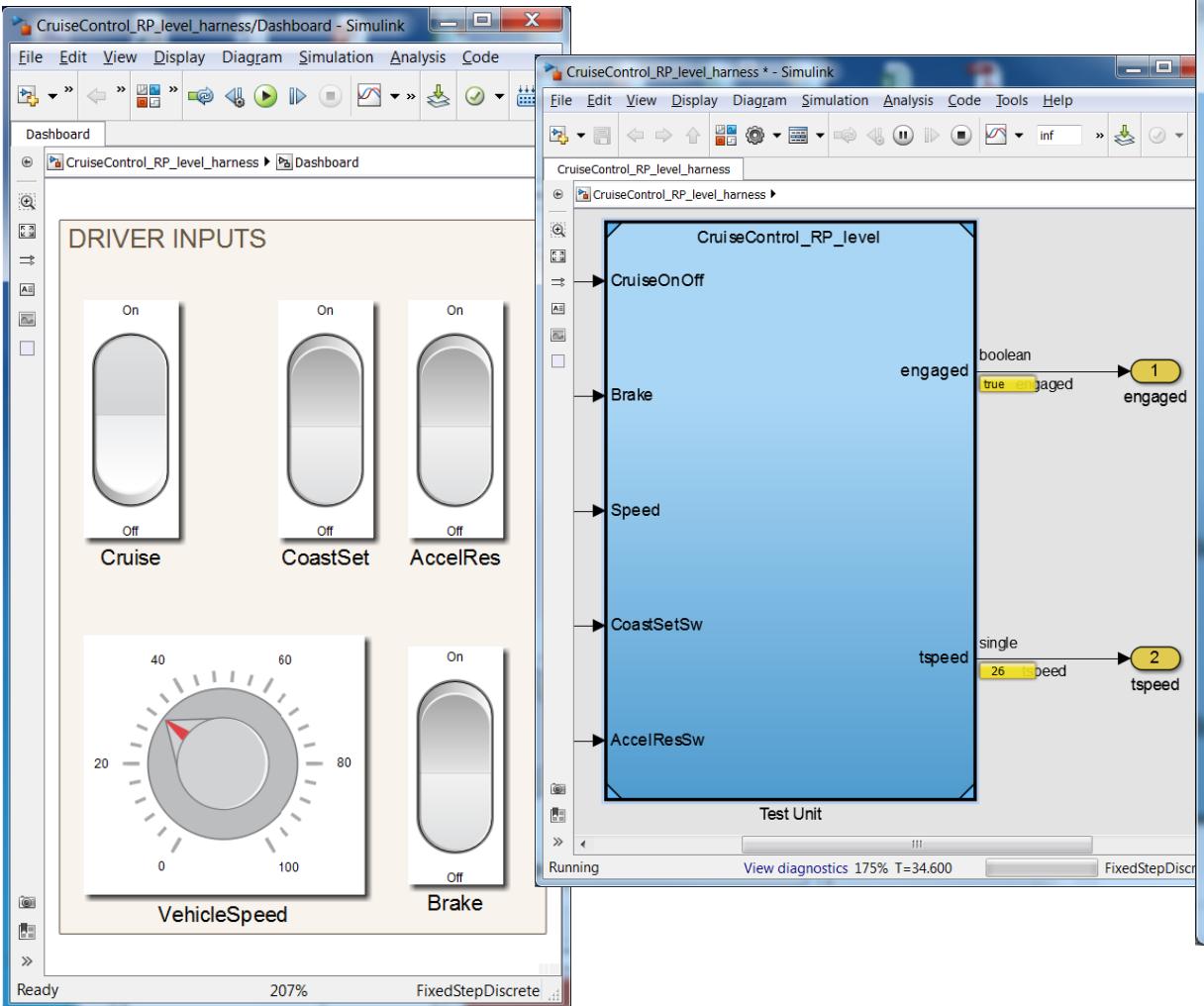


Gaining Confidence in our Design

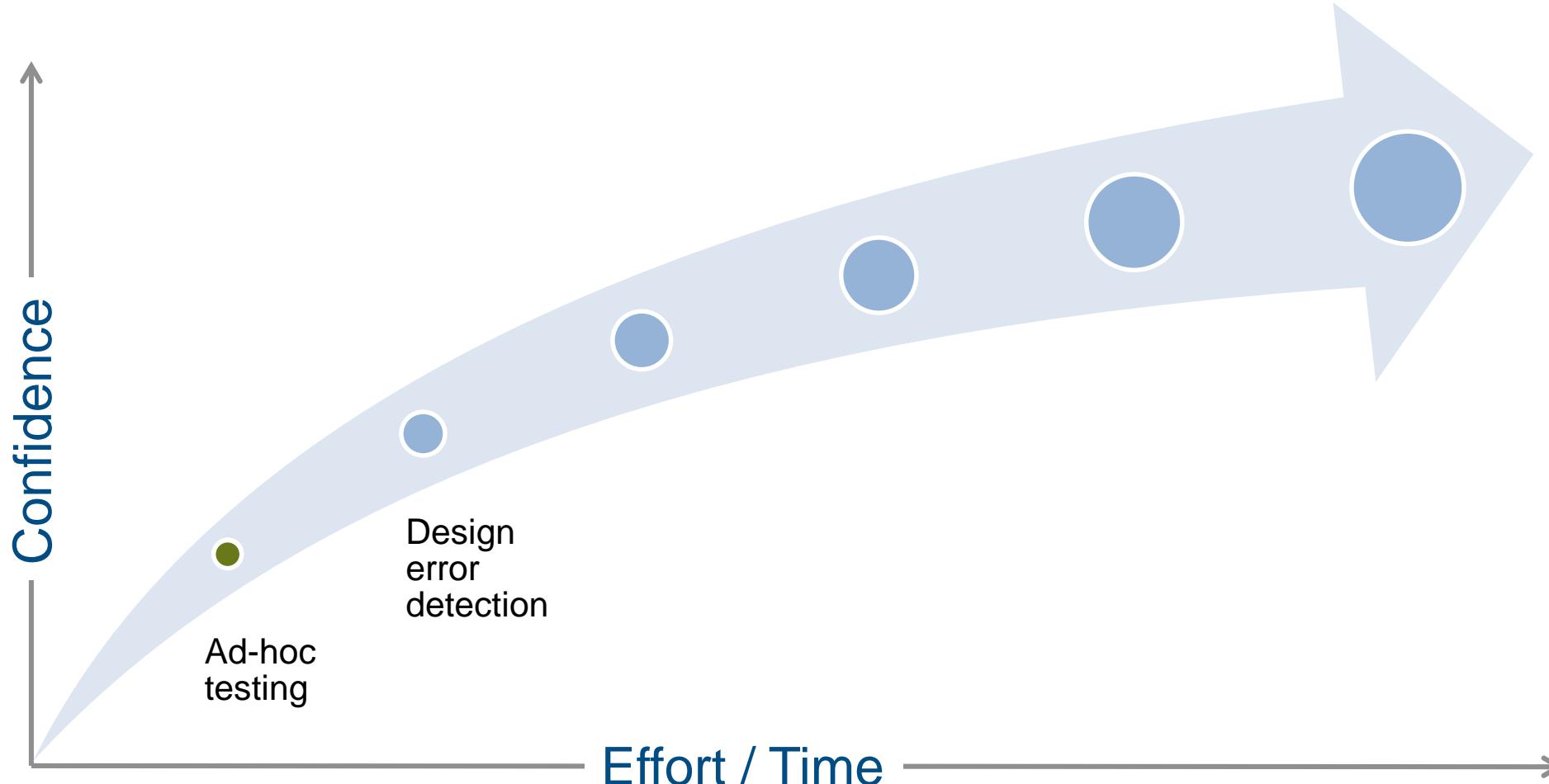


Ad-hoc Tests

Dashboard blocks facilitate early ad-hoc testing



Gaining Confidence in our Design



Finding Design Errors: Dead Logic

The screenshot illustrates the process of identifying dead logic in a Stateflow chart using Simulink Design Verifier.

Configuration Parameters: The "Design Error Detection" section is configured to detect Dead logic and Identify active logic.

Simulink Design Verifier Results: The results show that 2/70 objectives are dead logic, and 68/70 objectives are active logic.

Stateflow Chart: The chart shows a state transition from OFF to CRUISE. A transition from CRUISE to STANDBY is triggered by the expression `[after(incdec/holdrate... *10,tick)]`. This expression is highlighted in red and labeled as "DEAD LOGIC". Another transition from CRUISE to STANDBY is triggered by the expression `[later(incdec/holdrate... *10,tick)]`, which is highlighted in green and labeled as "ACTIVE LOGIC".

Annotations: Red boxes highlight the "DEAD LOGIC" and "ACTIVE LOGIC" annotations on the Stateflow chart.

Finding Unintended Behavior

Command Window

```

debug>> incdec
incdec =
    1

debug>> holdrate
holdrate =
    5

debug>> class(incdec)
ans =
uint8

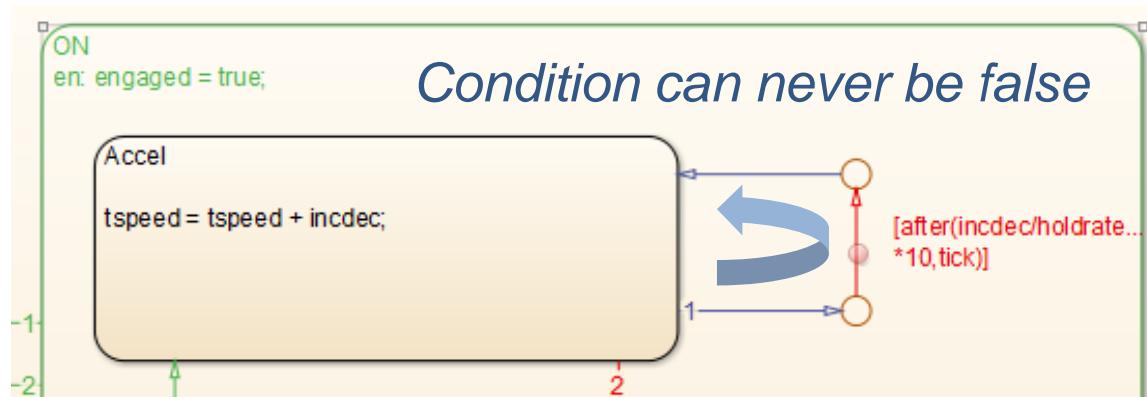
debug>> class(holdrate)
ans =
uint8

debug>> incdec/holdrate*10
ans =
    0

debug>> 10*incdec/holdrate
ans =
    2

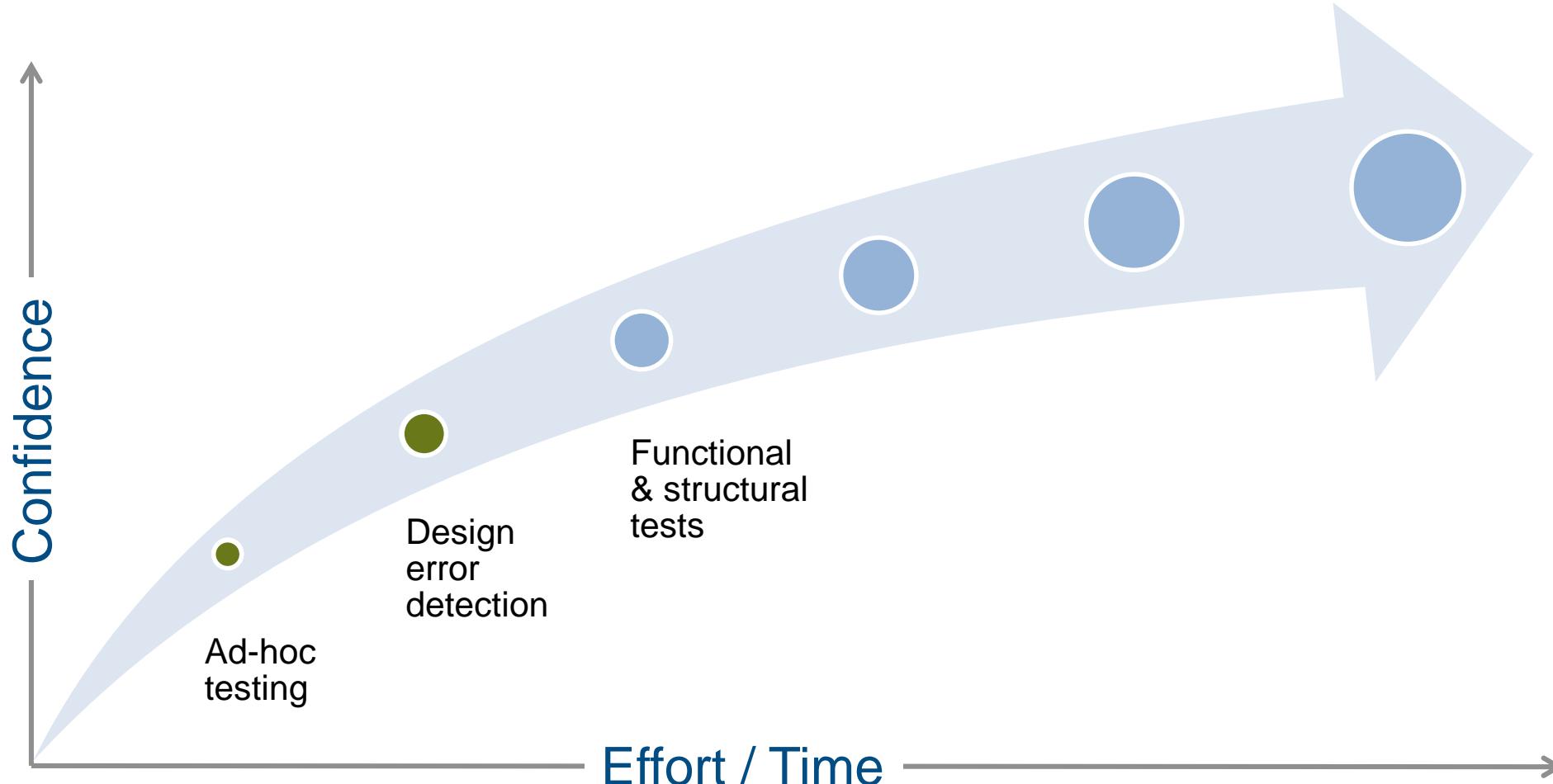
```

fx debug>>



- Dead logic due to “`uint8`” operation on `incdec/holdrate*10`
- Fix change the order of operation `10*incdec/holdrate`

Gaining Confidence in our Design



Simulation Testing Workflow

Requirements

2. Functional Requirements

2.1.1. Disabled (off) during start-up and not engaged (inactive)

Initial state of cruise control system shall be disabled.

2.1.2. Not engaged (inactive) with enabling (on)

The cruise control system shall not be initialized with enable.

2.1.3. Disengaged (not active) when

The cruise control system shall disengage when

2.1.4. Initial transition from disengaged

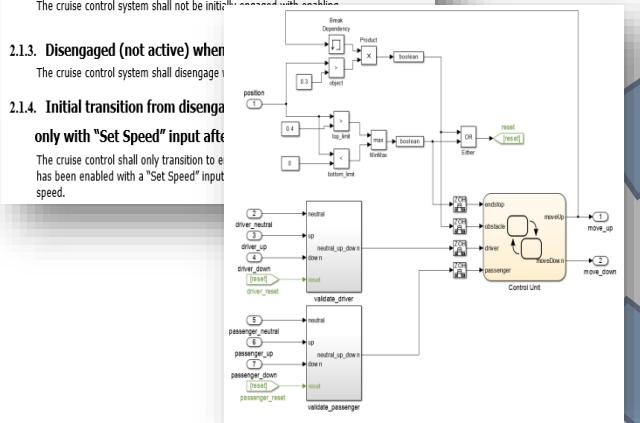
only with "Set Speed" input after

The cruise control shall only transition to e

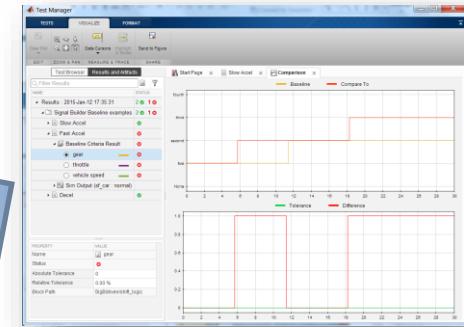
has been enabled with a "Set Speed" input

speed.

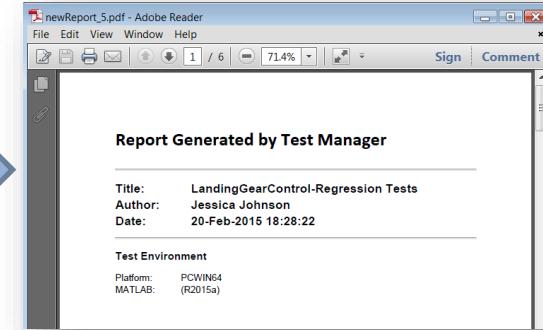
Design



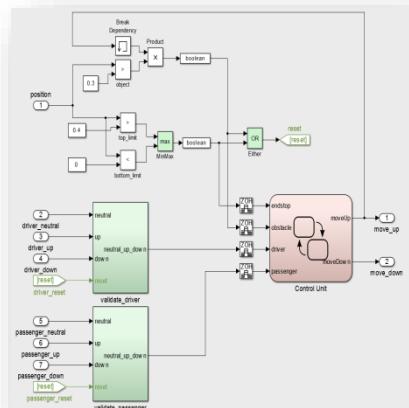
Did we meet requirements?



Review functional behavior



Did we completely test our model?



Functional
Structural

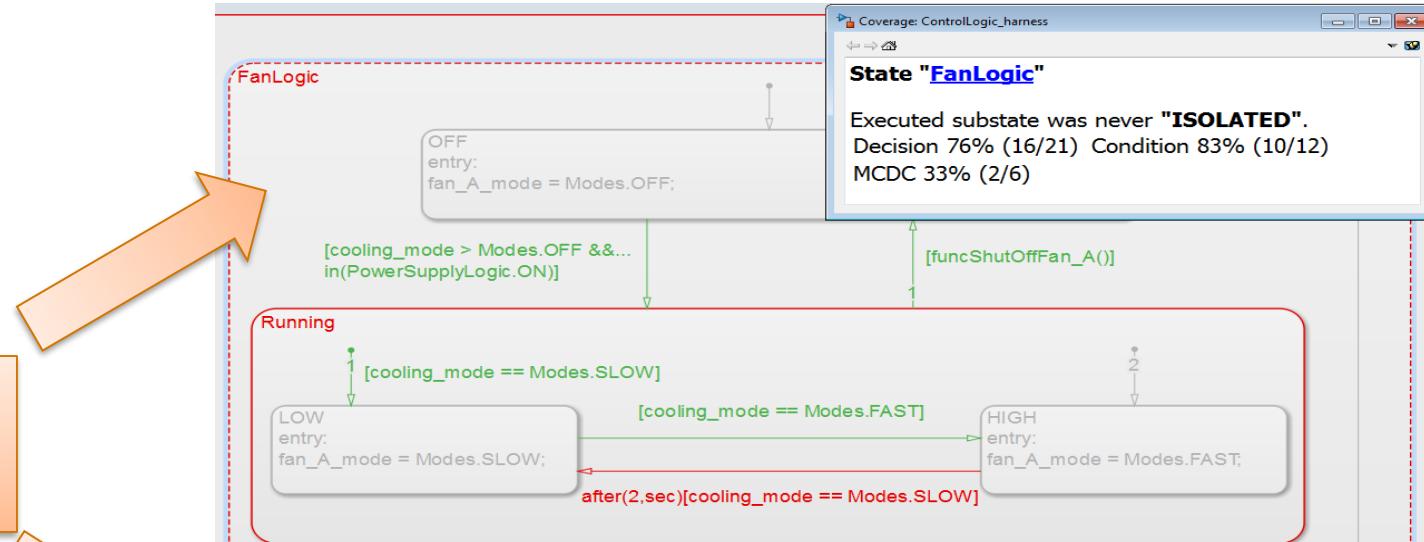
Structural coverage report

	D1	C1	MCDC
1. slvndemo_powerwindow_controller	58 100%	80% ■ 60% ■	60% ■
2.control	57 100%	80% ■ 60% ■	60% ■
3.SF:control	56 100%	80% ■ 60% ■	60% ■
4.SF:safe	52 100%	75% ■ 50% ■	50% ■
5.SF:driverDown	7 100%	75% ■ 50% ■	50% ■
6.SF:driverNeutral	26 100%	75% ■ 50% ■	50% ■
7.SF:passengerDown	7 100%	75% ■ 50% ■	50% ■
8.SF:passengerUp	7 100%	75% ■ 50% ■	50% ■
9.SF:driverUp	7 100%	75% ■ 50% ■	50% ■

Did We Completely Test our Model?

Model Coverage Analysis

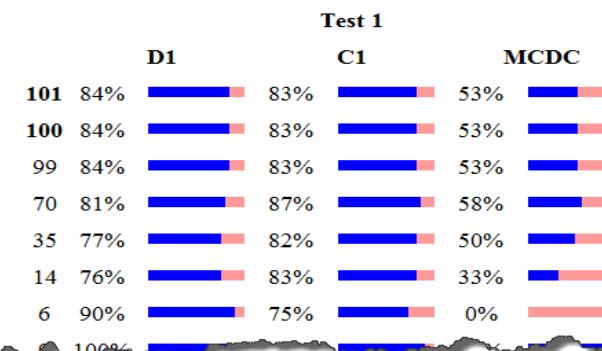
- Potential causes of less than 100% coverage:
- Missing requirements
 - Over-specified design
 - Design errors
 - Missing tests



Summary

Model Hierarchy/Complexity:

1. [ControlLogic_modelingcompleted](#)
2. [ModeLogic](#)
3. [SF: ModeLogic](#)
4. [SF: CoolingSystemLogic](#)
5. [SF: CoolingSystemA](#)
6. [SF: FanLogic](#)
7. [SF: Running](#)



Requirements Based Functional Testing with Coverage Analysis

All 14 requirements based test cases pass

By analyzing model coverage results we find:

- Missing test cases for vehicle speed exit conditions, and
- Missing requirements (and test cases) for “hold” or continuous speed button input

The screenshot displays three main windows:

- Test Manager**: Shows a list of test cases. The "tspeed" case is selected, indicated by a blue border. Other cases listed include AccelResSw, Brake, CoastSetSw, CruiseOnOff, and Speed.
- Stateflow (chart) CruiseControl/Compute target speed - Simulink**: A Stateflow chart titled "Compute target speed". It features three states: ON, Steady, and Coast. Transitions are triggered by events like "en: engaged = true;" (from Ready to ON), "hasChangedTo(AccelResSw,true) && tspeed < maxtspeed" (between ON and Accelerate), and "hasChangedTo(CoastSetSw,true)" (between Steady and Coast). Decision points are marked with green numbers (1, 2, 3) and red numbers (1, 2, 3).
- Coverage: CruiseControl_Harness...**: A coverage report for the "Compute target speed" subsystem. It shows coverage statistics: Decision 82% (31/38), Condition 69% (22/32), and MCDC 50% (8/16).

Functional Testing with Added Requirements & Test Cases

The screenshot illustrates the integration of functional testing with Stateflow and Simulink. On the left, the **Test Manager** shows a list of test cases for the **02_Functional_Baseline_EExcel_Fu** project, with 19 tests marked as green (passed). A plot window displays the vehicle speed over time, showing a constant value of approximately 10 units. Below it, the **Coverage: CruiseControl_Harness...** window shows full coverage for the **Compute target speed** chart.

The central part of the interface is the **Stateflow (chart) CruiseControl/Compute target speed - Simulink** window. It displays a Stateflow chart with three main states: OFF, CRUISE, and ON. The OFF state has initial assignments: `en: engaged = false;` and `tspeed = 0;`. Transitions from OFF to CRUISE and OFF to ON are triggered by [CruiseOnOff]. The CRUISE state contains a STANDBY state. Transitions from CRUISE to ON and CRUISE to STANDBY are triggered by [Brake]. The ON state contains three substates: Accel, Steady, and Coast. Transitions between these states are triggered by events such as [hasChangedTo(AccelResSw,true)], [hasChangedTo(CoastSetSw,true)], and [hasChangedTo(CoastSw,true)]. The chart also includes guard conditions like `[~Brake && Speed<maxspeed && Speed>minspeed]` and `[~isSpeed=0]`.

Annotations on the right side of the Stateflow chart highlight specific requirements:

- Top Annotation:** `![AccelResSw || ... tspeed >= maxspeed]`
- Bottom Annotation:** `![CoastSetSw || ... tspeed <= minspeed]`

The bottom status bar indicates the chart is **Ready** at 80% completion with a **FixedStepDiscrete** step type.

Functional Testing with Added Requirements & Test Cases

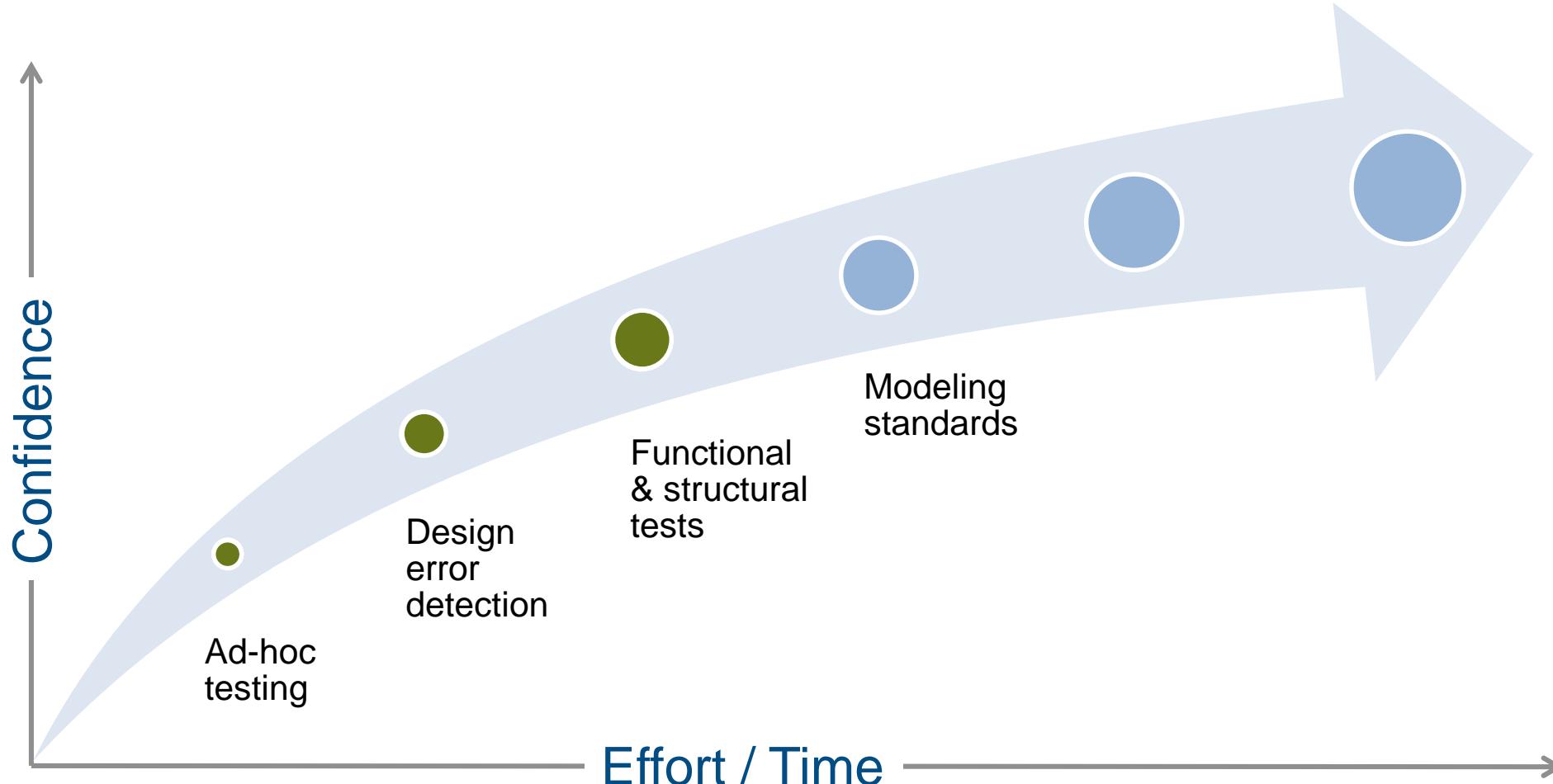
Test Manager

Stateflow (chart) CruiseControl/Compute target speed - Simulink

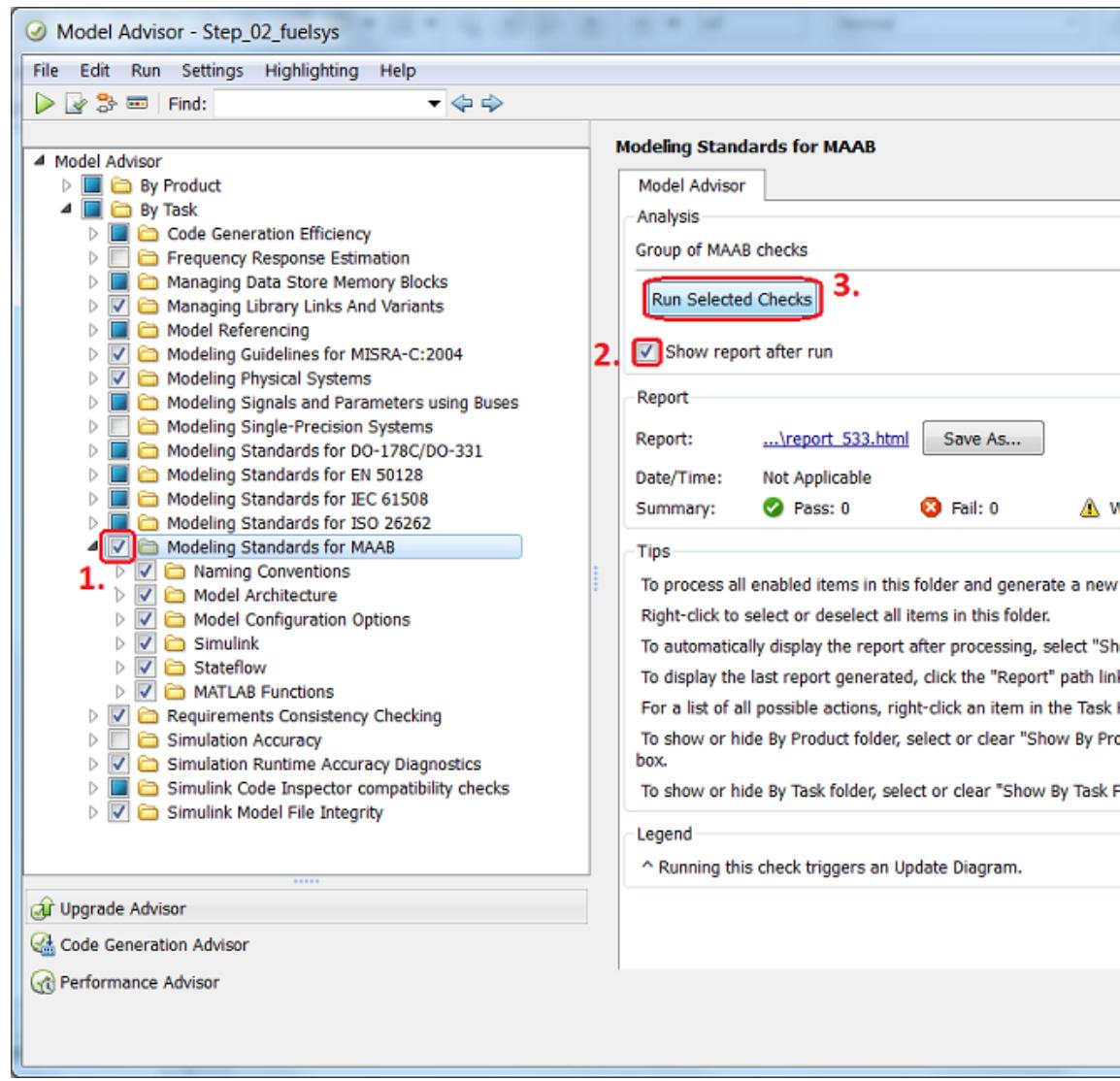
- Added 2 new requirements for the “hold” case for speed setting input buttons
- Added 5 test cases to the original 14 requirements based test cases
 - 3 test cases for the 2 new requirements
 - 2 test cases for the missing test cases for the vehicle speed exist conditions
- 4/5 new functional test cases pass
 - Failed test case showed overshoot beyond target speed limits
 - Coverage analysis highlighted transitions with design errors
 - Fixed comparison operators, $(<)$ → (\leq) , and $(>)$ → (\geq)
- **Now all (19) functional test cases pass with 100% model coverage!**

The screenshot shows the MATLAB Test Manager window on the left and a Stateflow chart window on the right. The Test Manager displays a 'Test Browser' with a tree view of test cases under 'NAME'. A 'Coverage' tab is also visible. The Stateflow chart titled 'CruiseControl/Compute target speed' shows a state labeled 'Coast' with a transition. The transition condition is annotated with text boxes: '![CoastSetSw || ... tspeed <= mintspeed]' and '![CoastSetSw || ... tspeed > mintspeed]'. The chart also includes annotations for 'hasChangedTo(CoastSetSw,true)' and 'tspeed = tspeed - indecq'. The overall status is 'Full Coverage'.

Gaining Confidence in our Design



Model Advisor – Model Standards Checking



The screenshot shows the 'Web Browser - Model Advisor Report for 'Step_02_fuelsys'' window. The title bar indicates the report is for 'Step_02_fuelsys'. The main content area displays the 'Model Advisor Report - Step_02_fuelsys.slx' report. Key information shown includes:

- Simulink version:** 8.2
- System:** Step_02_fuelsys
- Model version:** 1.242
- Current run:** 20-Sep-2013 08:45:50

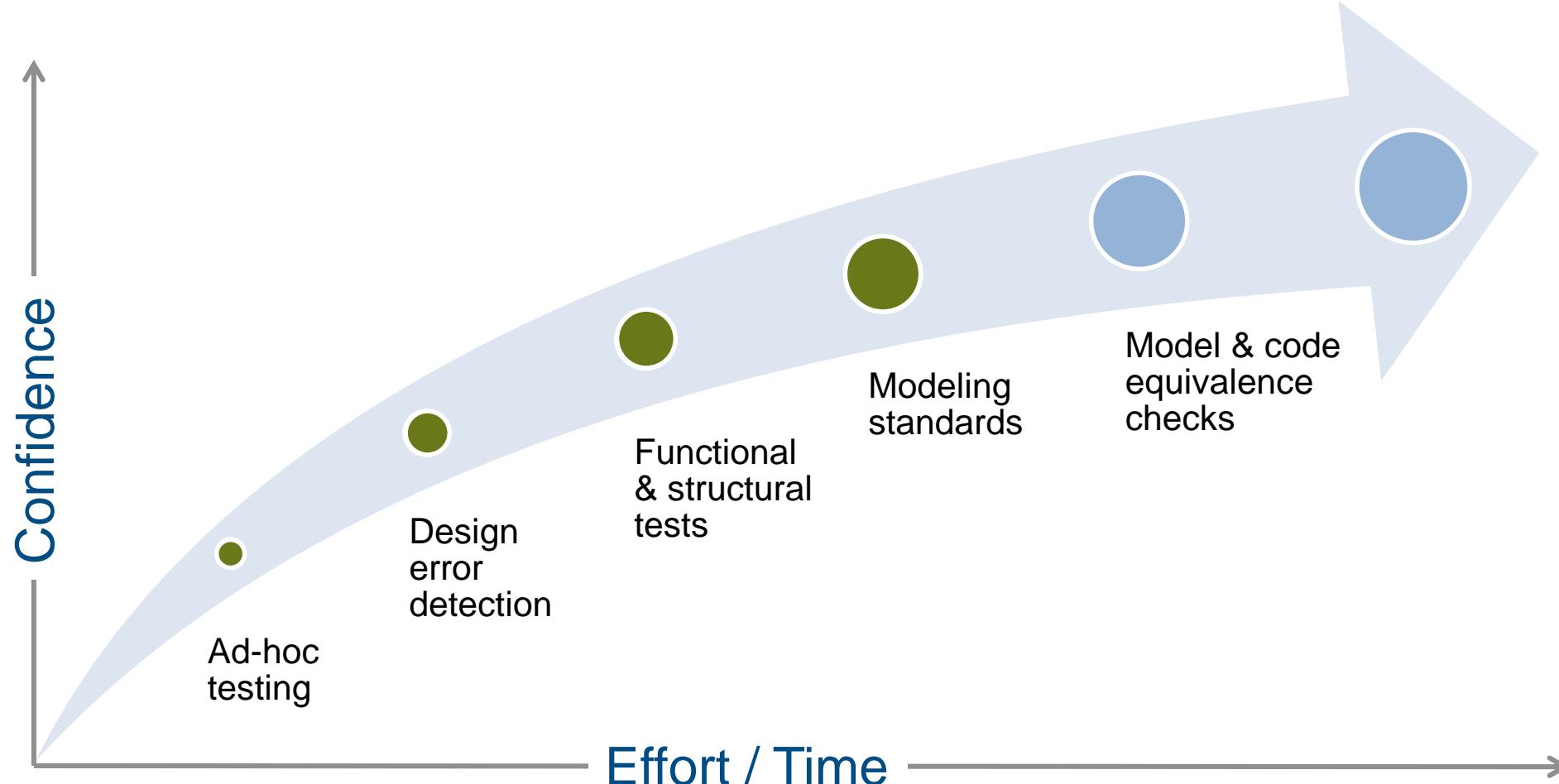
Run Summary table (highlighted with a red box):

	Pass	Fail	Warning	Not Run	Total
Pass	41	0	11	0	52

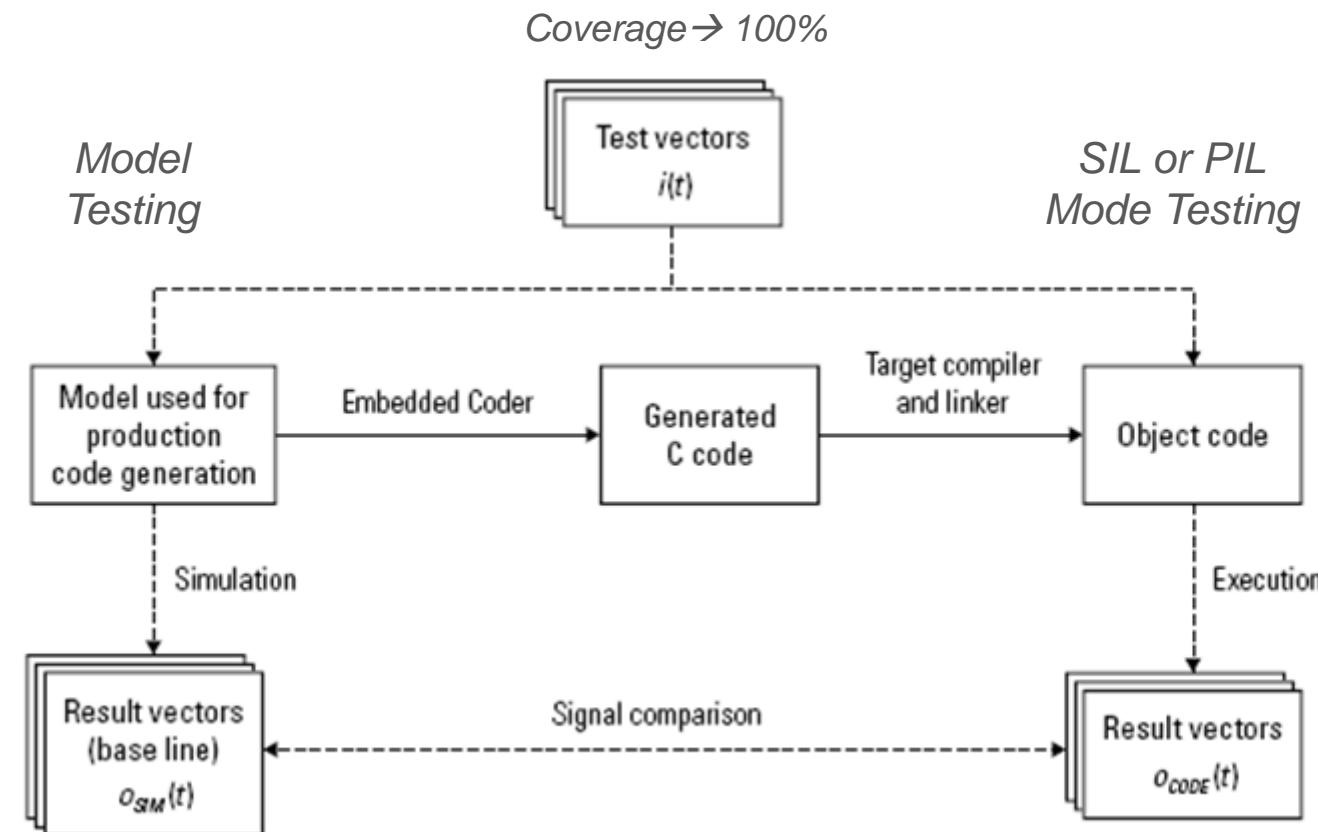
Modeling Standards for MAAB section:

- Naming Conventions** (Passed)
 - Check file names**: Identify file names with incorrect characters or formatting.
 - See Also**: MathWorks Automotive Advisory Board Guideline: ar_0001
 - Passed**: All files have correct names.
- Check folder names**: Identify folders using incorrect characters and formatting.
- See Also**: MathWorks Automotive Advisory Board Guideline: ar_0002

Gaining Confidence in our Design



Equivalence Testing: Model vs SIL or PIL Mode Testing



Code Generation with Model-to-Code Traceability

The screenshot illustrates the integration of model-to-code traceability in a MATLAB/Simulink environment. On the left, the Simulink interface shows a model named "Step_07_logic" containing a subsystem "safety_logic". The "safety_logic" block is highlighted with an orange border and contains four inputs: "throt", "speed", "EGO", and "press". The "speed" input is connected to a self-loop feedback connection. On the right, a "Code Generation Report" window is open, showing the generated C code. A blue box highlights a specific section of the code:

```

    ...
    default:
        localDW->is_Low_Emissions = Step_07_logic_IN_NO_ACTIVE_CHILD;
        break;
    }
    break;

    case Step_07_logic_IN_Rich_Mixture:
        if (localDW->is_Running != Step_07_logic_IN_Rich_Mixture) {
            localDW->is_Running = Step_07_logic_IN_Rich_Mixture;
            localDW->was_Running = Step_07_logic_IN_Rich_Mixture;
        }

        /* Entry 'Rich_Mixture': '<S1>:26'
         * Requirements for Entry 'Rich_Mixture': '<S1>:26':
         * 1. Enriched mixture usage
         */
        rty_fuel_mode = RICH;
    }

    /* Entry Internal 'Rich_Mixture': '<S1>:26'
     * Requirements for Entry Internal 'Rich_Mixture': '<S1>:26':
     * 1. Enriched mixture usage
     */
    localDW->is_Rich_Mixture = Step_07_logic_IN_Single_Failure;
    break;

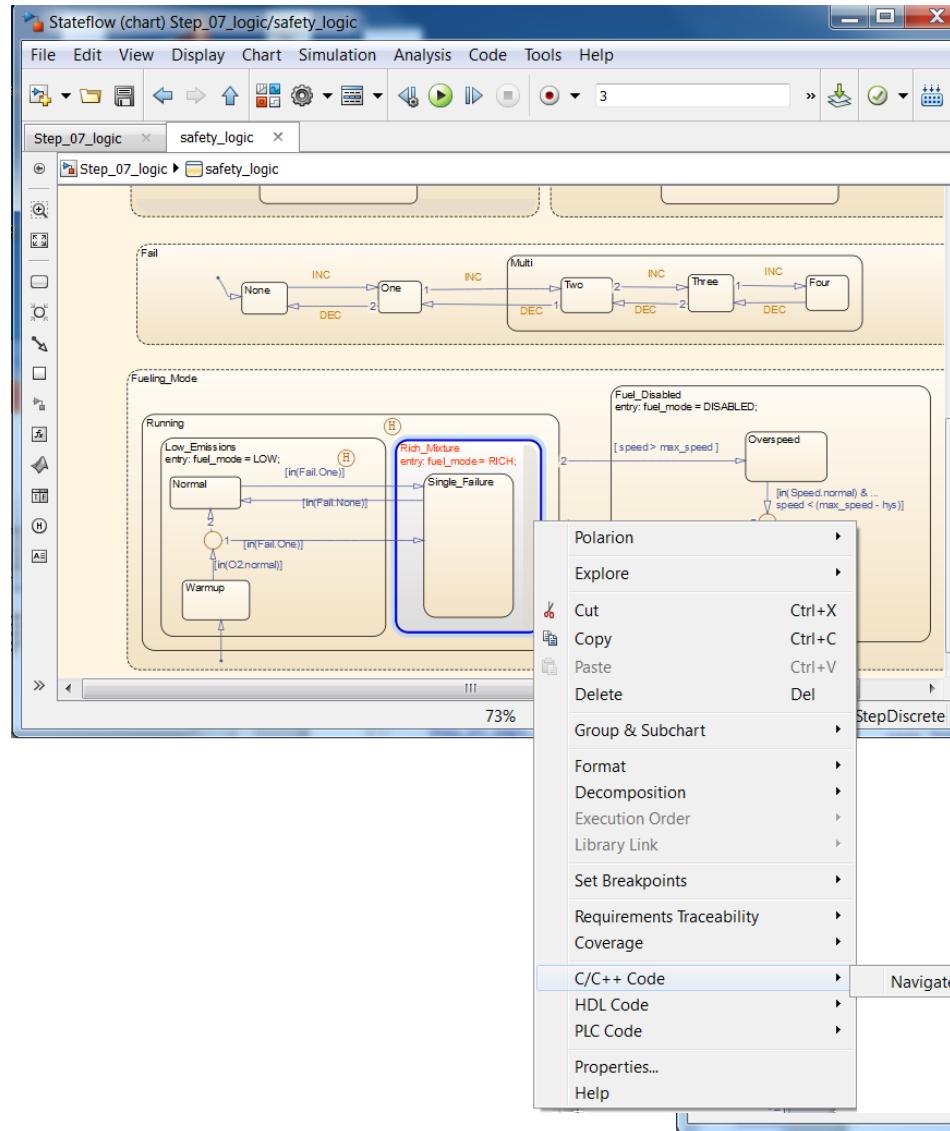
    default:
        localDW->is_Running = Step_07_logic_IN_NO_ACTIVE_CHILD;
        break;
    }
}
break;

case Step_07_logic_IN_Shutdown:
    /* During 'Shutdown': '<S1>:29' */
    if (localDW->sfEvent == Step_07_logic_exit_from_Multi) {
        /* Transition: '<S1>:63' */
        localDW->is_Fuel_Disabled = Step_07_logic_IN_NO_ACTIVE_CHILD;
    }
}

```

The code generation report also lists the generated files: "Step_07_logic.c", "Step_07_logic.h", "Step_07_logic_private.h", and "Step_07_logic_types.h". The "Generated Code" section is expanded to show the source code.

Code Generation with Model-to-Code Traceability



```

95
Sw 96
97
98
99
100
=> 101
102
103
104
105
106
Sw 107
=>T 108
109
110
111
112
113
114
115
116
117
118
119
120
121
122
123
124
125
=> 126
127
128
129
130
131
132
133

```

case Step_07_logic_IN_Warmup:
`localDW->is_Low_Emissions = Step_07_logic_IN_Warmup;`
`localDW->was_Low_Emissions = Step_07_logic_IN_Warmup;`
`break;`

default:
`localDW->is_Low_Emissions = Step_07_logic_IN_NO_ACTIVE_CHILD;`
`break;`
`}`
`break;`

case Step_07_logic_IN_Rich_Mixture:
`if (localDW->is_Running != Step_07_logic_IN_Rich_Mixture) {`
`localDW->is_Running = Step_07_logic_IN_Rich_Mixture;`
`localDW->was_Running = Step_07_logic_IN_Rich_Mixture;`

/* Entry 'Rich_Mixture': '<S1>:26'
 * Requirements for Entry 'Rich_Mixture': '<S1>:26':
 * 1. Enriched mixture usage
 */
`rty_fuel_mode = RICH;`

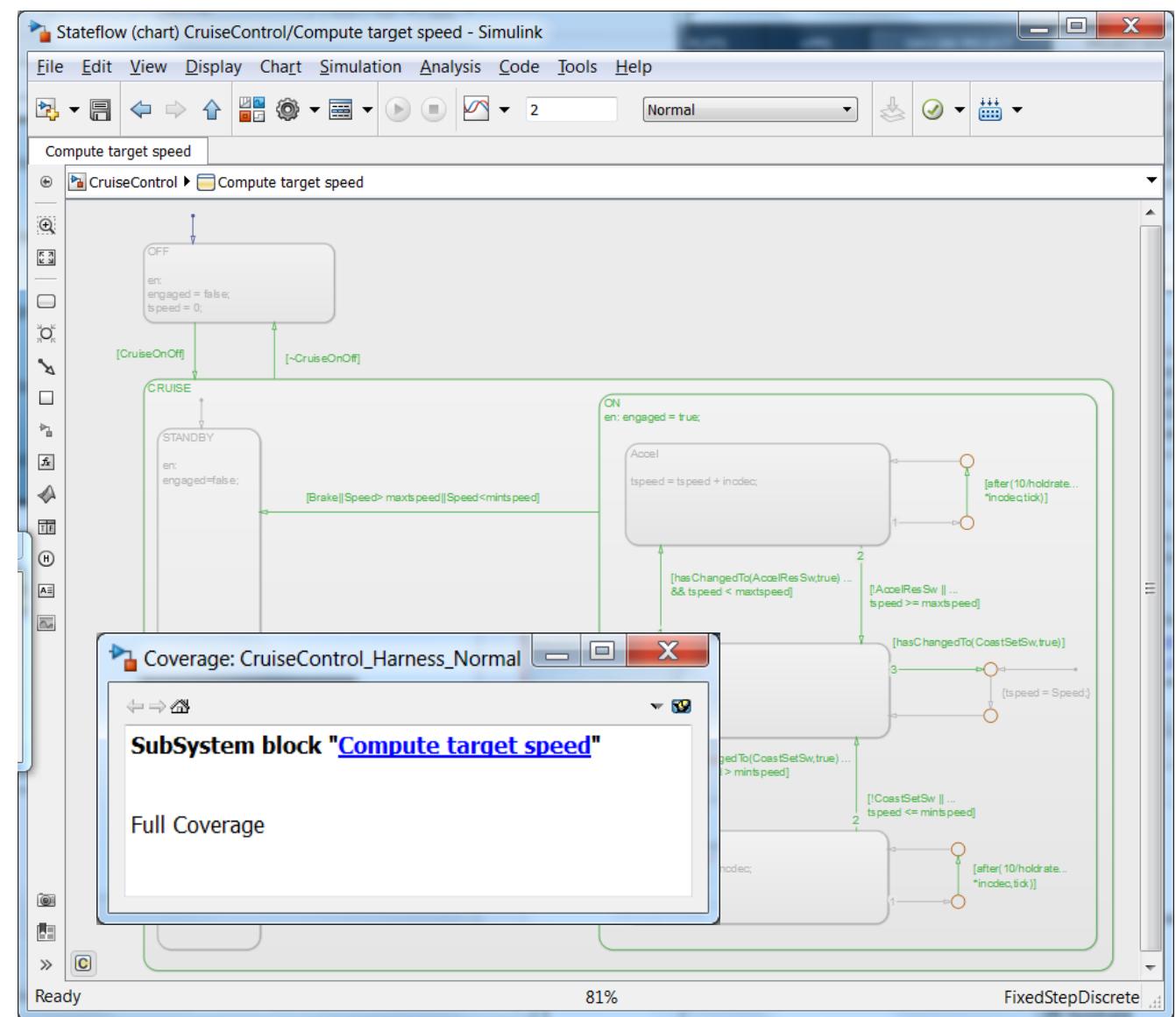
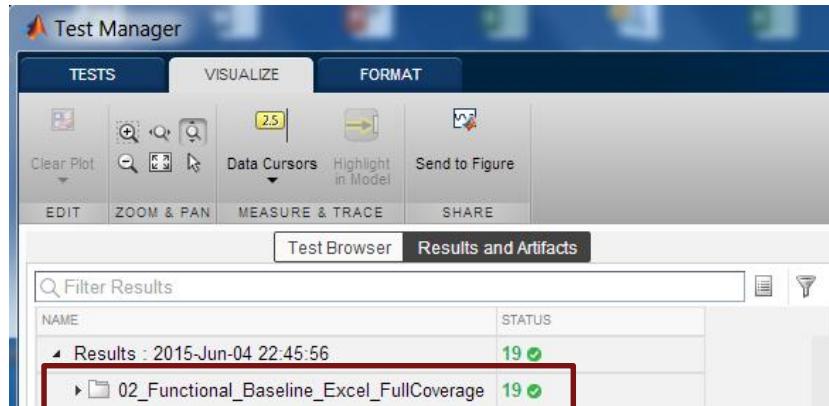
/* Entry Internal 'Rich_Mixture': '<S1>:26'
 * Requirements for Entry Internal 'Rich_Mixture': '<S1>:26':
 * 1. Enriched mixture usage
 */
`localDW->is_Rich_Mixture = Step_07_logic_IN_Single_Failure;`
`break;`

default:
`localDW->is_Running = Step_07_logic_IN_NO_ACTIVE_CHILD;`
`break;`
`}`
`}`
`break;`

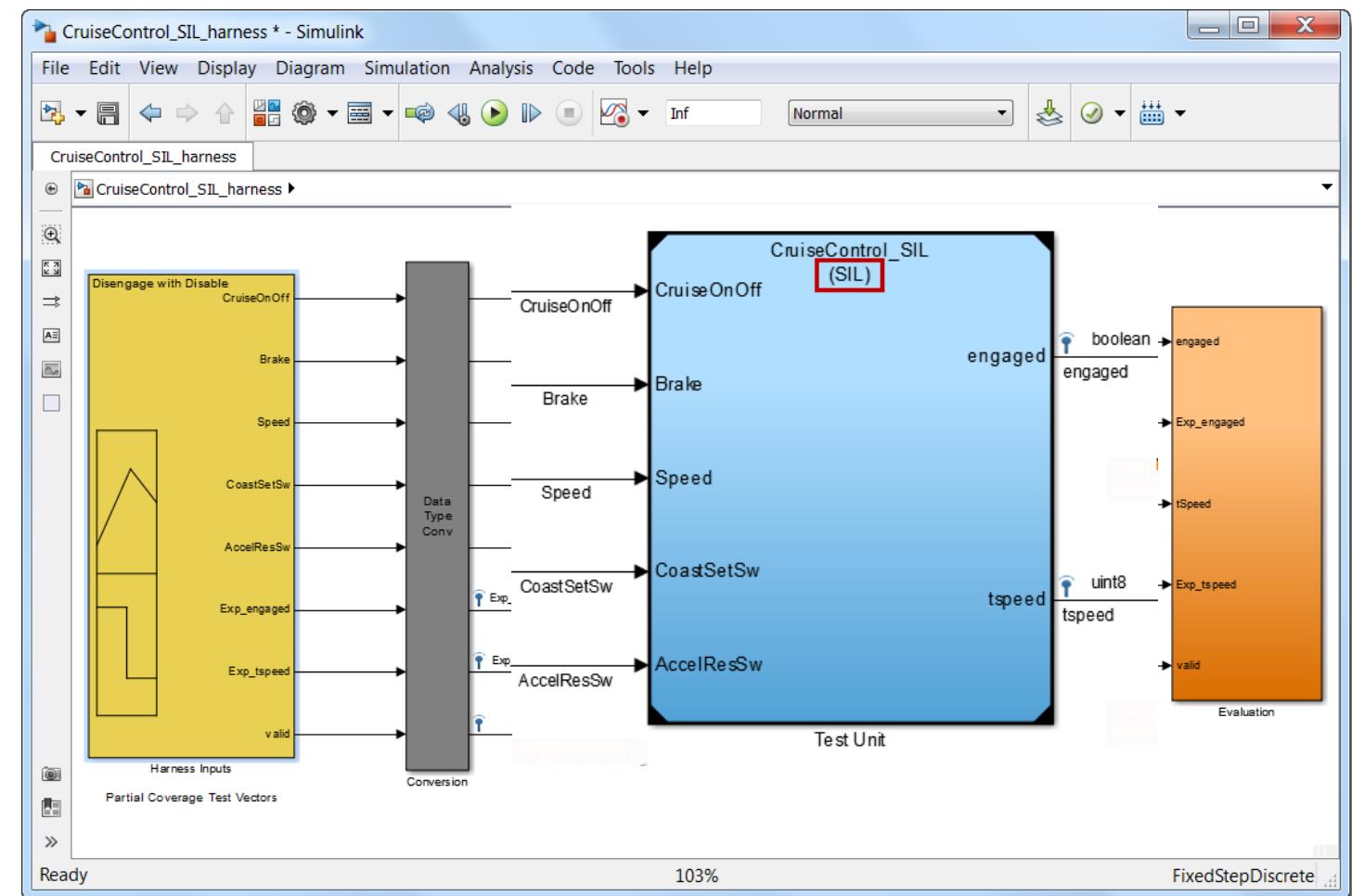
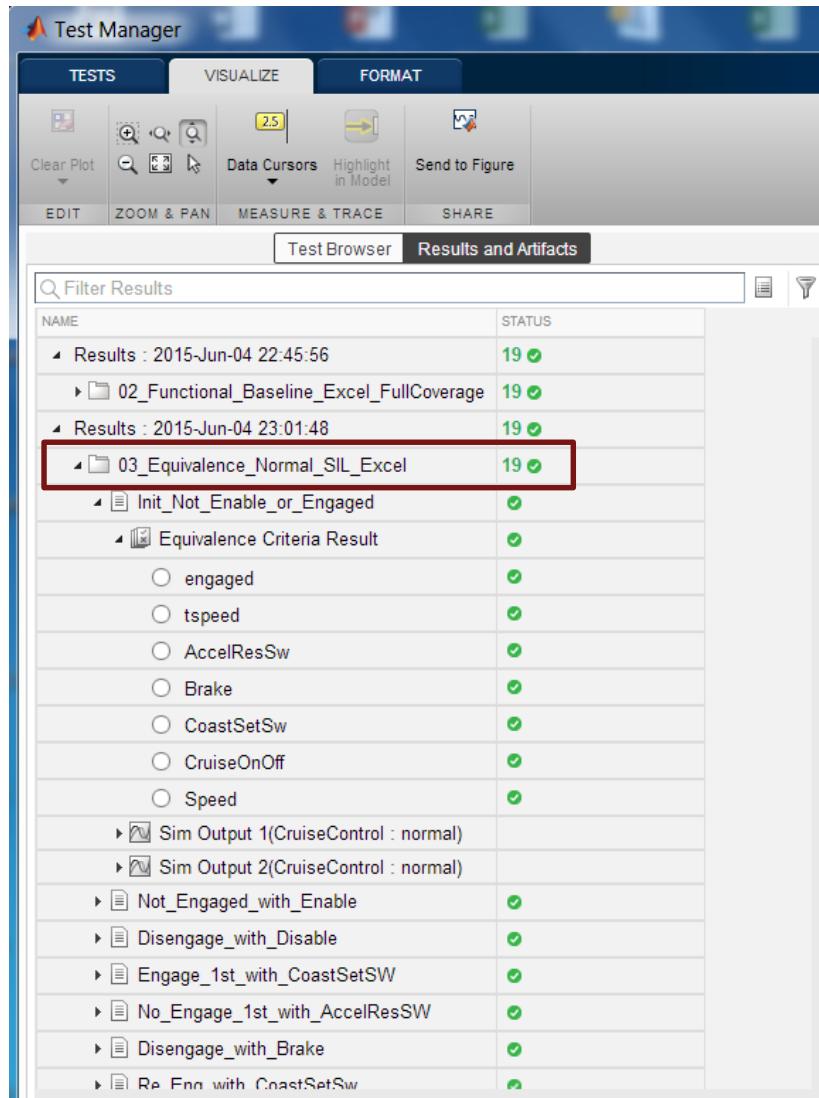
OK

Help

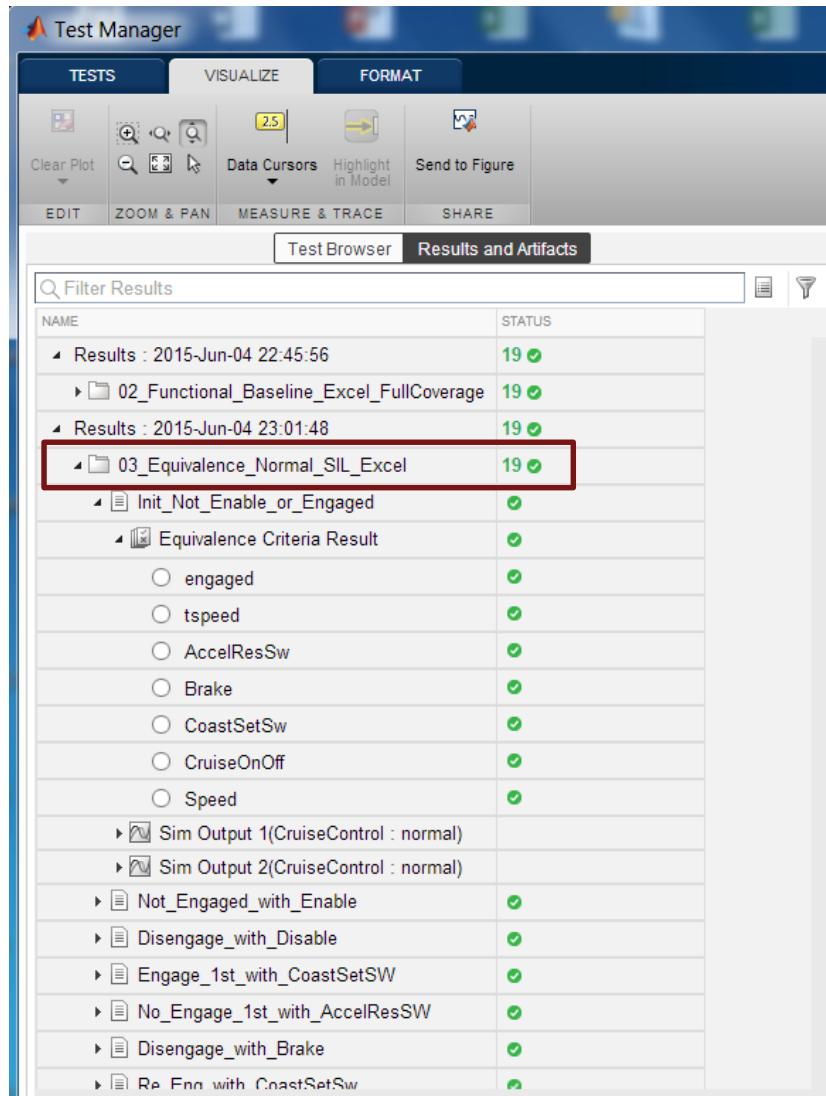
Code Equivalence Check Results: Model vs Code



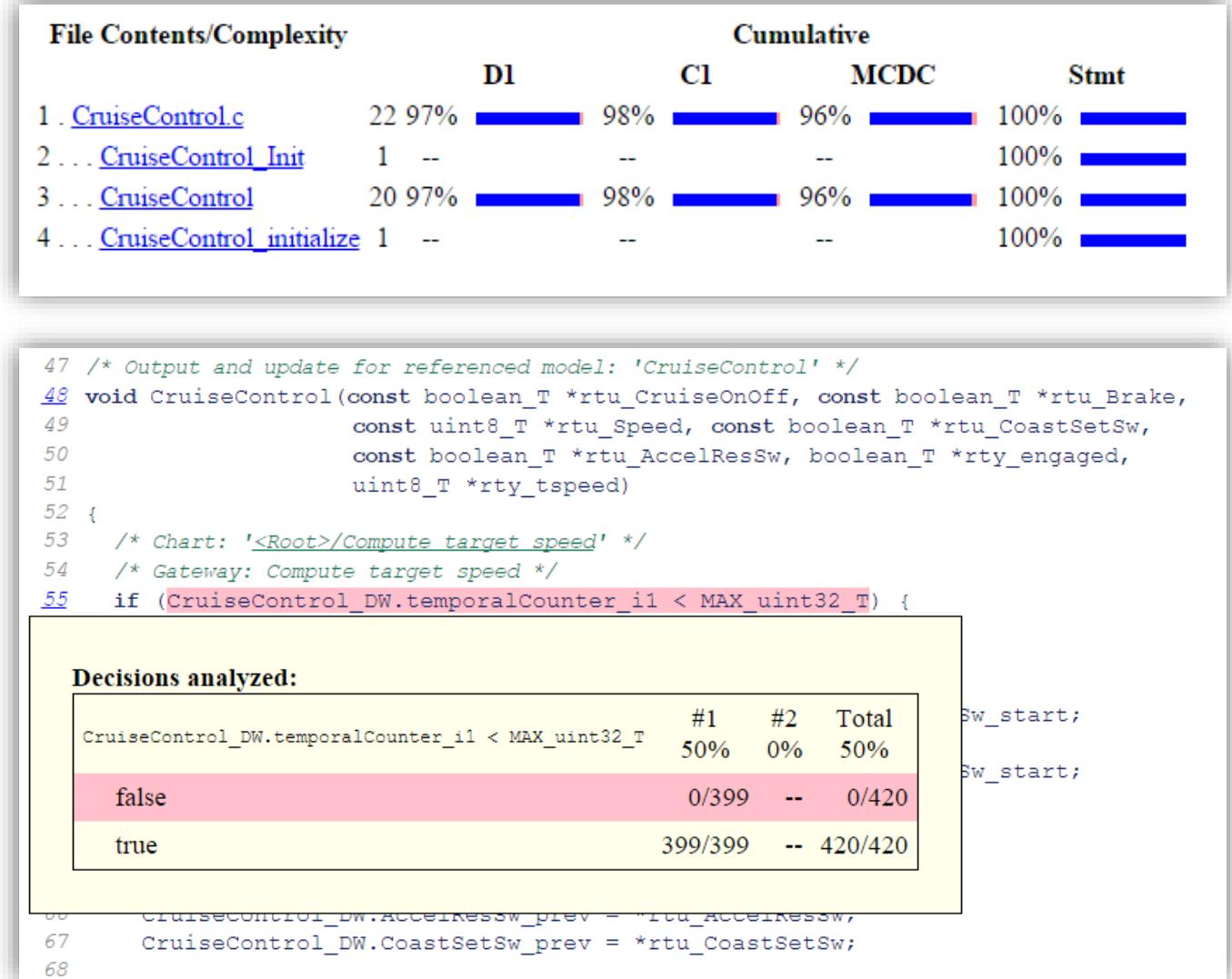
Code Equivalence Check Results: Model vs Code



Code Equivalence Check Results: Model vs Code

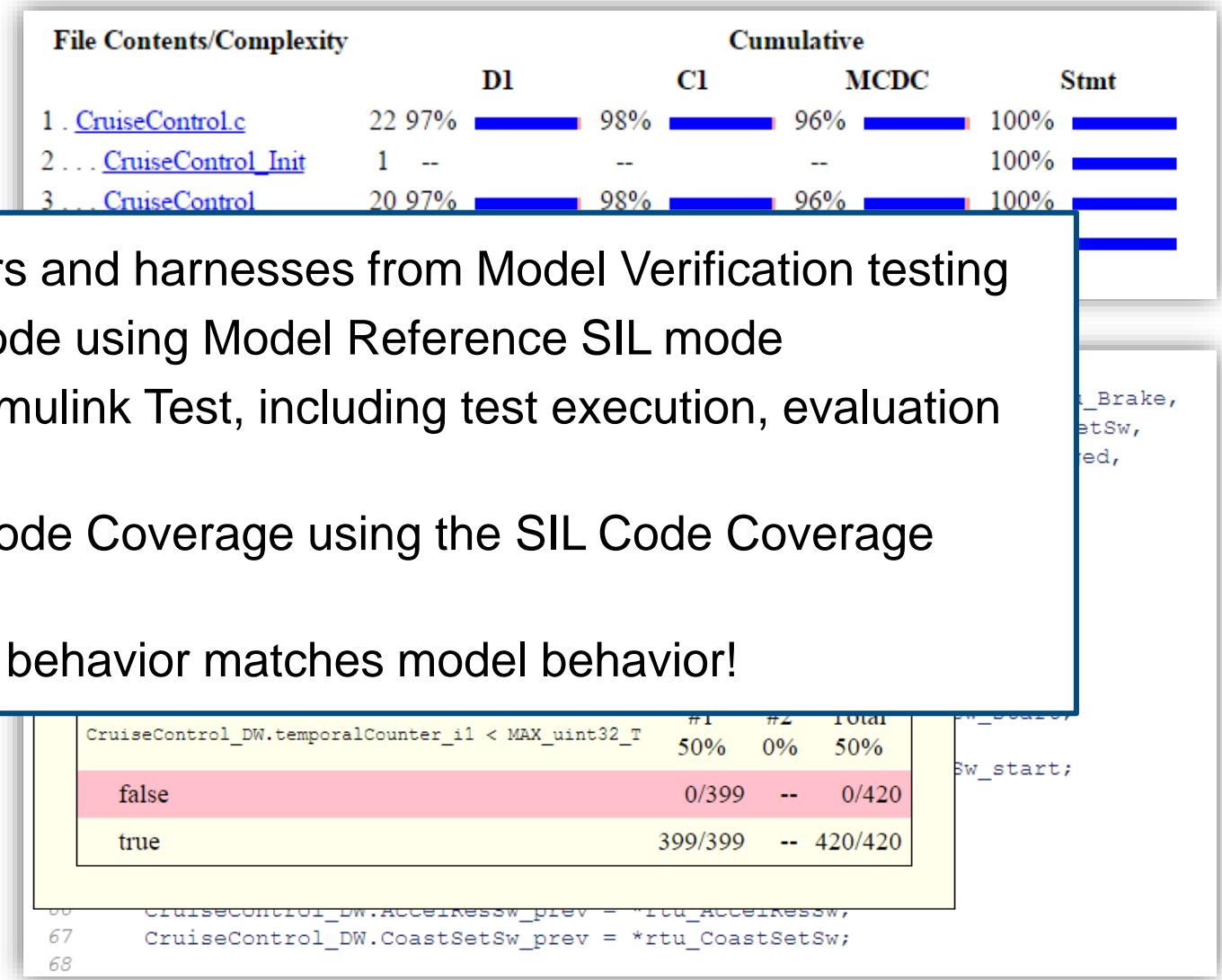
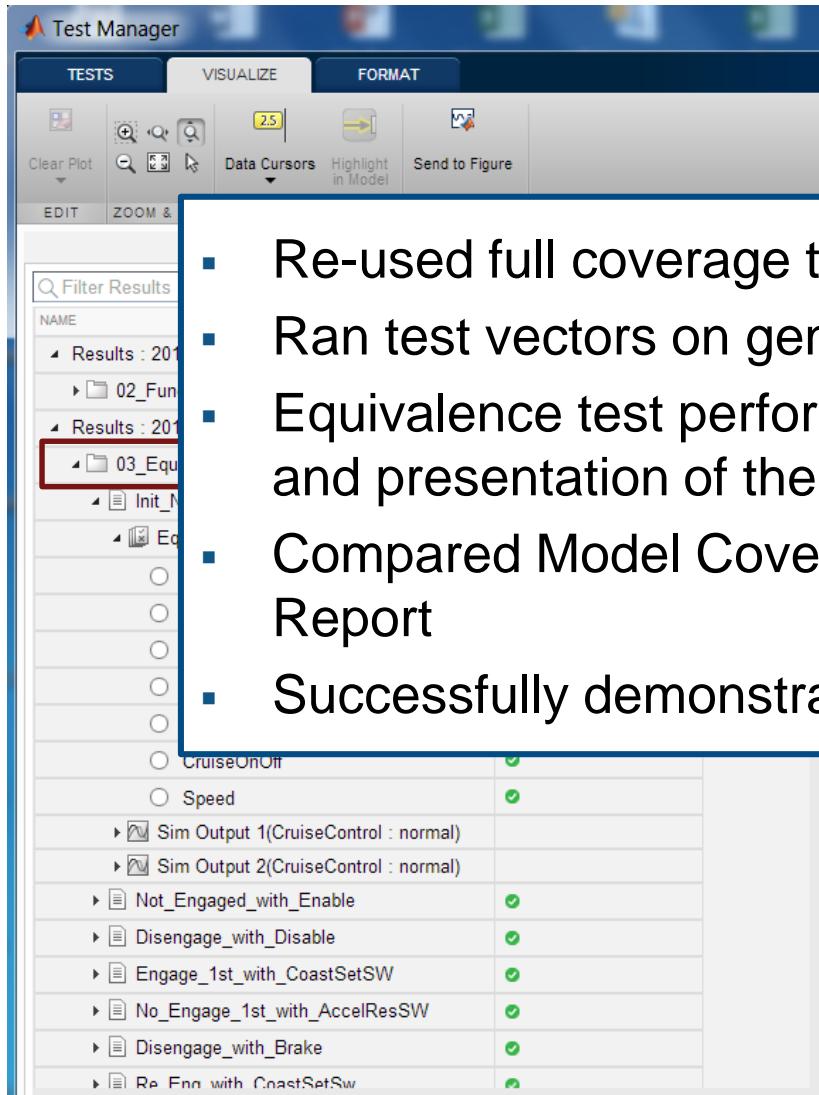


Code Coverage



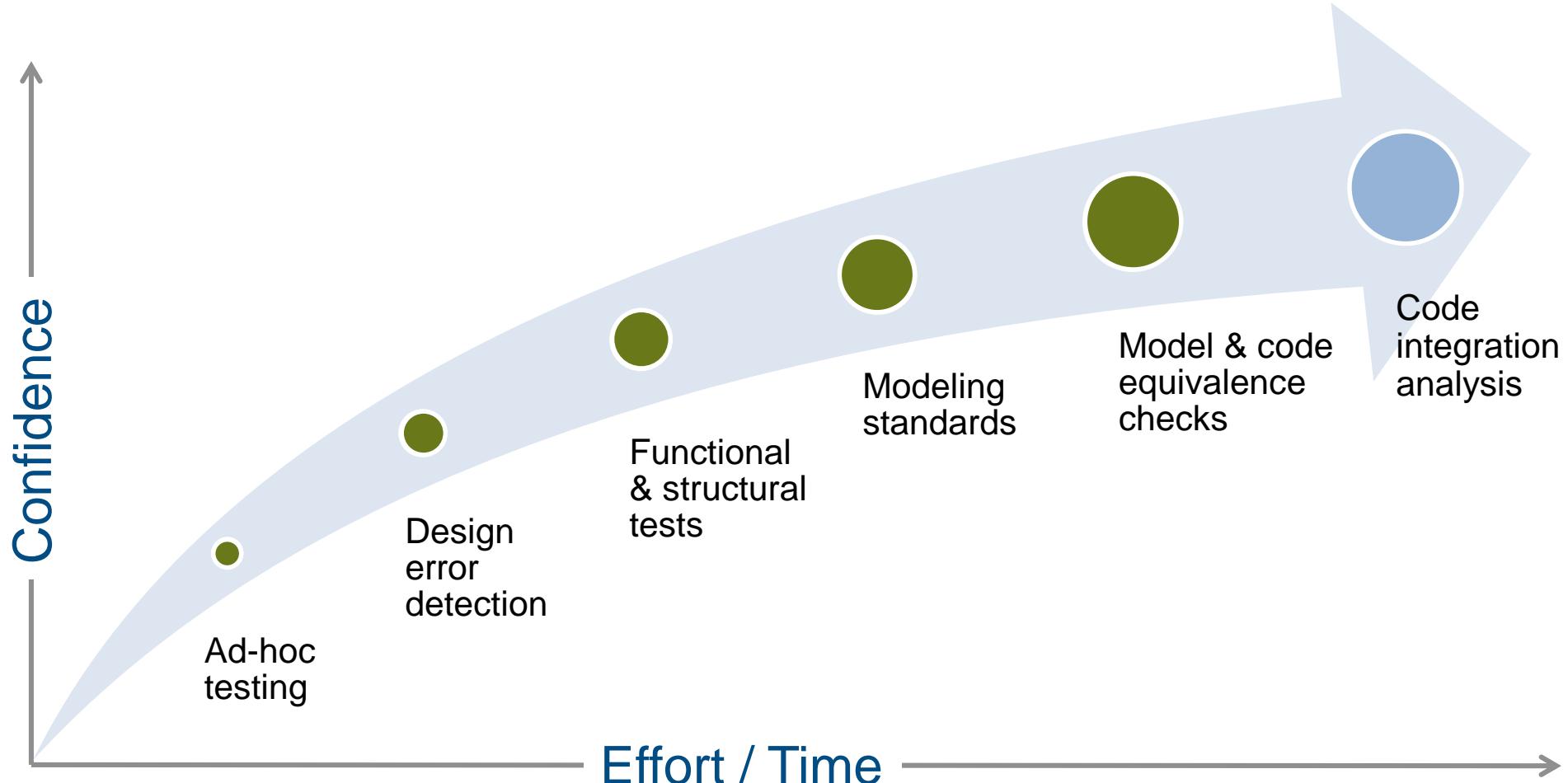
Code Equivalence Check Results: Model vs Code

Code Coverage

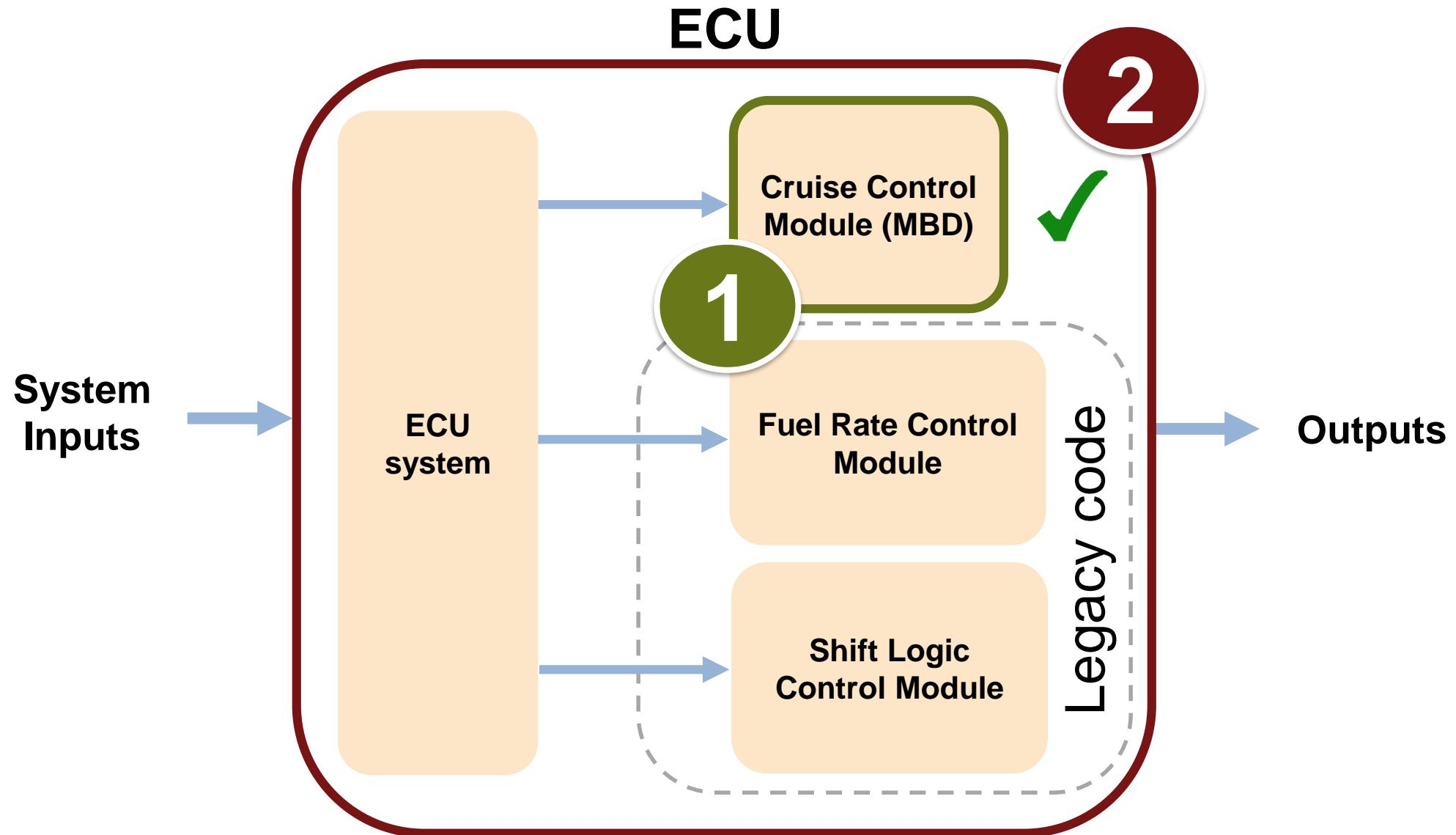


- Re-used full coverage test vectors and harnesses from Model Verification testing
- Ran test vectors on generated code using Model Reference SIL mode
- Equivalence test performed in Simulink Test, including test execution, evaluation and presentation of the results
- Compared Model Coverage to Code Coverage using the SIL Code Coverage Report
- Successfully demonstrated code behavior matches model behavior!

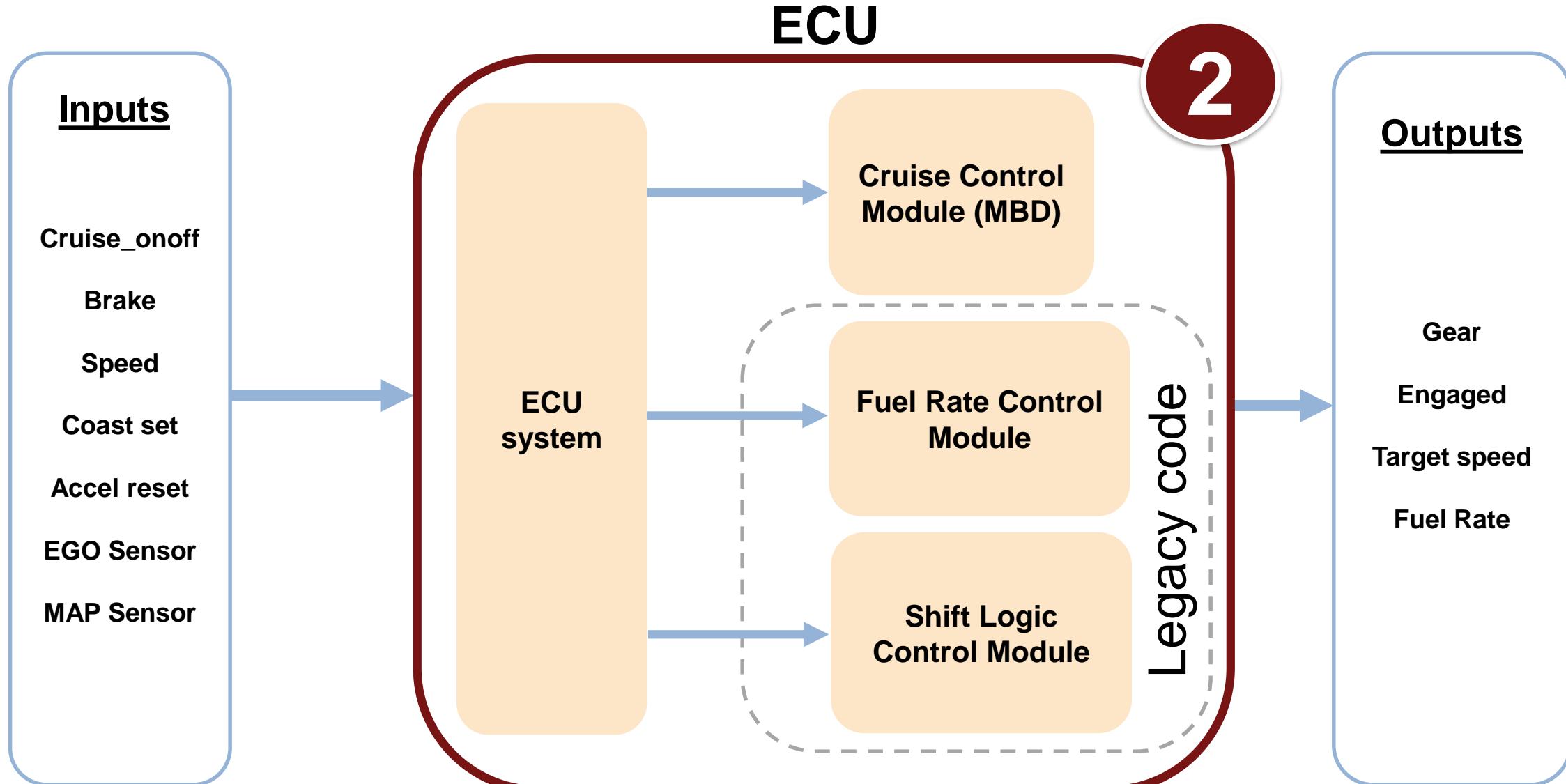
Gaining Confidence in our Design



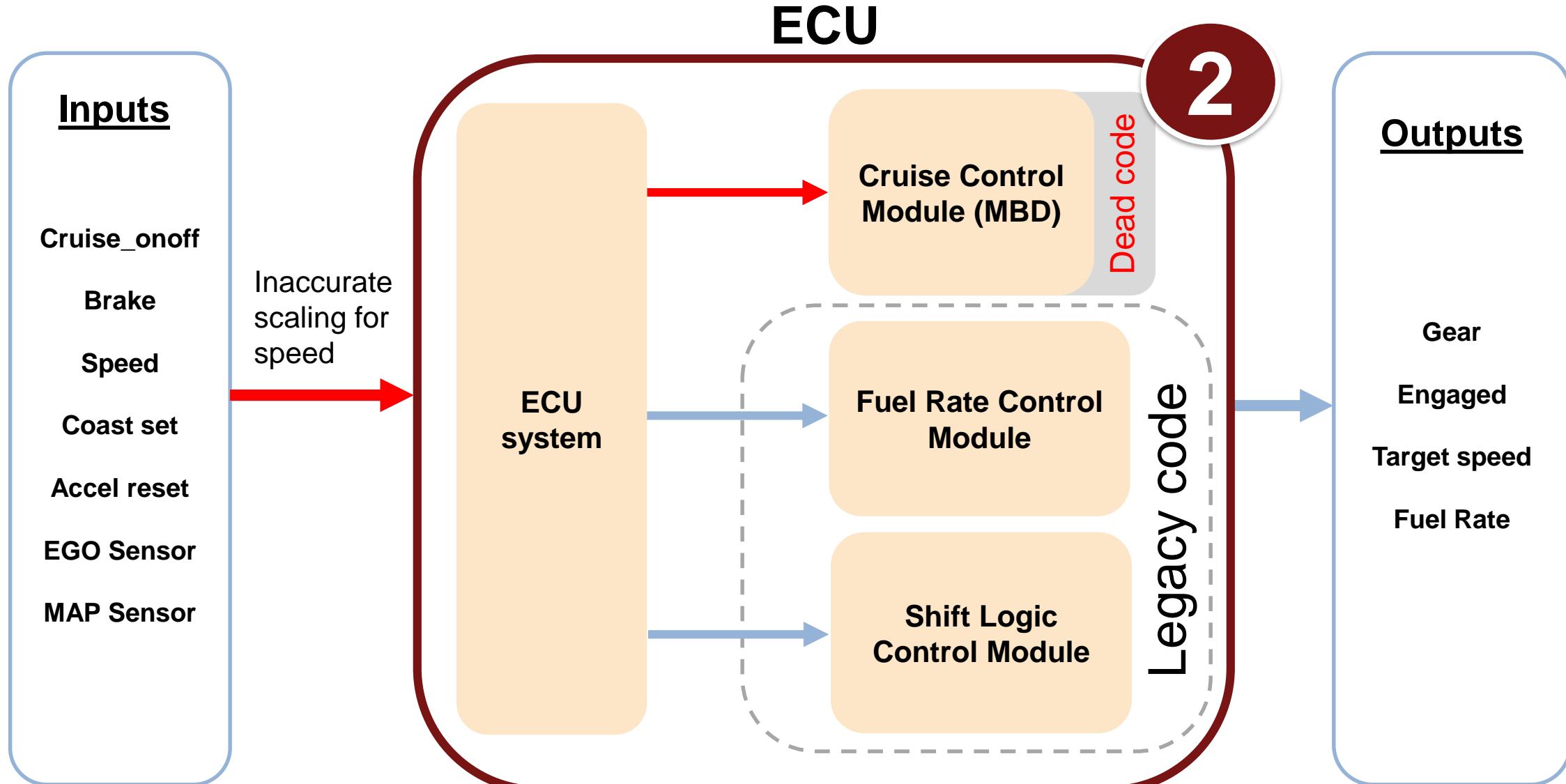
Code Integration Analysis



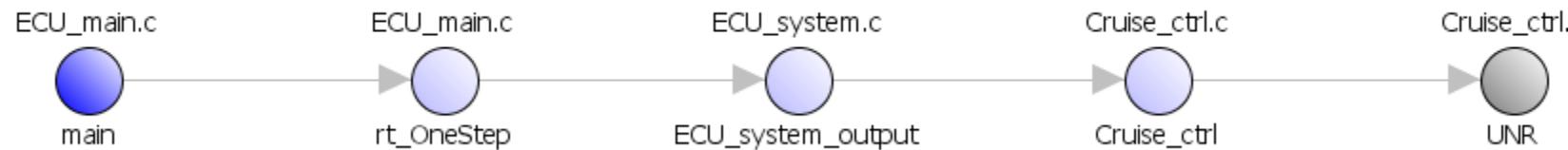
Code Integration Analysis



Finding Dead Code During Integration



Finding Dead Code with Polyspace



Target speed parameter
propagated to “Cruise_ctrl.c”
[0 ... 40]

Maximum target speed = 90

Dead code

```
/* Entry 'STANDBY': '<S5>:52' */
*rty_Engaged = false;
} else if (rtu_Speed > maxtspeed) {
/* Transition: '<S5>:55' */
/* Exit Internal 'ON': '<S5>:54' */
localDW->is_ON = IN_NO_ACTIVE_CHILD;
localDW->is_CRUISE = IN_STANDBY;

/* Entry 'STANDBY': '<S5>:52' */
*rty_Engaged = false;
} else if (rtu_Speed < mintspeed) {
/* Transition: '<S5>:113' */
```

Root Cause for Dead Code: Speed Sensor Input Hand Code

Changing analog-to-digital converter from 14 to 12-bit results in dead code

MASK – accounts for scaling down for new ADC from 14-bit to 12-bit

CONV_FACTOR – accounts for translating sensor input counts to mph

Overlooked changing CONV_FACTOR for new ADC

```
*C:\Working\...\d_ECU\inFunc.c x
13 #define NEW_ECU
14
15 /* Changing the mask (MASK) to account for new ECU per requirement CR102*/
16 #ifdef NEW_ECU
17 MASK = 0xFFFF; //New ECU
18 #else
19 MASK = 0x3FFF; //Original design specification
20 #endif
21
22 /* Scaling factor CONV_FACTOR for translating sensory input to miles/hr */
23 const real32_T CONV_FACTOR = 0.01; // FAILS
24
25 void readHW(void)
26 {
27 gvar_U_ECU_system_CruiseOnOff = CruiseOnOff;
28
29 gvar_U_ECU_system_Brake = Brake;
30
31 uint16_T temp=speed_inp;
32
33 gvar_U_ECU_system_Speed = CONV_FACTOR * (temp & MASK) ;
```

Polyspace Code Analysis

Start with C/C++ source code

```
static void pointer_arithmetics (void) {
    int array[100];
    int *p = array;
    int i;

    for (i = 0; i < 100; i++) {
        *p = 0;
        p++;
    }

    if (get_bus_status() > 0) {
        if (get_oil_pressure() > 0) {
            *p = 5;
        } else {
            i++;
        }
    }

    i = get_bus_status();

    if (i >= 0) {
        *(p - i) = 10;
    }
}
```

Polyspace Code Analysis

Source code painted in green, red, gray, orange

Green: reliable
safe pointer access

Red: faulty
out of bounds error

Gray: dead
unreachable code

Orange: unproven
may be unsafe for some
conditions

Purple: violation
MISRA-C/C++ or JSF++
code rules

Range data
tool tip

```
static void pointer_arithmetic (void) {
    int array[100];
    int *p = array;
    int i;

    for (i = 0; i < 100; i++) {
        *p = 0;
        p++;
    }

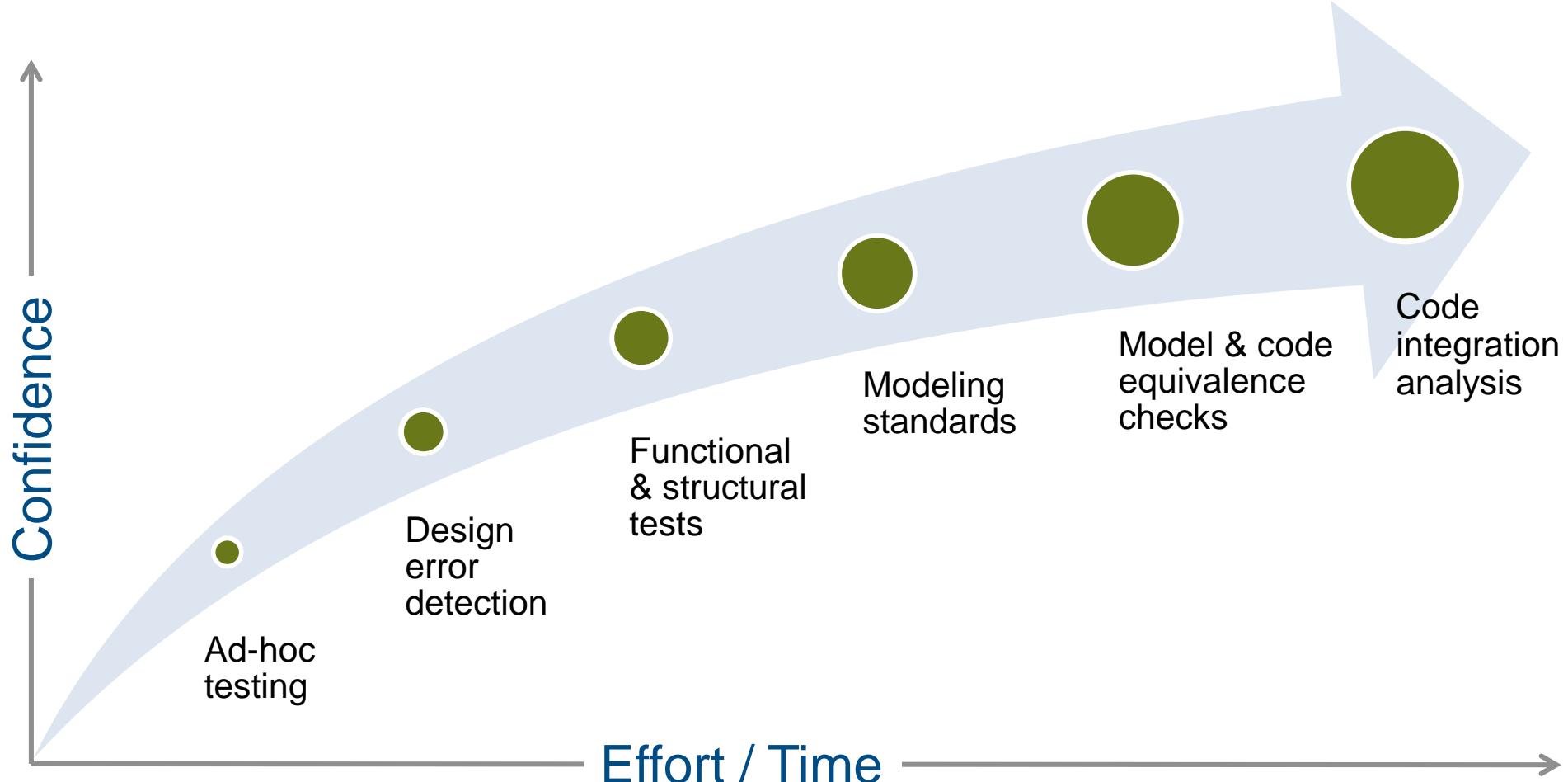
    if (get_bus_status() > 0) {
        if (get_oil_pressure() > 0) {
            *p = 5;
        } else {
            i++;
        }
    }

    i = get_bus_status();

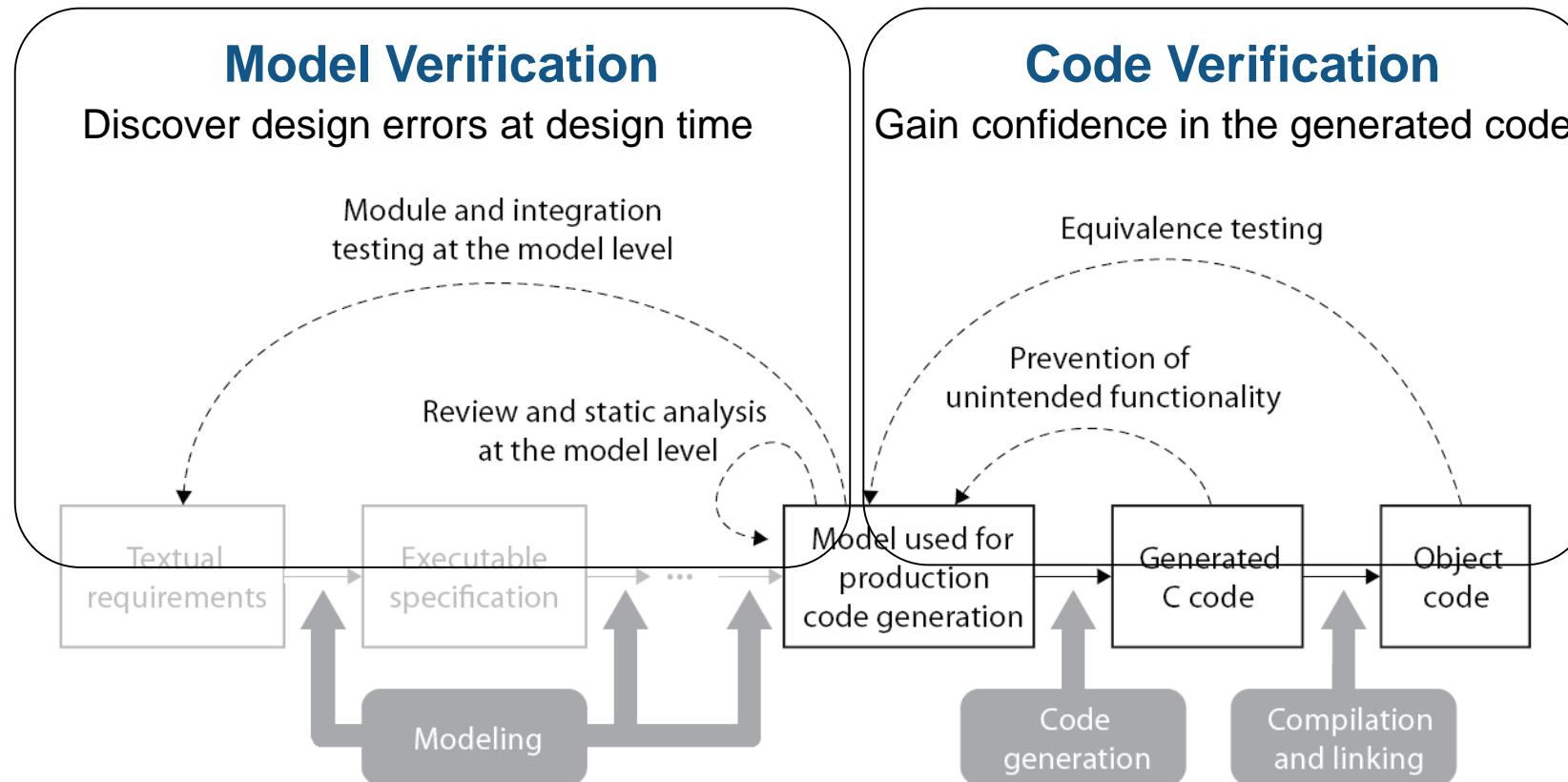
    if (i >= 0) {
        *(p - i) = 10;
    }
}
```

variable 'i' (int32): [0 .. 99]
assignment of 'i' (int32): [1 .. 100]

Gaining Confidence in our Design



Conclusion: Model-Based Design Verification Workflow



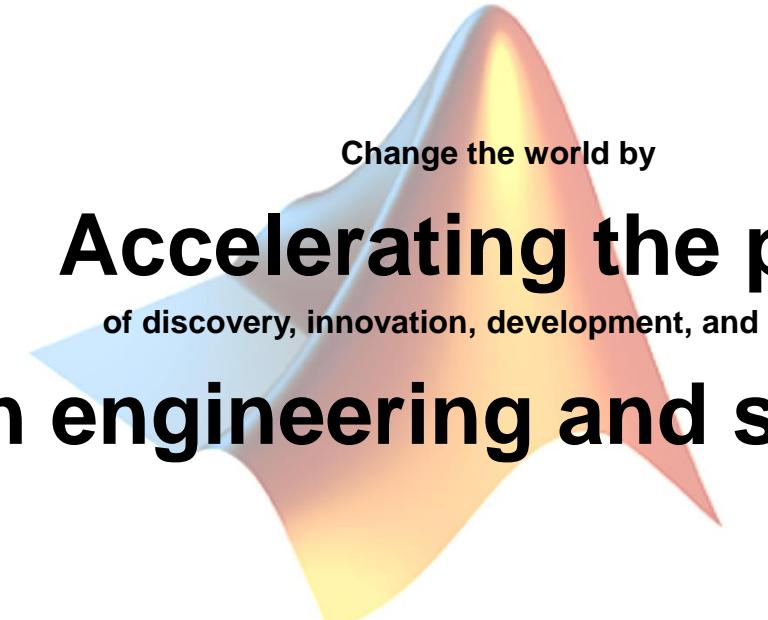
Workflow approved by TÜV SÜD for development of safety-critical software in accordance with ISO 26262 (automotive), IEC 61508 (industrial), EN 50128 (railway), IEC 62304 (medical devices)

Conclusion

It is easier and less expensive to fix design errors early in the process when they happen.

Model-Based Design enables:

1. *Early testing to increase confidence in your design*
2. *Delivery of higher quality software throughout the workflow*



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